

## **RECEIVER PINOUT CODE A37**

**Connector: Male MDM-37** 



Pin	Function		
1	High Speed DAC B		
2	High Speed DAC A		
3	High Speed DAC C		
4	DAC Ground		
5	AM Out		
6	Low Speed DAC A		
7	DC Power In		
8	DC Power Return		
9	DC Power Return		
10	TTL Clock		
11	TTL Data		
12	Serial Control Ground		
13	Lock Detect		
14	Serial Control Reply From Receiver		
15	TTL Clock B		
16	TTL Data B		
17	TTL Clock C		
18	TTL Data C		
19	N/C		
20	DAC Ground		
21	DAC Ground		
22	High Speed DAC D		
23	Low Speed DAC B		
24	DAC Ground		
25	RSSI		

No part of the document may be circulated, quoted, or reproduced for distribution without prior written approval from Quasonix, Inc.



Pin	Function		
26	DC Power In		
27	DC Power Return		
28	Power On Reset		
29	TTL Clock Return		
30	TTL Data Return		
31	Ones Detect		
32	Serial Control Input to Receiver		
33	TTL Clock B Return		
34	TTL Data B Return		
35	TTL Clock C Return		
36	TTL Data C Return		
37	N/C		

DRAWING	RX_PCA37_REVdocx		
REVISION	-		
DRAWN	MRE	DATE	08-23-2018
VERIFIED	MS	DATE	08-23-2018
APPROVED	MS	DATE	08-23-2018