# Quasonix

# TRANSMITTER PINOUT CODE 15

## Connector: Male MDM-21
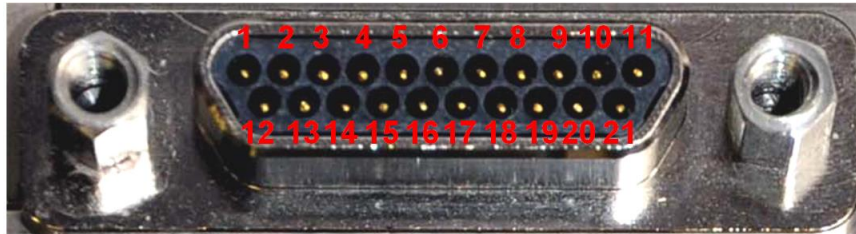


| Pin | Function |
|---|---|
| 1 | Terminal Control TxD {XMT} RS-422(+) [normal] status output |
| 2 | Terminal Control TxD {XMT} RS-422(-) [inverted] / RS-232 status output |
| 3 | Terminal Control TxD {XMT} status shield |
| 4 | Terminal Control RxD {RCV} RS-422(+) [normal] programming input |
| 5 | Terminal Control RxD {RCV} RS-422(-) [inverted] / RS-232 programming input |
| 6 | Terminal Control RxD {RCV} programming shield |
| 7 | Terminal Control RS-232/422 status/programming select |
| 8 | Terminal Control RS-232/422 status/programming select common |
| 9 | Terminal Control TxD {XMT} RS-422 120 ohm termination for status output |
| 10 | Terminal Control RxD {RCV} RS-422 120 ohm termination for programming input |
| 11 | Terminal Control 10/100/1000 Base-T Ethernet TX_D1(+), (Transceive Data+ for twisted pair #1) |
| 12 | Terminal Control 10/100/1000 Base-T Ethernet TX_D1(–), (Transceive Data– for twisted pair #1) |

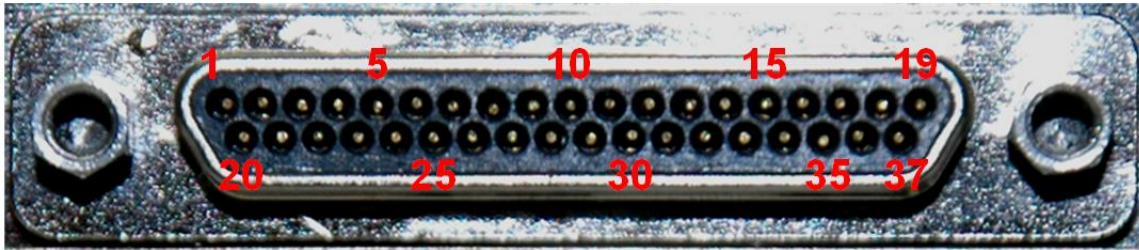| Pin | Function |
|-----|----------|
| 13 | Terminal Control 10/100/1000 Base-T Ethernet RX_D2(+), (Receive Data+ for twisted pair #2) |
| 14 | Terminal Control 10/100/1000 Base-T Ethernet BI_D3(+), (Bi-directional Data+ for twisted pair #3) |
| 15 | Terminal Control 10/100/1000 Base-T Ethernet BI_D3(–), (Bi-directional Data– for twisted pair #3) |
| 16 | Terminal Control 10/100/1000 Base-T Ethernet RX_D2(–), (Receive Data– for twisted pair #2) |
| 17 | Terminal Control 10/100/1000 Base-T Ethernet BI_D4(+), (Bi-directional Data+ for twisted pair #4) |
| 18 | Terminal Control 10/100/1000 Base-T Ethernet BI_D4(–), (Bi-directional Data– for twisted pair #4) |
| 19 | Terminal Control 10/100/1000 Base-T Ethernet shield |
| 20 | Admiral Pin |
| 21 | Reserved for future use |

# Quasonix

## Connector: Male MDM-15
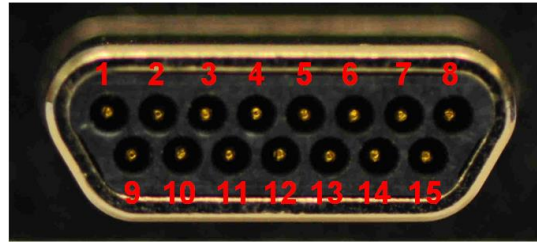


| Pin | Function |
|---|---|
| 1 | 0 Degree Bit Clock RS-422 (+) [normal] / TTL |
| 2 | 0 Degree Bit Clock RS-422 (-) [inverted] |
| 3 | 0 Degree Bit Clock RS-422 / TTL Shield |
| 4 | NRZL RS-422 (+) [normal] / TTL |
| 5 | NRZL RS-422 (-) [inverted] |
| 6 | NRZL RS-422 / TTL Shield |
| 7 | 0 Degree Bit Clock RS-422 / TTL Signal Ground |
| 8 | NRZL RS-422 / TTL Signal Ground |
| 9 | Filtered PCM (FPCM) Hi |
| 10 | Filtered PCM (FPCM) Lo |
| 11 | Filtered PCM (FPCM) Signal Ground |
| 12 | Reserved for Future Use |
| 13 | Reserved for Future Use |
| 14 | Reserved for Future Use |
| 15 | Reserved for Future Use |

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## Connector: Male MDM-37



| Pin | Function |
|-----|----------|
| 1 | Terminal/Parallel Control Select |
| 2 | Terminal/Parallel Control Select Common |
| 3 | 4000 MHz Carrier Frequency Select |
| 4 | 2000 MHz Carrier Frequency Select |
| 5 | 1000 MHz Carrier Frequency Select |
| 6 | 800 MHz Carrier Frequency Select |
| 7 | 400 MHz Carrier Frequency Select |
| 8 | 200 MHz Carrier Frequency Select |
| 9 | 100 MHz Carrier Frequency Select |
| 10 | 80 MHz Carrier Frequency Select |
| 11 | 40 MHz Carrier Frequency Select |
| 12 | 20 MHz Carrier Frequency Select |
| 13 | 10 MHz Carrier Frequency Select |
| 14 | 8 MHz Carrier Frequency Select |
| 15 | 4 MHz Carrier Frequency Select |
| 16 | 2 MHz Carrier Frequency Select |
| 17 | 1 MHz Carrier Frequency Select |
| 18 | 0.5 MHz Carrier Frequency Select |
| 19 | 4000 MHz-0.5 MHz Carrier Frequency Select Common |

| Pin | Function |
|-----|----------|
| 20 | IRIG-106-09 Modulation Type Select Bit 1 (MSB) |
| 21 | IRIG-106-09 Modulation Type Select Bit 0 (LSB) |
| 22 | IRIG-106-09 Modulation Type Select Bit Common |
| 23 | Differential Encoding Select |
| 24 | Data Randomization Select |
| 25 | RF Output On/Off Select |
| 26 | Forward Error Correction Select |
| 27 | RF Output High/Low Select |
| 28 | RS-422 and TTL / FPCM Select |
| 29 | Binary Select Common |
| 30 | Preset Bit 3 (MSB) |
| 31 | Preset Bit 2 |
| 32 | Preset Bit 1 |
| 33 | Preset Bit 0 (LSB) |
| 34 | Preset Common |
| 35 | Reserved |
| 36 | Reserved |
| 37 | Reserved |

# Quasonix

## Connector: Amphenol 4-pin Power Connector
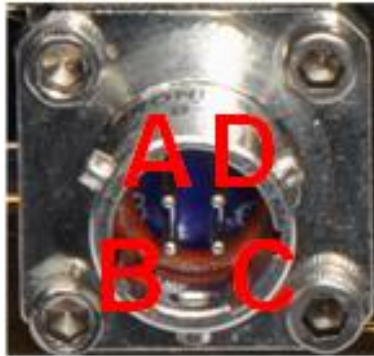


| Pin | Function |
|-----|----------|
| A | +28 VDC |
| B | Reserved |
| C | 28 Return |
| D | 28 Shield |

| DRAWING | qsx_pc15.docx | | |
|---------|------|------|------|
| REVISION | B | | |
| DRAWN | MRE | DATE | 08-17-2018 |
| VERIFIED | DEF | DATE | 08-17-2018 |
| APPROVED | DEF | DATE | 08-17-2018 |