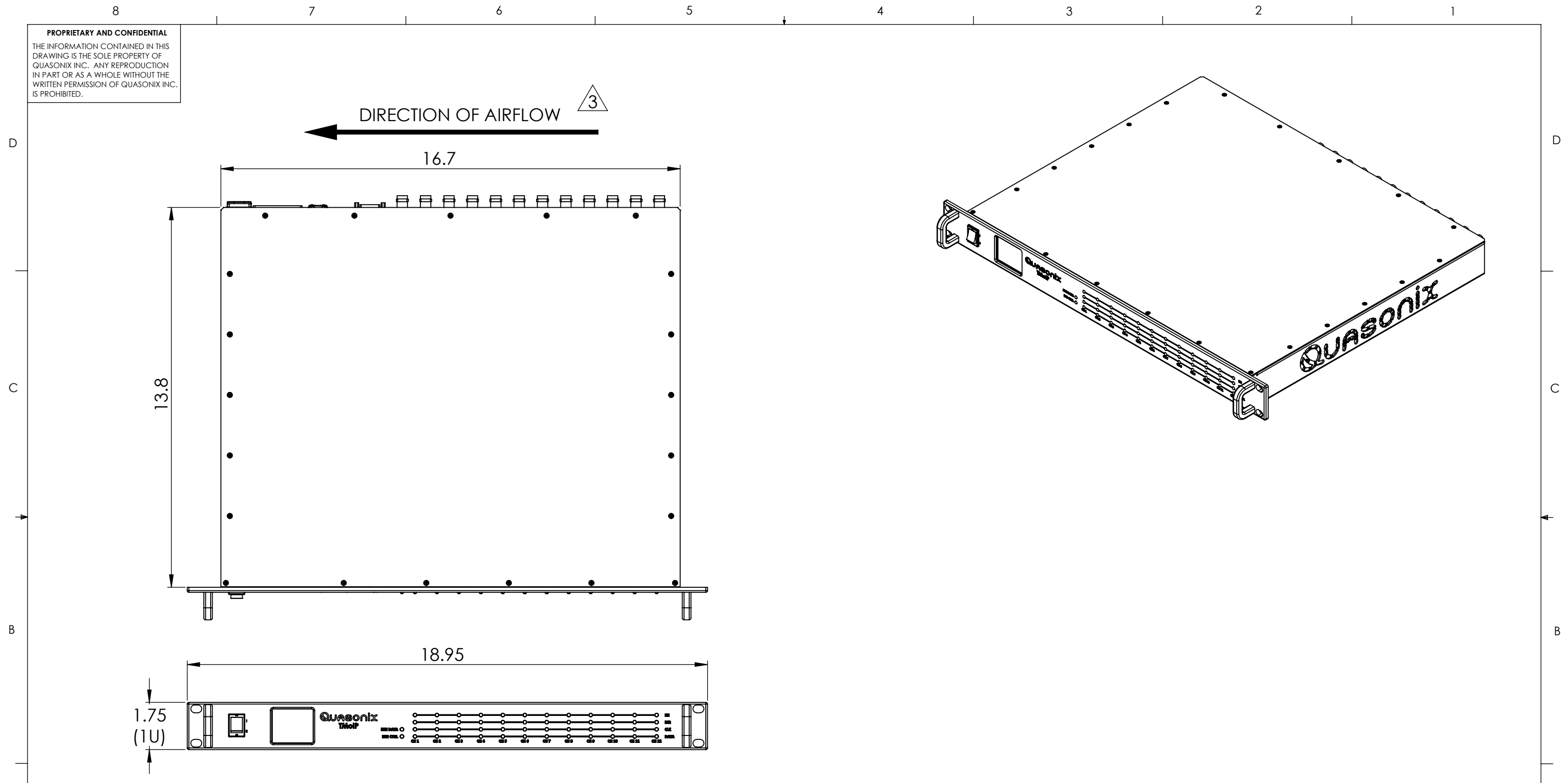


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- NOTES:**
- ① SUPPORTS UP TO 12 CHANNELS
  - ② INPUT VOLTAGE RANGE: 90-264 V-RMS, 47-63 HZ
  - ③ UNIT IS CROSS VENTILATED FROM THE RIGHT SIDE OF UNIT. THIS IS THE SAME DIRECTION OF FLOW AS THE RDMS™ RACKMOUNT RECEIVERS.

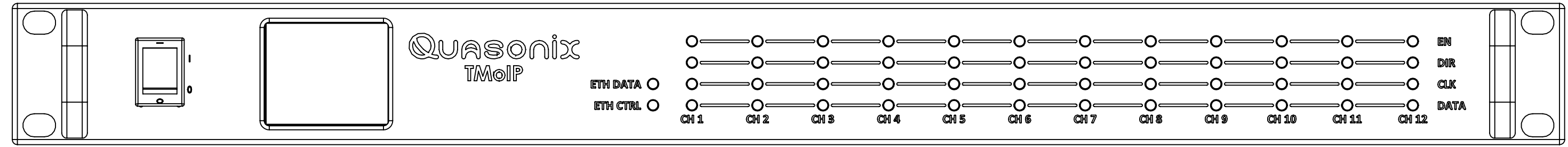
UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES ARE: ANGULAR: MACH/BEND ± .5° ONE PLACE DECIMAL ±0.1 TWO PLACE DECIMAL ±0.03 THREE PLACE DECIMAL ±0.010 FRACTIONS ±1/32 DO NOT SCALE DRAWING		NAME	DATE										
DRAWN	MBS	08/24/2022	TITLE:										
ENG APPR.	MBS	08/24/2022	TMOIP PROCESSOR, 1U										
MATERIAL:		<table border="1" style="width: 100%;"> <tr> <td>SIZE</td> <td>DWG. NO.</td> <td>REV</td> </tr> <tr> <td style="font-size: 2em;"><b>B</b></td> <td>QS-TMOIP-12CH</td> <td></td> </tr> <tr> <td>SCALE: 1:5</td> <td>CAGE CODE: 3CJA9</td> <td>SHEET 1 OF 4</td> </tr> </table>			SIZE	DWG. NO.	REV	<b>B</b>	QS-TMOIP-12CH		SCALE: 1:5	CAGE CODE: 3CJA9	SHEET 1 OF 4
SIZE	DWG. NO.				REV								
<b>B</b>	QS-TMOIP-12CH												
SCALE: 1:5	CAGE CODE: 3CJA9				SHEET 1 OF 4								
FINISH:													
HOLE DIA.	TOLERANCE												
.0135 THRU .125	+ .004 - .001												
.126 THRU .250	+ .005 - .001												
.251 THRU .500	+ .006 - .001												
.501 THRU .750	+ .008 - .001												
.751 THRU 1.000	+ .010 - .001												
1.001 THRU 2.000	+ .012 - .001												

8 7 6 5 4 3 2 1

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D

D

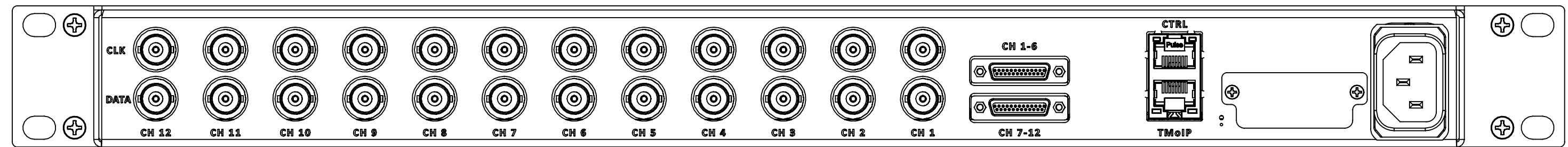


C

C

B

B



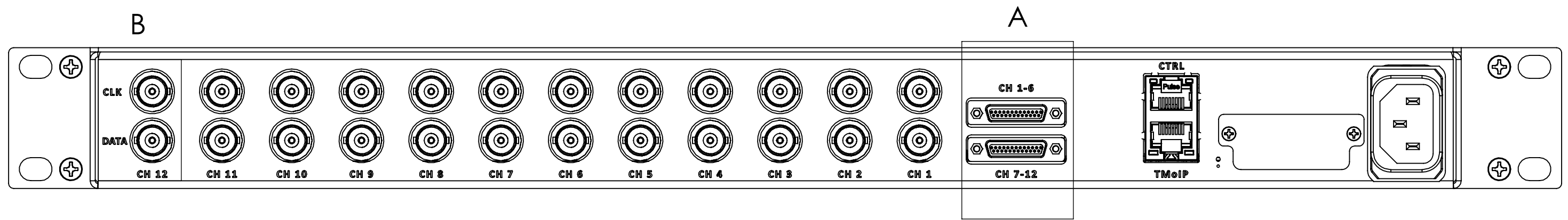
A

A

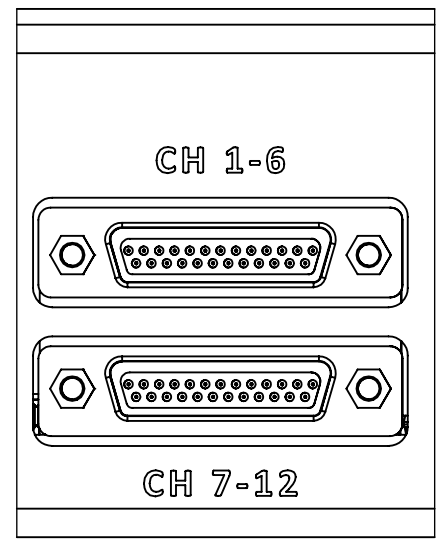
8 7 6 5 4 3 2 1

SIZE <b>B</b>	DWG. NO. QS-TMOIP-12CH	REV
SCALE: 1:5	CAGE CODE: 3CJA9	SHEET 2 OF 4

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**INTERFACE INTERCONNECTS**

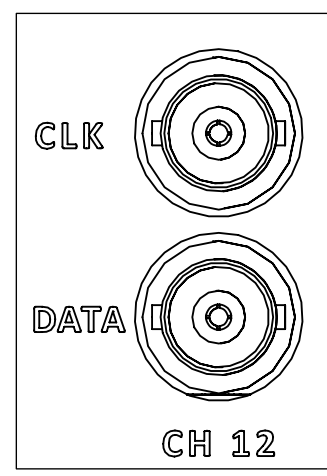


DETAIL A  
SCALE 2 : 1.25

RS422 CH1-6 PINOUT	
PIN	SIGNAL
1	CH1_CLK_422_P
2	CH1_DATA_422_P
3	CH2_CLK_422_P
4	CH2_DATA_422_P
5	CH3_CLK_422_P
6	CH3_DATA_422_P
7	CH4_CLK_422_P
8	CH4_DATA_422_P
9	CH5_CLK_422_P
10	CH5_DATA_422_P
11	CH6_CLK_422_P
12	CH6_DATA_422_P
13	GND
14	CH1_CLK_422_N
15	CH1_DATA_422_N
16	CH2_CLK_422_N
17	CH2_DATA_422_N
18	CH3_CLK_422_N
19	CH3_DATA_422_N
20	CH4_CLK_422_N
21	CH4_DATA_422_N
22	CH5_CLK_422_N
23	CH5_DATA_422_N
24	CH6_CLK_422_N
25	CH6_DATA_422_N

RS422 CH7-12 PINOUT	
PIN	SIGNAL
1	CH7_CLK_422_P
2	CH7_DATA_422_P
3	CH8_CLK_422_P
4	CH8_DATA_422_P
5	CH9_CLK_422_P
6	CH9_DATA_422_P
7	CH10_CLK_422_P
8	CH10_DATA_422_P
9	CH11_CLK_422_P
10	CH11_DATA_422_P
11	CH12_CLK_422_P
12	CH12_DATA_422_P
13	GND
14	CH7_CLK_422_N
15	CH7_DATA_422_N
16	CH8_CLK_422_N
17	CH8_DATA_422_N
18	CH9_CLK_422_N
19	CH9_DATA_422_N
20	CH10_CLK_422_N
21	CH10_DATA_422_N
22	CH11_CLK_422_N
23	CH11_DATA_422_N
24	CH12_CLK_422_N
25	CH12_DATA_422_N

**CHANNEL INTERCONNECTS**

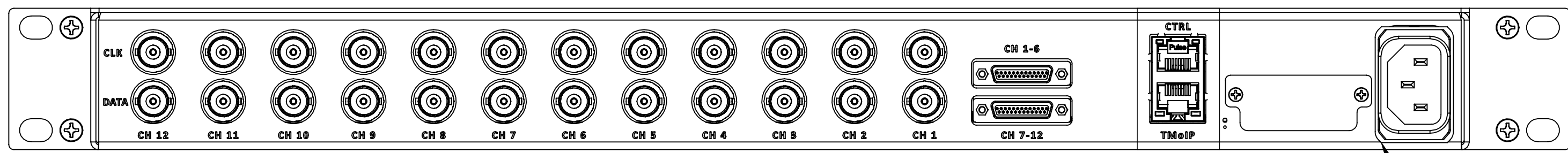


DETAIL B  
SCALE 2 : 1.25



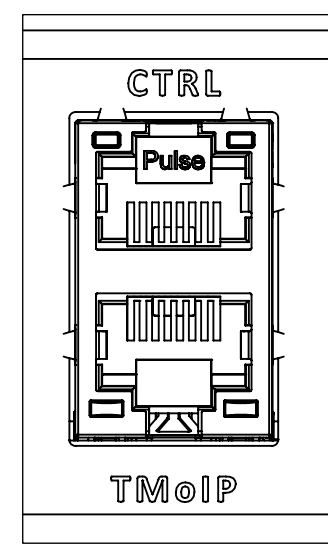
SIZE <b>B</b>	DWG. NO. QS-TMOIP-12CH	REV
SCALE: 1:1.25	CAGE CODE: 3CJA9	SHEET 3 OF 4

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2  
 POWER INLET  
 IEC 60320 C1 4

**INTERFACE INTERCONNECTS**



**DETAIL C**  
 SCALE 2 : 1.25

CTRL PINOUT	
PIN	SIGNAL
1	ETH_SOM_MD1_N
2	ETH_SOM_MD1_P
3	ETH_SOM_MD2_N
4	ETH_SOM_MD2_P
5	ETH_SOM_MD3_N
6	ETH_SOM_MD3_P
7	ETH_SOM_MD4_N
8	ETH_SOM_MD4_P

TMOIP PINOUT	
PIN	SIGNAL
1	ETH_CC_MD1_N
2	ETH_CC_MD1_P
3	ETH_CC_MD2_N
4	ETH_CC_MD2_P
5	ETH_CC_MD3_N
6	ETH_CC_MD3_P
7	ETH_CC_MD4_N
8	ETH_CC_MD4_P

SIZE <b>B</b>	DWG. NO. QS-TMOIP-12CH	REV
SCALE: 1:1.25	CAGE CODE: 3CJA9	SHEET 4 OF 4