#### ISO 9001:2015 Certified

# Quasonix

## Installation and Operation Manual

## 3<sup>rd</sup> Generation Receiver Analyzer



Quasonix, Inc. 6025 Schumacher Park Dr. West Chester, OH 45069 17 October, 2024

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## 1 Introduction

The Quasonix Receiver Analyzer (RA) supports a wide range of receiver and combiner performance measurements, including bit error rate, sync time, sync threshold, receiver latency, DQE/DQM verification, modulation index performance, break frequency, and combiner gain, and provides multipath emulation for static and dynamic multipath measuring. The compact 1U chassis contains two complete ARTM signal generators and eight bit error rate testers with calibrated RF levels from -125 dBm to -10 dBm.

The two ARTM signal generators cover P, lower L, upper L, S, and C bands (200 MHz to 2500 MHz and 4.4 GHz to 5.25 GHz contiguously), and can be configured to produce a broad array of telemetry transmit signals.

The intuitive RxAn Graphical User Interface (GUI) runs on a Windows 10 PC. The RxAn provides access to the RA hardware via a USB interface connected to an FPGA. RxAn is closely mapped to the structure of the FPGA and facilitates detailed control of all available functionality.

Automated scripting allows precise measurement using many parameters with rapid acquisition of extremely large datasets. Saved results files facilitate result aggregation, analysis, presentation, and archival. Measurement results may be exported as .csv, which can be opened in an Excel spreadsheet for flexible data analysis.

The RA is manufactured by:

#### Quasonix, Inc. 6025 Schumacher Park Drive West Chester, OH 45069 CAGE code: 3CJA9

### 1.1 Description

This document describes the installation and operation of the Quasonix RA. The RA is designed to measure the performance of receivers and demodulators by supplying RF test signals to the unit under test and monitoring receiver performance via the clock and data outputs of the unit under test.

Measurements use six key parameters: Frequency (RF) range, modulation, data patterns, data rates, noise, and power level.

The following waveform formats are supported by the RA:

- PCM/FM (ARTM Tier 0)
- SOQPSK (ARTM Tier I)
- SOQPSK-LDPC
- Multi-h CPM (ARTM Tier II)
- STC
- DPM
- Legacy (PSK) suite, which includes:
  - BPSK
  - QPSK
  - Offset QPSK (OQPSK)
  - UQPSK
- 1 Quasonix, Inc.

- \*AQPSK
- \*AUQPSK

The eight (8) built in bit error rate testers can be used to test any clock and data outputs from *any* receiver or demodulator. The only true limitation of testing the output of any receiver is maximum bit rate.

\*AQPSK and AUQPSK are generated from two orthogonal BPSK-modulated bit streams.

#### 1.2 Nomenclature

The RA is identified by the following part number:

#### QSX-RXAN-3R1D-A1-1111

#### 1.3 Package Contents

The contents of the box include the following:

- RA unit
- USB 2.0 AB cable
- Power cord
- Two (2) calibrated RF output cables
- Six (6) 75 ohm clock and data BNC cables
- CD with control software, user manual, data sheets, etc.

Note: The very latest User Manual and Data Sheet are always available on the Quasonix web site.

## 2 Specifications

Signal Generator Section	
RF Outputs	2, can be slaved
Power Level	-10 dBm to –150 dBm, default range (set in 0.01 dB or finer steps)
Output RF Frequency	200.0-2500.0 MHz, tunable in 1 Hz or finer steps 4400.0-5250.0 MHz, tunable in 1 Hz or finer steps
Modulation Formats	PCM/FM, SOQPSK, MHCPM, BPSK, QPSK, OQPSK, UQPSK, AQPSK*, AUQPSK*, DPM, STC
Bit Rates	100 bps to 46 Mbps (mode dependent) settable in 1 bps or finer steps
Data Generator Functions	Patterns: PN6, PN9, PN11, PN15, PN17, PN20, PN23, PN31, User (2 to 32 bits) Manual error insertion Continuous error insertion, BER 1e-9 to 0.5
Coding Options	LDPC encoding (per IRIG 106-22 Appendix 2-D) Reed-Solomon encoding (E=16, I=1-8, per CCSDS 131.0-B-3 Section 4) Convolutional encoding (r=1/2, K=7, per CCSDS 131.0-B-3 Section 3) Randomization (per IRIG 106-22 and CCSDS 131.0-B-3 Sections 8, 10) PCM encoding: NRZ-L/M/S, BIΦ-L/M/S, RZ, DM-M/S, M2-M/S Basic PCM framing (sync pattern 16 to 33 bits, minor frame up to 16384 bits, major frame up to 256 minor frames, with subframe ID insertion) Data inversion
Signal Generator Functions	Multipath fading (settable synchronized or asynchronous phase relationship between RF channels) Multi-ray multipath channel emulation Amplitude modulation Calibrated additive white Gaussian noise
Clock and Data In/Out	TTL (BNC) Supports one (1) input and one (1) selectable output (including decapsulated DQE)

Receiver Input/Status Output Section			
Clock and Data In	TTL (BNC) Supports up to eight (8) clock and data input pairs for bit error rate testing from receivers, demodulators, etc.		
Input Codes	NRZ-L		
Status Out	TTL (HDB-15) BERT Error (CH1I, CH2I), Signal On/Off (CH1I, CH2I), Sync Detect (CH1I, CH2I, CMBI, BSSI)		
DQE Decapsulation	Automatic payload size detection		
Data Sync Detection	Continuous sync time monitoring Continuous sync loss time (i.e., latency) monitoring		
BERT	Elapsed time, measured bit rate, total bits Measured error rate, measured errors, measured errored seconds, measured link availability Estimated error rate, estimated errors, estimated errored seconds, estimated link availability (estimated based on DQM)		
Environmental Section			
Operating Temperature	0°C to +50°C		
Non-operating Temperature	0°C to +70°C		
Operating Humidity	0 to 95% (non-condensing)		
Altitude	Up to 30,000 ft.		
Physical Section			
Size	1U chassis; 19" wide, 1.75" tall, 14-5/16" rack depth, 15-11/16" overall depth		
Weight	12.0 lbs.		
Connectors – per RF Channel	RF Out: N female Status Out: DB-15 High Density female		
Connectors – per Receiver Channel (CH1, CH2, CMB, BS	SS) I Clock, Q Clock, I Data, Q Data In: BNC female		
Connectors – per Chassis	TX Clock/Data In/Out: BNC female USB-B for user interface control AC power in		
Power	25 W @ 120 VAC		

\*AQPSK and AUQPSK are generated from two orthogonal BPSK-modulated bit streams.

## **3** Installation Instructions

#### 3.1 Mechanical

The RA's enclosure fits in a standard 19" rack, occupying just 1U of rack space. Mechanical layouts are provided in Figure 1 and Figure 2.

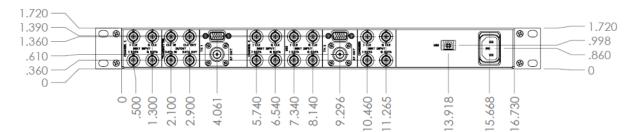
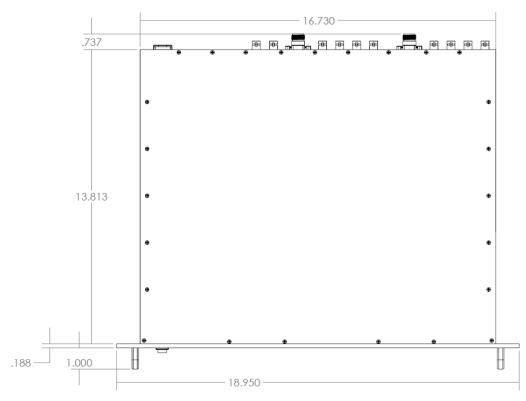


Figure 1: Mechanical Drawing – Back View





#### 3.2 Thermal

The storage temperature of the Rack Mount RA is rated for  $0^{\circ}$ C to  $+70^{\circ}$ C, while the operating temperature is rated for  $0^{\circ}$ C to  $+50^{\circ}$ C. It is recommended that the unit be kept in a temperature controlled environment to minimize the risk of operating (or storing) outside the ranges specified.

The RA features cooling vents on both sides of its aluminum chassis. These vents must be kept entirely unobstructed in order to allow for maximum airflow through the system. Whenever feasible, it is helpful to leave an open rack space above and below the Rack Mount RA for additional heat dissipation.

#### 3.3 Electrical

The Rack Mount RA is available only in a dual channel configuration, with all pertinent electrical connections located on the rear panel.

#### 3.3.1 RA Connections

The electrical interface connector layout for the RA is shown in Figure 3. The TTL inputs are compatible with both 3.3 volt and 5 volt signals.



Figure 3: RA Back Panel

Function	<b>Electrical Characteristics</b>	Connector Type
Channel 1, In-phase (I) Clock BERT Input	75 ohm TTL	75 ohm BNC
Channel 1, In-phase (I) Data BERT Input	75 ohm TTL	75 ohm BNC
Channel 1, Quadrature (Q) Clock BERT Input	75 ohm TTL	75 ohm BNC
Channel 1, Quadrature (Q) Data BERT Input	75 ohm TTL	75 ohm BNC
Channel 2, In-phase (I) Clock BERT Input	75 ohm TTL	75 ohm BNC
Channel 2, In-phase (I) Data BERT Input	75 ohm TTL	75 ohm BNC
Channel 2, Quadrature (Q) Clock BERT Input	75 ohm TTL	75 ohm BNC
Channel 2, Quadrature (Q) Data BERT Input	75 ohm TTL	75 ohm BNC
Combiner, In-phase (I) Clock BERT Input	75 ohm TTL	75 ohm BNC
Combiner, In-phase (I) Data BERT Input	75 ohm TTL	75 ohm BNC
Combiner, Quadrature (Q) Clock BERT Input	75 ohm TTL	75 ohm BNC
Combiner, Quadrature (Q) Data BERT Input	75 ohm TTL	75 ohm BNC

#### **Table 1: Rear Panel Connector Specifications**

Function	Electrical Characteristics	Connector Type
BSS, In-phase (I) Clock BERT Input	75 ohm TTL	75 ohm BNC
BSS, In-phase (I) Data BERT Input	75 ohm TTL	75 ohm BNC
BSS, Quadrature (Q) Clock BERT Input	75 ohm TTL	75 ohm BNC
BSS, Quadrature (Q) Data BERT Input	75 ohm TTL	75 ohm BNC
Transmitter Clock In	75 ohm TTL	75 ohm BNC
Transmitter Clock Out	75 ohm TTL	75 ohm BNC
Transmitter Data In	75 ohm TTL	75 ohm BNC
Transmitter Data Out	75 ohm TTL	75 ohm BNC
Channel 1, Auxiliary Output 1	75 ohm TTL	Ch1 DB-15, Pin 6
Channel 1, Auxiliary Output 2	75 ohm TTL	Ch1 DB-15, Pin 11
Channel 1, Auxiliary Output 3	75 ohm TTL	Ch1 DB-15, Pin 12
Channel 1, Auxiliary Output 4	75 ohm TTL	Ch1 DB-15, Pin 13
Channel 2, Auxiliary Output 1	75 ohm TTL	Ch2 DB-15, Pin 6
Channel 2, Auxiliary Output 2	75 ohm TTL	Ch2 DB-15, Pin 11
Channel 2, Auxiliary Output 3	75 ohm TTL	Ch2 DB-15, Pin 12
Channel 2, Auxiliary Output 4	75 ohm TTL	Ch2 DB-15, Pin 13
Tx 1 RF Output	50 ohms	N
Tx 2 RF Output	50 ohms	N
USB Control	5 V Standard	USB-B
Power	90-264 V-rms AC, 47-63 Hz	EAC309X

#### 3.3.2 Digital Status Outputs

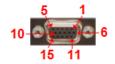
The RA digital status outputs allow the user to monitor internal status in real time. There are two female DB-15 auxiliary connectors used for this purpose. The channel 1 connector is defined as J9, while the channel 2 connector is defined as J19, as shown in Figure 4.



Figure 4: RA Back Panel, J9 and J19 Labeled



The female DB-15 connector pins are numbered as shown in Figure 5.



#### Figure 5: Female DB-15 Connector, Numbered

Table 2 describes the default status output signal parameters. These outputs can be configured for various output status. They can optionally be configured to drive a DAC for eight (8) analog outputs. Refer to section Status Outputs for details.

Signal	Function	Connector	Pin	Note	
CH1I Error	CH 1, Aux Output 1	Jð	6	Bit error detected. High for every bit that is in error.	
CH2I Error	CH 2, Aux Output 1	J19	6	Bit error detected. High for every bit that is in error.	
CH1 RF On	CH 1, Aux Output 2	Jð	11	High when CH1 RF enabled, low when RF disabled	
CH2 RF On	CH 2, Aux Output 2	J19	11	High when CH2 RF enabled, low when RF disabled	
CH1I Sync Detect	CH 1, Aux Output 3	Jə	12	High when CH1I synchronization detected (i.e., the user-selected sync detect data pattern is present in the receiver output data), low otherwise	
CH2I Sync Detect	CH 2, Aux Output 3	J19	12	High when CH2I synchronization detected (i.e., the user-selected sync detect data pattern is present in the receiver output data), low otherwise	
CMBI Sync Detect	CH 1, Aux Output 4	J9	13	High when CMBI synchronization detected (i.e., the user-selected sync detect data pattern is present in the receiver output data), low otherwise	
BSSI Sync Detect	CH 2, Aux Output 4	J19	13	High when BSSI synchronization detected (i.e., the user-selected sync detect data pattern is present in the receiver output data), low otherwise	

#### Table 2: Digital Status Output Descriptions

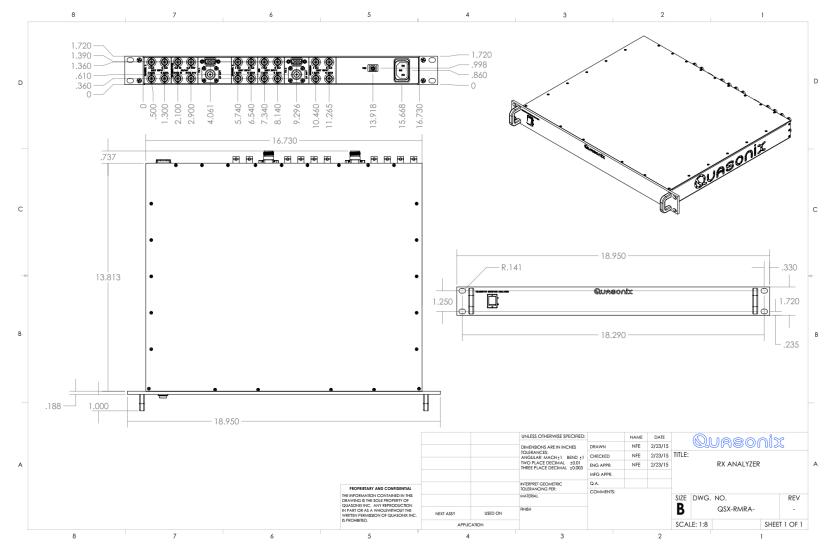


Figure 6: RA Installation Drawing

#### 3.4 Connect Devices

Connections between the Receiver and the RA will vary depending on the type of receiver and the measurements desired.

- 1. Connect a Receiver to the RA using the appropriate TTL clock and data cables for the receiver; connect the RF cable from the RA to the Receiver RF input.
- 2. Connect the RA to a Windows PC using the USB connector on the back panel.
- 3. Connect each device to an appropriate power source.

#### 3.5 Install RxAn GUI Software

The Quasonix 3<sup>rd</sup> Generation RA is shipped with an installer for the RxAn GUI.

- 1. Copy the installer software to a location on the connected PC.
- 2. Go to the software folder and locate the setup.exe file.
- 3. Double-click on the setup.exe file to start running.

Do not execute RA3Install.msi directly. Setup.exe installs any needed external requirements. It is recommended that the default file locations be used.

RxAn uses a support library from Microsoft for the advanced math. The installer checks to see if the system has this library installed and attempts to install it, if necessary. If your system does not have this library installed, you will be prompted to let the installer install it, as shown in Figure 7.

Click on the Install button to continue.

💀 RxAn Setup	×			
The following components will be installed on your machine:				
Visual C++ "14" Runtime Libraries (x64)				
Do you wish to install these components?				
If you choose Cancel, setup will exit.				
Install Cancel				

Figure 7: RxAn Setup, Install Components

A status window shows the progress of the installation, as shown in Figure 8.

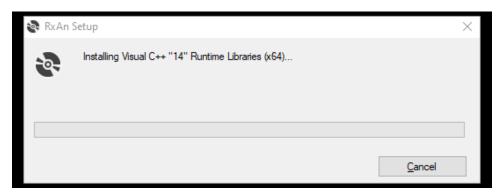


Figure 8: RxAn Setup, Installing Components Status

These libraries have their own installers. As each runs, you will be prompted to allow the installer to make changes to your system. This may only be indicated by the secure desktop icon in the taskbar, as shown in Figure 9.



Figure 9: Secure Desktop Icon

Click on the Secure icon, then select Yes to continue.

After the required library is verified as installed, the RxAn Setup Wizard displays, as shown in Figure 10.

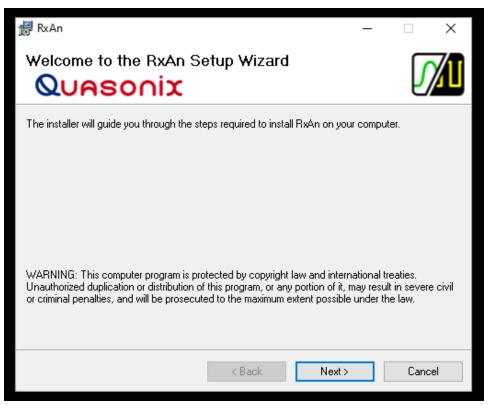


Figure 10: RxAn Setup Wizard Welcome Screen

4. Click on the Next button to continue the installation.

The RxAn installation includes the matching firmware for the version of RxAn being installed. This is located separately in case the user has difficulty with a firmware update, this can be used, under the guidance of Quasonix, to help complete the firmware update.

While the PC location of the firmware file may be changed, it is recommended that the default file locations be used, as shown in Figure 11.

5. Click on the Next button to continue.



Figure 11: RxAn Setup Wizard, Firmware Location

RxAn communicates with the RxAn hardware through a USB interface. This interface requires drivers to be installed on the PC, if they are not already installed. To install the drivers, click on the Install Drivers? check box, as shown in Figure 12.

If this is an update or reinstallation of RxAn, the user can uncheck the box.

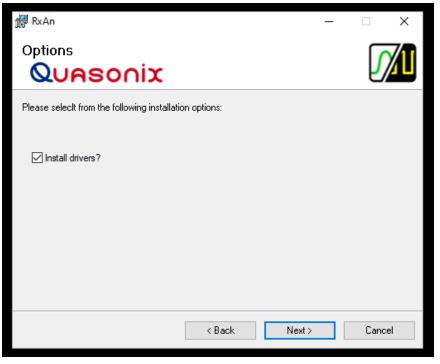


Figure 12: RxAn Setup Wizard, Install Drivers Selection

RxAn is a 64-bit executable and will be installed in the 64 bit application folder, shown in Figure 13.

妃 RxAn	– 🗆 X
Select Installation Folder	
The installer will install RxAn to the following folder.	
To install in this folder, click "Next". To install to a different folder, enter it be	elow or click "Browse".
<u>F</u> older:	
C:\Program Files\Quasonix\RxAn\	Browse
	Disk Cost
Install RxAn for yourself, or for anyone who uses this computer:	
Just me	
< Back Next >	Cancel

Figure 13: RxAn Setup Wizard, Select Installation Folder

6. Click on the Next button to continue.

When Confirm Installation displays, all of the installation options are set, and the installer is ready to begin the installation, as shown in Figure 14.

🚏 RxAn		-	
Confirm Installation			
The installer is ready to install RxAn on Click "Next" to start the installation.	your computer.		
	< Back	Next >	Cancel

Figure 14: RxAn Setup Wizard, Confirm Installation

7. Click on the Next button to start the installation.

The installation may ask if it is ok to install from an unknown source. If it does, click on the Yes button.

User Account Control	×
Do you want to allow to unknown publisher to device?	this app from an make changes to your
C)	
C:\Users\l\OneDriv	ve\Desktop\Release
\RA3Install.msi	
Publisher: Unknown	
File origin: Hard drive on this co	omputer
Show more details	
	No

Figure 15: RxAn Setup Wizard, Unknown Publisher Message

While RxAn is being installed, a progress window displays, as shown in Figure 16.

🛃 RxAn		_	
Installing RxAn			
RxAn is being installed.			
Please wait			
	< Back	Next >	Cancel

Figure 16: RxAn Setup Wizard, Installing RxAn Status

After RxAn is installed, if the user has allowed the drivers to be installed, the Device Driver Installation Wizard displays, as shown in Figure 17.

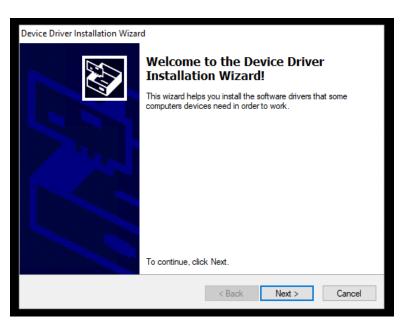


Figure 17: Device Driver Installation Wizard Welcome Screen

8. Click on the Next button to continue the driver installation.

A License Agreement for the device driver displays, as shown in Figure 18. Select "I accept this agreement." You may print or save the agreement to a file, if desired.

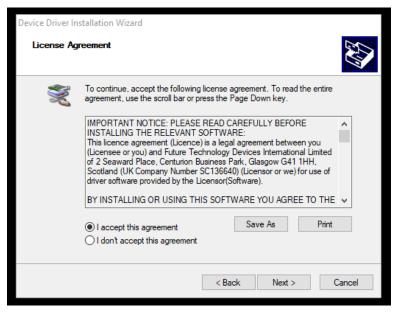


Figure 18: Device Driver License Agreement Acceptance

9. Click on the Next button to continue.

When the driver installation is complete, an installation status displays, as shown in Figure 19.



Figure 19: Completing the Device Driver Installation Wizard

10. Click on the Finish button to complete the driver installation.

When the RxAn installation is complete, a final status displays, as shown in Figure 20.



Figure 20: RxAn Installation Complete

After the installation is complete, a shortcut is added to the desktop. The RxAn icon is shown in Figure 21.



Figure 21: RxAn Desktop Shortcut Icon

When the installation finishes, a shortcut to RxAn is also installed in the Windows Start Menu under a Quasonix folder along with shortcuts to the RA3 Firmware and other RxAn Resource, including project files, layouts, measurements, and other documentation, as shown in Figure 22.

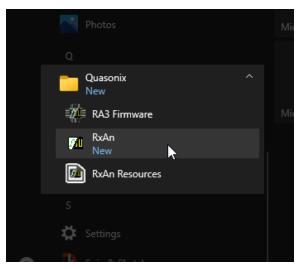


Figure 22: RxAn Resource Shortcuts on Start Menu

### 3.6 Uninstall RxAn

RxAn can be uninstalled by using either the Windows Apps & Feature settings, or by executing the original setup.exe file.

The Welcome to RxAn Setup wizard displays, as shown in Figure 23. This lets the user repair the existing installation or to remove RxAn.

1. Select Remove RxAn, then click on the Finish button.



Figure 23: RxAn Setup Wizard, Repair or Remove



2. The installation may ask if it is ok to install from an unknown source. Click on the Yes button.



Figure 24: RxAn Setup Wizard, Unknown Publisher Message

A confirmation displays after RxAn is removed, as shown in Figure 25.

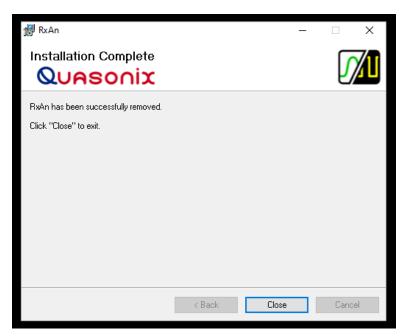


Figure 25: RxAn Setup Wizard, Installation (Removal) Complete

3. Click on the Close button to exit.



# 3.7 Install Firmware Update

The RA hardware contains an Altera FPGA with embedded microprocessor. Each requires its own firmware to operate the RA. RxAn requires a specific version of firmware.

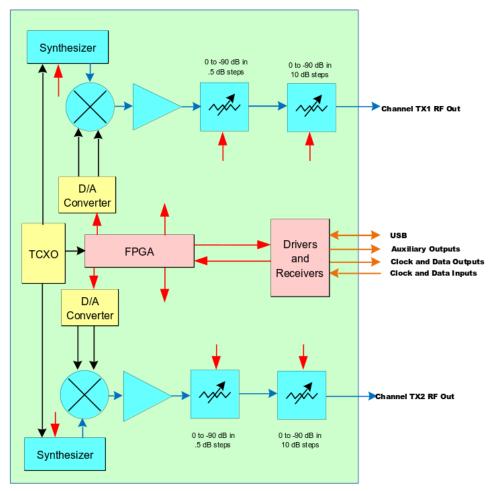
Firmware updates are initiated from the Tools menu. For detailed information, refer to section 12, Appendix C.

# 4 Theory of Operation

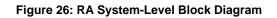
The RA consists of two channels of RF output and eight channels of receiver data analysis. The RxAn GUI provides access to this hardware via a USB interface connected to an FPGA. The RxAn GUI is closely mapped to the structure of the FPGA and facilitates detailed control of all available functionality.

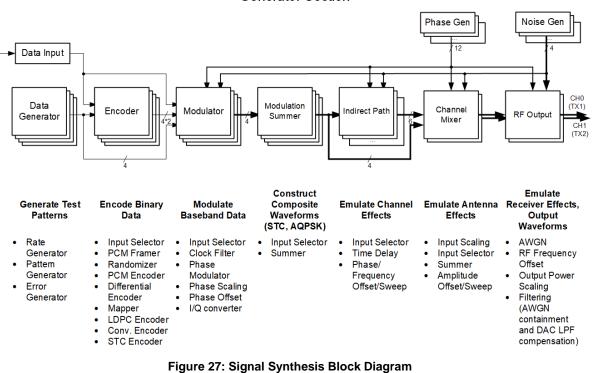
Each of the dual RF channels consists of a Digital Baseband Signal Generator in the FPGA, Digital-to-Analog (D/A) Converter, Synthesizer, I/Q Modulator, Amplifiers, and Step Attenuators. The channel frequencies can either be synchronized (coherent) or different for frequency diversity simulation or adjacent channel interference testing. Behind the scenes, the FPGA controls all RA hardware, including loading the Synthesizer frequencies, and setting the Step Attenuators for the proper output power. Calibrated output power is achieved through digital gain control in the FPGA.

Drivers and receivers buffer and convert the high speed clock and data signals to the appropriate FPGA operational levels. These signals are monitored in the FPGA to measure bit rate, pattern synchronization, and both actual and estimated bit error rate (if Data Quality Encapsulation is enabled in the receiver under test).

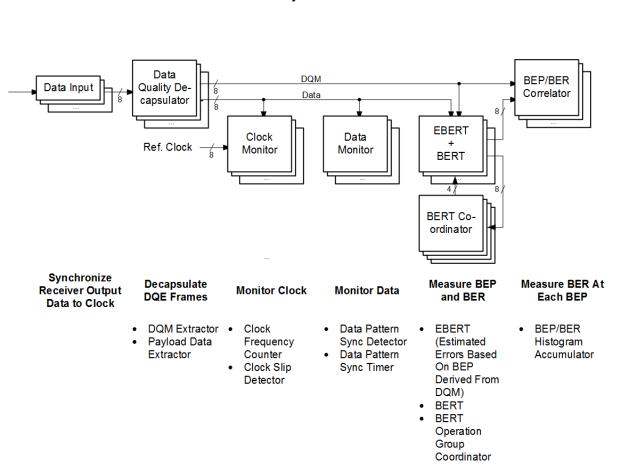


Legend: Red = Digital, Blue = RF, Yellow = Mixed/Both





**Receiver Analyzer Generator Section** 



Receiver Analyzer Analyzer Section

Figure 28: Analyzer Section Block Diagram

# 5 Hardware and Communications Set Up

# 5.1 RA Front-Panel

The RA front panel contains only a power switch.



Figure 29: RA Front Panel

# 5.2 RA Back Panel

A variety of connectors are located on the RA back panel, shown in Figure 30. Enlarged illustrations of the left and right half of the back panel are shown in Figure 31 and Figure 32. Note that channel and I/Q designations are intended to provide simple signal identification for most typical use cases, but they generally have no special meaning in RA operation. The only functional exception is for sync time measurement (refer to section 6.4.13.2). Therefore, for most testing, any receiver Clock/Data pair may be connected to any RA Clock/Data inputs.



Figure 30: RA Back Panel

An enlarged photo of the left half of the back panel is shown in Figure 31. It contains the following connectors: BERT I and Q Clock and Data Input for Channel 1, Transmitter I and Q Clock and Data In/Out, Transmitter 1 RF Output and DB-15 connector, and BERT I and Q Clock and Data Input for Channel 2.

## 3<sup>rd</sup> Generation Receiver Analyzer

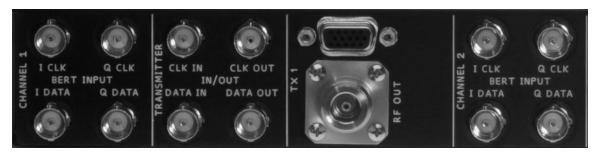


Figure 31: RA Back Panel, Left Side Enlarged

An enlarged photo of the right half of the back panel is shown in Figure 32. It contains the following connectors: BERT I and Q Clock and Data Input for Best Source Selection (BSS), Transmitter 2 RF Output and DB-15 connector, BERT I and Q Clock and Data Input for Diversity Combiner, USB Control port, Power Supply port.



Figure 32: RA Back Panel, Right Side Enlarged

# 5.3 RA Communications Connection

Be sure the RA is connected to the PC and that the RxAn software and drivers have been installed, as described in section 3.4 and section 3.5.

- 1. Go to the desktop shortcut or to the Windows Start menu.
- 2. Double-click on the RxAn.exe file.

The RA application scans available communication ports (COM), and automatically connects to the one attached to the RA.

When the application opens, a box in the lower left corner of the RxAn GUI displays the COM port connected. It displays in green when successfully connected, as shown in Figure 33.

The lower right side displays status information received from the connected RA, as shown in Figure 33.

If there is no valid COM port connection, or no status information, use the Connection option in the Main Menu Toolbar, discussed in section 6.3.1.1, to troubleshoot the problem.

If connecting to multiple RAs, run another instance of the application after successfully connecting the first unit.

	- 4 × D	ata Generator DG0 🛛 🔻 🕸 🕽	< Encode	r ENC0 ▼ ♯ ×	Modulator MOD0	<b>~</b> ↓ ×	Channel CH0	→	Channel CH1	<b>→</b> ‡
Ξ	2	የ 🕼 🖗	三智	12 🖏 🏘	<b>=</b> %	12 🖏 🕸	Ξ	9	Ξ	2
Measurement: None Start End: Waiting Waiting Single Paused Complete Canceled	0	Pattern: PN23 V Rate: 10 V Enable BER: 1.000E-05 V Continuous Insert Single	Diff E B C	a Source: DGO v incoding: IRIG: CCSDS: Iock FEC: None v incoding: NRZL v	Airlink Data Rate	n: PCMFM · r: 1 ¢r able	Frequency: 2225 Power; 50 AWUN: 100 RF Signal	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Frequency: 2225 Power: -50 AWGN: 100 R Sign	
DQE CH11	→ ↓ × P @ Ø	Pattern: PN23 Time Limit: 1		BERT CH1I Monitor Messured Bit Error Rate O.OOOI 4000 Estimated Bit Error Rate 1. Time: Bit Rate 00:01:49:08.5 10.00000	E + 00 009E-12 Bits	BERT CMBI Monitor Measured Bit Error Ra 0 0 00 Estimated Bit Error Ra Time: Bit R: 00:01:49:08.5 9.99	<b>0E+00</b> te 1.144E-12 ate Bits	Measure O. Estimate	d Bit Error Rate <b>0000E</b> d Bit Error Rate 1.00 Bit Rate	

Figure 33: RA RxAn GUI, COM Port and Status Information Circled

# 6 System Operation

# 6.1 RA

The Quasonix RA is composed of two primary sections:

- Signal Generation
- Measurement (Demodulated data analysis)

The signal generation section is used to create a broad variety of telemetry transmit RF signals and simulate conditions often found after signal propagation over the air. The measurement section is used to examine data from a receiver after demodulating the generated signals.

Each section is composed of one or more instances of functional modules. There are over 30 module types, and over 100 instances of modules. Each of these modules can be connected and configured in various ways to perform a broad array of receiver performance measurements. This results in over 1000 configuration properties and measurement parameters.

The RA is a powerful and flexible piece of test equipment. With that power and flexibility comes complexity. RxAn software is the RA graphical user interface used to harness and control that power to perform a wide array of receiver performance measurements.

Not all of the RA's modules, properties, and parameters are required for every measurement. In some cases, only the generated signal is needed to see if a receiver is properly configured. In other cases, only the Bit Error Rate Test module is needed to check demodulated data from an external signal source. Sometimes complex measurements of subtle performance differences between receivers is required.

Having a user interface that can accommodate the most complex cases would be overwhelming for the simpler situations. The RxAn GUI is designed to be flexible and configurable, providing only what is necessary for the task at hand and hiding unnecessary features.

Figure 34 shows an example of the Quasonix RxAn GUI.

# 3<sup>rd</sup> Generation Receiver Analyzer

Veasurement 👻 A 🗙	Data Generator DG0 🛛 🔻 🕂 🗙	Encoder ENC0 💌 🕂 🗙	Modulator MOD0 - 4 2	X Channel CH0	▼ # × Channel CH1 ▼ J
Ξ	😲 🖉 🕸	三 12 02 08	Ξ 😢 👘 😢 🕸	Ξ	e) = 0
Measurement: 0 None Start: End: Running Waiting Single Paused Complete Canceled	Pattern: PN23 V Rate: 10 C P Enable BER: 1.000E-05 C P Continuous Insert Single	Data Source: DGO V Diff Encoding: IRIG: CCSDS: Block FEC: None V Conv FEC: PCM Encoding: NRZL V	Data Source: ENCO V Modulation: PCMFM V Modulation Scale: 1 PCMFM Enable Airlink Data Rate: 10.000018 Modulat Modulat Summer	Power: -50	Image: Constraint of the state of
Content of the second		<ul> <li>Time: Bit Rate</li> <li>00:01:49:55.6</li> <li>9.999996</li> </ul>	009E-12         Estimated Bit Error           Bits         6.60e+10           rr Secs Avail         Errors           0.000000 bit         0.000000 bit           Errors EDebt         EErrors EDebt	Rate <b>DOE + + 00</b> Rate 1.144E-12 Rate 8 its 999956 6.60e+10 Er Err Secs Avail 0 1.00 a Err EER Secs EAvail 0 1.00	ERT CH2I Monitor         Image: Ch2i Monitor           Measured Bit Error Rate         0.000 E + 000           Estimated Bit Error Rate         0.009 E - 12           Time:         Bit Rate         6.06+10           0.00149:55.6 6.06+10         0         1.00           Errors         Delta Err         Err Sec         Avail           0.000000         0         1.00         Errors           Efform         E.Delta Err         Err Sec E.Avail         0.06544           0.06544         0         1.00         Image: Character

Figure 34: Example of Basic 3rd Generation RA RxAn GUI

The RxAn GUI consists of a set of panels, with each panel representing a different function within the interface. Panels may be displayed, hidden, moved, and arranged to suit the user.

## 6.2 RxAn Elements

The RxAn GUI is broken into three major regions:

- Menu Bar
- Status Bar
- Main Dock

#### 6.2.1 Menu Bar

The Menu Bar, shown in Figure 35, contains a standard Windows menu interface for lesser used operations that are not directly related to the operation of the RA, such as opening/saving files, loading/saving layouts, connecting to the RA, setting software options, etc. Detailed menu descriptions are provided in section 6.3.

## 3<sup>rd</sup> Generation Receiver Analyzer

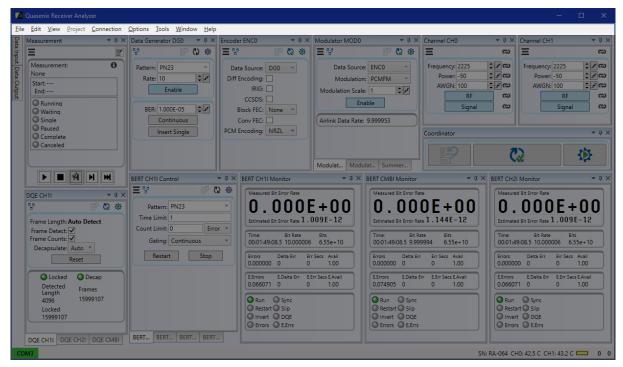


Figure 35: RxAn GUI, Menu Bar

### 6.2.2 Status Bar

The Status Bar, shown in Figure 36, contains information about the status of the RA and its connection to the RxAn software, such as the communications port in use, the serial number of the RA attached, communication status, etc. Detailed status and connection information is described in section 6.3.1.1.

## 3<sup>rd</sup> Generation Receiver Analyzer

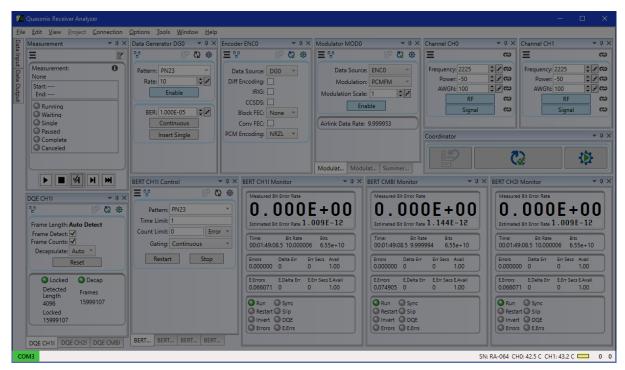


Figure 36: RxAn GUI, Status Bar

## 6.2.3 Main Dock

The Main Dock, shown in Figure 37, contains the RA control and status Tool Panel layout. This region can be tuned for specific tasks. Tool Panels can be added, removed, positioned, and sized where it makes the most sense to the user. After a layout has been created, it can be saved and reloaded.

# 3<sup>rd</sup> Generation Receiver Analyzer

Measurement 👻 म 🗙	Data Generator DG0 🛛 🔻 🕮 🗙	Encoder ENC0 💌 🕮 🗙	Modulator MOD0 👻 🕮	× Channel CH0	▼ ♯ × Channel CH1 ▼
Ξ	😌 🔮 😨 🕸	Ξ 😗 👘 😰 🕸	Ξ 😢 🔮 🕲 🕸	5 =	8
Measurement: 0 None Start: End: © Running © Waiting © Single © Paused © Complete © Canceled	Pattern: PN23 V Rate: 10 C Z Enable BER: 1.000E-05 C Z Continuous Insert Single	Data Source: DG0 V Diff Encoding: IRIG: CCSD5: Block FEC: None V Conv FEC: PCM Encoding: NRZL V	Data Source: ENCO v Modulation: PCMFM v Modulation Scale: 1 F Enable Airlink Data Rate: 9.999953	Frequency: 2225 Power; 50 AWGN; 100 Coordinator	
DQE CH11 · · · · · · · · · · · · · · · · · ·		v ↓ × BERT CH1I Monitor V V V V V V V V V V V V V V V V V V V	* # ×     BERT CMBI Monit       E + 00     Measured Bit Erro       009E-12     Estimated Bit Erro       Bits     Time:	Rate <b>DOE + 00</b> Rate 1.144E-12 it Rate Bits	BERT CH2! Monitor           Measured Bit Error Rate           O           D           Estimated Bit Error Rate 1           000 DE + 000           Estimated Bit Error Rate 1           00001:49:08.5 10.000006           6.55e+10
Reset	Restart Stop	0.000000 0 0	Err Secs E.Avail	0 1.00 Ita Err E.Err Secs E.Avail 0 1.00	Errors         Delta Err         Err Secs         Avail           0.0000         0         1.00         1.00           E.Errors         E.Delta Err         E.Err Secs         E.Avail           0.066071         0         1.00         1.00           Quarter         Quarter         0.00         1.00

Figure 37: RxAn GUI, Main Dock

The following examples describe basic layouts included with RxAn. Each has various Tool Panels displayed in different configurations optimized for different tasks.

### **Transmit Only Layout**

File	Quasonix Receiver Analyzer Edit View Project Connec	ction <u>O</u> ptions <u>T</u> ools <u>W</u> indow	<u>H</u> elp		– o x
Data Input Data Output Status Outputs		Encoder ENCO	Modulator MOD0 • # × Comparison of the second seco	Frequency: 2225 \$ 7 @ Power: -50 \$ 7 @ AWGN: 15.5 \$ 7 @ RF @ Signal @	Channel CH1
	JM3		Modulat Modulat Summer	SN: RA-064 CH0	428 C CH1: 43.5 C

Figure 38: RxAn GUI, Transmit Only Layout

#### **Basic Layout**

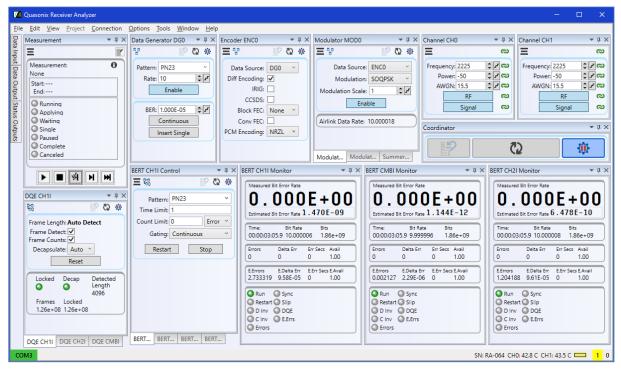


Figure 39: RxAn GUI, Basic Layout

### **Complete Layout**

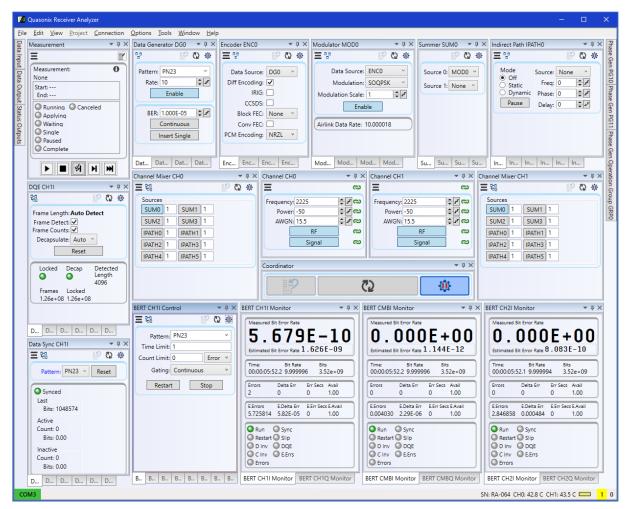


Figure 40: RxAn GUI, Complete Layout

### 6.2.3.1 Tool Panels

The RxAn GUI uses multiple Tool Panels. Each Tool Panel encompasses the functionality of a single module in the RA. A Tool Panel may have controls to set properties, display status, or both.

Tool Panels exist in one of four states:

- Closed
- Floating
- Docked
- Auto Hide

### 6.2.3.1.1 Docked

A Tool Panel can be docked within the Main Dock. The user can place these Tool Panels in the Main Dock in various combinations to suit the user's needs. In this configuration, Tool Panels automatically reflow and resize, expanding to consume the available space. The available space can be controlled by sizing the main window and by dragging Resizing Bars between Tool Panels.

The following figures show three simple examples of RxAn, all with the same six Tool Panels docked in various combinations in the Main Dock.

🕅 Quasonix Receiver Analyzer				– 🗆 X
<u>File Edit View Project Co</u>	nnection <u>O</u> ptions <u>T</u> ools <u>W</u> indo	w <u>H</u> elp		
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Pattern: PN23 V Rate: 10 V Enable BER: 1.000E-05 V Continuous	Data Source: DG0 V Diff Encoding: IRIG: CCSDS: Block FEC: None V Conv FEC:	Data Source: ENCO Modulation: PCMFM ··· Modulation Scale: 1 ··· Enable Airlink Data Rate: 10.00018	E © Frequency: 2225 \$ <b>7</b> © Power: -50 \$ <b>7</b> © AWGN: 100 \$ <b>7</b> © AWGN: 100 \$ <b>7</b> © Signal ©	E
Insert Single	PCM Encoding: NRZL V		Coordinator	+ 43.0 C CH1: 43.8 C

Figure 41: RxAn GUI, Simple Docking Arrangement

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Rate:     10     Image: Constraint of the second se	r MODO • # × channel CHO • # × channel CHO • # × coordinator • # * coordinator • # × coordinator • # * coordinator • # *
СОМЗ	SN: RA-064 CH0: 42.5 C CH1: 43.2 C 🥅 👖 0

Figure 42: RxAn GUI, Docking Arrangement 2

💯 Quasonix Receiver Analyzer			– 🗆 X
<u>File Edit View Project Connection C</u>	<u> P</u> ptions <u>T</u> ools <u>W</u> indow <u>H</u> elp		
Data Generator DG0 🛛 🔻 🕸 🗙	Encoder ENC0 💌 म 🗙	Modulator MOD0 👻 A 🗙	Coordinator 👻 🕂 🗙
Pattern: PN23 Rate: 10 BER: 1.000E-05 Continuous Insert Single	Data Source: DG0 V Diff Encoding: CCSDS: CSDS: Block FEC: None V Conv FEC: PCM Encoding: NRZL V	Enable	🐑 🐼 🅸
Channel CH0	- ŋ	X Channel CH1	
Ξ	G	» Ξ	9
Frequency: 2225 Power: -50 AWGN: 100 R Sig		Power:         -50           AWGN:         100           RF	2 2 2 2 *******************************
СОМЗ		SN: RA-0	064 CH0: 42.5 C CH1: 43.2 C 🥅 👖 0

Figure 43: RxAn GUI, Docking Arrangement 3

Because Tool Panels automatically resize, it is possible to create a layout such that Tool Panel contents are clipped, as shown in Figure 44. This can be corrected by resizing the main window or by dragging the Resizing Bars.

💋 Quasonix Receiver Analyzer			—	
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BER: 1.000E-05	Block FEC: None V	Enable		
Channel CH0	<b>~</b> 0	X Channel CH1		<b>~</b> ↓ ×
Ξ		ະ ອ Ξ		୍ଚ
Frequency: 2225	÷ F G	S Frequency: 2225		(S) L =
Power: -50	÷ r c			≎ r ⇔
AWGN: 100	\$ I G	S AWGN: 100		≎ r ©
	RF	ອ	RF	9
Si	ignal	ອ	Signal	3
СОМЗ			SN: RA-064 CH0: 42.5 C CH1: 43.2 C	0 0

Figure 44: RxAn GUI, Tool Panel Contents Clipped

## 6.2.3.1.1.1 Docked Tool Panel Groups

Tool Panels may be combined into Tool Panel Groups, as shown in Figure 45. A Tool Panel Group can be moved to a different location in the Main Dock as a single unit and will resize as a single unit when a Tool Panel Resizing Bar for the Tool Panel Group is moved, as shown in Figure 46. For more information about resizing, refer to section 6.2.3.3.3.



💋 Quasonix Receiver Analyzer		
<u>File Edit View Project Connection</u>	<u>Options</u> <u>T</u> ools <u>W</u> indow <u>H</u> elp	
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8 2 4	E **	ව
Pattern: PN23 V Rate: 10 C Enable BER: 1.000E-05 C Continuous Insert Single	Data Source:       DOT         Diff Encoding:       Data Source:         IRIG:       Modulation:         RIG:       Modulation:         CCSDS:       Enable         Block FEC:       None         Airlink Data Rate:       9.999953         Channel CH0       Channel CH1	0 0 0 0
СОМЗ	Coordinator	

Figure 45: RxAn GUI, Tool Panel Group

🚺 Quasonix Receiver Analyzer				-	
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中 🖓 🤹	Ξ 😢 😰 🅸	<b>=</b> %	12 🐼 🐼		
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Rate: 10 🗘 🚅	Diff Encoding:	Modulation: PCMFM	v		
Enable	IRIG:	Modulation Scale: 1	¢.r		
	CCSDS:	Enable			
BER: 1.000E-05	Block FEC: None Y		$ \longrightarrow $	123	
Continuous	Conv FEC:  PCM Encoding: NRZL	Airlink Data Rate: 9.999953			
Insert Single					
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Frequency: 2225					⇔ r ≎
Power: -50					€ r ©
AWGN: 100					÷r⊗
		RF			9
		Signal			9
Channel CH0 Channel CH1					
СОМЗ				SN: RA-064 CH0: 42.5 C CH1: 43.	2 C 🔲 0 0

Figure 46: RxAn GUI, Tool Panel Group Moved and Resized

When panels are in a panel group, they are shown as tabs. A panel group can hold any panel type. The active panel displays and is indicated by a white tab, with the other grouped panel tabs in grey 'behind' the active panel, as shown in Figure 47; clicking on a tab moves that panel to the front of the panel group.

Modulator N	NOD0		<b>▼</b> џ ×
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	Source: EN		
Modulatio	n Scale: 1 Enable	÷-	
Airlink Dat	a Rate: 9.99	9953	
Modulat	Modulat	Modulat	Modulat

Figure 47: Example Modulator Panels Docked as Tabbed

If panels are Floating, as described in the previous section, each panel displays independently on the desktop and may be moved (dragged) to a desired location. Floating panels may also be grouped as in the Main Dock. For example, if the same four Modulator panels are open and MOD0 and MOD1 are floating, MOD2 and MOD3 are grouped.

## 6.2.3.1.2 Floating

A Tool Panel can be floating outside the main RxAn window. These Tool Panels can be positioned anywhere on the screen and resized independently, as shown in Figure 48 and Figure 49.

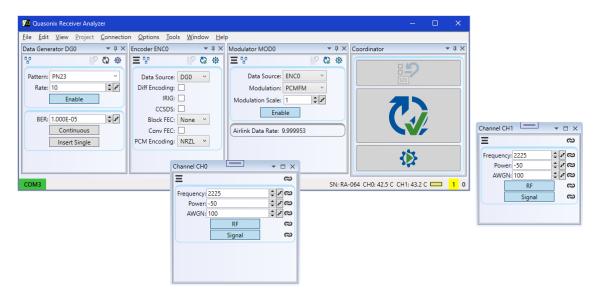


Figure 48: RxAn GUI, Floating Tool Panel Example 1

File Edit View Project Co Data Generator DG0	· · · · · · · · · · · · · · · · · · ·	Encoder ENC0	- ü ×	Modulator MOD0	 <b>•</b> µ >
Pattern: PN23 Rate: 10 Enable BER: 1.000E-05 Continuous Insert Single		Data Source: DG0 Diff Encoding: IRIG: CCSDS: Block FEC: None Conv FEC: PCM Encoding: NRZL		Data Source: ENCO Modulation: PCMFM Modulation Scale: 1 Enable Airlink Data Rate: 10.000018	2 39
annel CH0		Coordinator		SN: RA-064 CH0: 42.5	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

Figure 49: RxAn GUI, Floating Tool Panel Example 2

## 6.2.3.1.2.1 Floating Tool Panel Groups

Like Docked Tool Panel Groups, floating Tool Panels may be combined into a Floating Tool Panel Group, as shown in Figure 50, Figure 51, and Figure 52. Floating Tool Panel Groups support all of the same docking configurations as the Main Dock.

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File Edit View Project Connection	· · · · ·		
Data Generator DG0		▼ ♯ × Modulator MOD0	- ų
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Pattern: PN23	Data Source: DG0	Data Source:	FNC0 V
Rate: 10	⇒ F Diff Encoding:	Modulation:	
Enable	IRIG:		
Enable		Modulation Scale:	
BER: 1.000E-05	Block FEC: None	Enab	le
Continuous	Conv FEC:	Airlink Data Rate: 1	0.000018
		Arrink Data Rate:	0.000018
Insert Single	PCM Encoding: NRZL		
10 • × Coordinate	r 👻 Khannel	CH1 * X	4 CH0: 42.5 C CH1: 43.2 C 🔲 0
e		©	CHU: 42.5 C CHI: 43.2 C
y: 2225 🗘 🞜 🔊	Fraque	ncy: 2225 🗘 🞜 🝽	
n: -50 ÷ r co		wer: -50 ÷ r ©	
± 100 ≑ <b>r</b> ≈ 80		GN: 100 ÷ r 🖎	
k: 100 ♀ <b>F</b> ≈ ₽		RF (2)	
Signal 🔊		Signal 🔊	
		Signal Co	

Figure 50: RxAn GUI, Floating Tool Panel Group Example 1

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File Edit View Project	Connection Options	Tools Window Help		3		
Data Generator DG0	<b>~</b> ₫ ×	Encoder ENC0	<b>-</b> ↓ ×	Modulator MOD0		₹ Д
Pattern: PN23 Rate: 10 Enabl	2 Q ⊕ ↓ ↓  e	Data Source: DG0   Diff Encoding: IRIG:	\$ <b>63</b>	Data Source: ENCO V Modulation: PCMFM V Modulation Scale: 1 V	1	20
BER: 1.000E-05 Continu Insert Si		CCSDS: Block FEC: None Conv FEC: PCM Encoding: NRZL		Enable Airlink Data Rate: 9.999953		
		Channel CH0	▼ × Chi	annel CH1 ×		
СОМЗ			e E		: 43.2 C 💻	0
		Frequency: 2225 Power: -50 AWGN: 100 RF Signal		equency: 2225 \$700 Power: -50 \$700 AWGN: 100 \$700 RF 0 Signal 0		
		Coordinator		<b>~</b> ×		
			0			

Figure 51: RxAn GUI, Floating Tool Panel Group Example 2

	x Receiver Analyzer		- 1 - M/C - 1 - 11 - 1			- □ >
Data Genera	View Project Connect tor DG0	· · · · · · · · · · · · · · · · · · ·	Encoder ENC0	<b>-</b> ↓ ×	Modulator MOD0	<b>~</b> ņ
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Pattern: P Rate: 10 BER: 1.		• • •	Data Source: DG0 Diff Encoding: IRIG: CCSDS: Block FEC: None Conv FEC: PCM Encoding: NRZL		Data Source: ENCO Modulation: PCMFM Modulation Scale: 1 Enable Airlink Data Rate: 9.999953	
	Channel CH0	- ×	Channel CH1	□ × ▼ ×		
СОМЗ	Ξ	0	Ξ	0	SN: RA-064 CH0: 42.5 C CH1:	43.2 C 🔲 0
	Frequency: 2225 Power: 50 AWGN: 100 RF Signal		Frequency: 2225 Power: 50 AWGN: 100 RF Signal	8 8 8 8 8 8 8 8	Coordinator	

Figure 52: RxAn GUI, Floating Tool Panel Group Example 3

## 6.2.3.1.3 Auto Hide

Tool Panels can be set to automatically collapse to a tab against the edge of the Main Dock when not in use, as shown in Figure 53. This makes the Tool Panel easily accessible yet takes up very little room. This can result in extremely compact layouts.



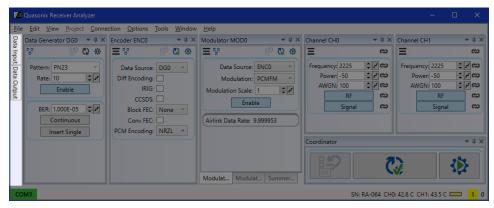


Figure 53: RxAn GUI, Auto Hide Tool Panel Example

Auto Hide Tool Panels can be docked to the left, right, top, and bottom of the Main Dock, as shown in Figure 54.

💯 Quasonix Receiver Analyzer			- 0	×
<u>File Edit View Project Connection</u> Data Generator DG0	<u>O</u> ptions <u>I</u> ools <u>W</u> indow <u>H</u> elp			
Modulator MODO • 4 × Data Source: ENCO • Modulation: PCMFM • Modulation Scale: 1 • Enable Airlink Data Rate: 10.000018 Coordinator	Channel CH0 • 4 × E • • • • • • • • • • • • • • • • • • •	Channel CH1		Encoder ENC0
СОМЗ	SN: RA-0	064 CH0: 42.8 C CH1	: 43.8 C 📖 👘	1 0

Figure 54: RxAn GUI, Multiple Auto Hide Tool Panel Examples

By clicking on, or hovering over, the tab of an Auto-Hide Tool Panel, it expands, making its contents accessible, as shown in Figure 55.

70	Quason	nix Rece	iver Analy	/zer					—		×
<u>F</u> ile	<u>E</u> dit	<u>V</u> iew	<u>P</u> roject	<u>C</u> onnection	<u>O</u> ptions	Tools	<u>W</u> indow	/ <u>H</u> elp			
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a Ge	°."				: ? (	2 (Q		0	Ξ		9
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Figure 55: RxAn GUI, Unhidden Expanded Tool Panels

## 6.2.3.1.4 Closed

A Tool Panel that is closed is not visible on the screen, either in the Main Dock or floating. It can be opened using the View menu and selecting the desired Tool Panel. Sometimes when a Tool Panel is shown on a complex display, it is difficult to see where the Tool Panel is located. Holding a Shift key down while selecting the Tool Panel in the View menu causes the Tool Panel to be opened floating in the center of the main screen.

## 6.2.3.2 Tool Panel Components

Each Tool Panel contains common components, as shown in Figure 56.

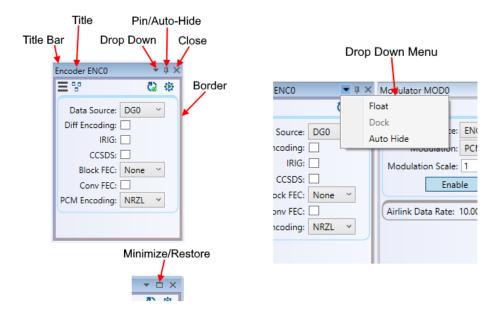


Figure 56: RxAn GUI, Common Tool Panel Components

### 6.2.3.2.1 Title Bar

The Title Bar of a Tool Panel moves a docked Tool Panel in the layout or a floating Tool Panel on the screen.

### 6.2.3.2.1.1 Title

The Title of the Tool Panel is the name of the module the Tool Panel controls.

### 6.2.3.2.1.2 Drop Down Arrow

The drop down Arrow icon opens the drop down menu.

### 6.2.3.2.1.2.1 Drop Down Menu

- The Float entry causes a Docked Tool Panel to become a floating Tool Panel. It is not available in a floating Tool Panel.
- The Dock entry causes a floating Tool Panel to become docked in the Main Dock. It is not available in a docked Tool Panel.
- The Auto Hide entry toggles a Docked Tool Panel between its normal docked state and the Auto Hide state.

### 6.2.3.2.1.3 Pin/Auto Hide

The Pin/Auto-Hide icon toggles a Docked Tool Panel between its normal docked state and the Auto Hide state.

### 6.2.3.2.1.4 Minimize/Restore

The Minimize/Restore icon maximizes a floating Tool Panel, or restores a maximized Tool Panel to its former state.

### 6.2.3.2.1.5 Close

The Close icon closes the Tool Panel in both the Docked and Floating state.

### 6.2.3.2.2 Unit of Measure Tooltips

Units of measure are presented as tooltips in the panels. For example, when hovering over Frequency in the RF Output panel, 'In MHz' displays. Similarly, hovering over AWGN in the RF Output panel, displays 'In dB Eb/N0'. Examples of tooltips in the RF Output CH1 panel are shown for Frequency, Power, and AWGN in Figure 57.



Figure 57: RA On-screen ToolTips Examples

### 6.2.3.2.3 Border

The Tool Panel Border resizes a floating Tool Panel. Hovering over the border changes the mouse cursor to a double headed arrow. The left/right and top/bottom borders allow changing the width/height respectively, and the four corners allow changing both the width and height, as shown in Figure 58.

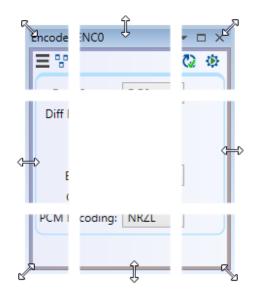


Figure 58: RxAn GUI, Tool Panel Border Resizing

### 6.2.3.3 Creating and Managing Layouts

RxAn comes with three layouts of varying complexity and capability. They can be used as the basis for custom layouts.

### 6.2.3.3.1 Adding a Tool Panel

Tool Panels can be opened and closed by using the View menu on the Menu Toolbar, as shown in Figure 59.

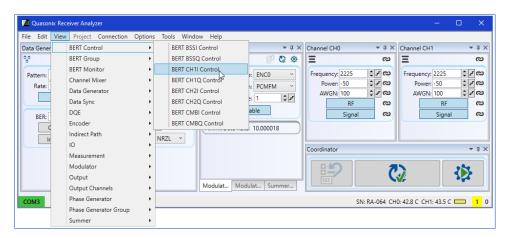


Figure 59: RxAn GUI, View Menu, Add a Tool Panel

The selected Tool Panel is added to the Main Dock, as shown in Figure 60.

🚺 Quasonix Receiver Analyzer				– 🗆 X
<u>File Edit View Project Con</u>	nection <u>O</u> ptions <u>T</u> ools <u>W</u> ine	dow <u>H</u> elp		
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😗 👘 🖗	Ξ 🕆 👘 🖓 🅸	三部 🔮 🕲 🕸	S E	9 E
Pattern: PN23 V Rate: 10 V Enable	Data Source: DGO v Diff Encoding: IRIG: CCSDS:	Data Source: ENCO Modulation: PCMFM Modulation Scale: 1 0 Modul Modul Summ 0 0	Frequency: 2225 7 0 Power: -50 7 0 AWGN: 100 7 0 RF 0 Signal 0	Frequency: 2225 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
Pattern: PN23 Time Limit: 1 Count Limit: 0 Gating: Continuous		✓ Error ✓	Coordinator	***
СОМЗ			SN: RA-064	4 CH0: 42.8 C CH1: 43.5 C 🥅 1 0

Figure 60: RxAn GUI, Tool Panel Added to Main Dock

The Tool Panel is generally added to the same location it was in when it was last closed. When there are many Tool Panels and Tool Panel Groups, it may be difficult to determine where the Tool Panel is located. Holding a Shift key when opening the Tool Panel will open it in a floating state, centered over the main RxAn window, as shown in Figure 61.

💋 Quasonix Receiver Analyzer					-	□ ×
File Edit View Project Cor	nnection Options Tools V	Vindow Help				
Data Generator DG0 • # ×	Encoder ENCO • 4	X Modulator MOD0	← # × Channel CH0     ⑦	<b>0</b> × ⊕ ×	Channel CH1	<del>ک</del> تا ×
Pattern: PN23 V Rate: 10 V Enable BER: 1.000E-05 V Continuous Insert Single	Data Source: DG0 Diff Encoding: IRIG: CCSDS: Block FEC: None Conv FEC: PCM Encoding: NRZL	BERT CH11 Control Pattern: PN23 Time Limit: 1 Count Limit: 0 Gating: Continuo Restart	ت ا × الا ک الا ک ال الا ک الا ک الا الا ک الا ک	25 C P Q Q Q C P P Q Q Q Q Q Q Q Q Q Q Q Q	Frequency 2225 Power, 50 AWGN: 100 RF Signal	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
		Modul Modul	Summ	(	2	
СОМЗ				SN: RA-064	CH0: 42.8 C CH1: 43.5 C	: 💶 📘 0

Figure 61: RxAn GUI, Open Tool Panel in Floating State

### 6.2.3.3.2 Moving Tool Panels

A Tool Panel can be moved by left clicking on, and holding, the Title Bar and dragging it to a new location. As the Tool Panel is moved, Docking Hints are displayed, as shown in Figure 62.

Eile         Edit         View         Project         Connection         Q           Data Generator DG0         • • • • ×         Encoder EN	ptions <u>lools W</u> indow <u>H</u> elp				
	C0 • 4 × Modulator MO	D0 🔤 • म × C	Channel CH0 🔹	′ ₽ × Channel CH1	<b>-</b> ↓ ×
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Pattern: PN23 V Data So	urce: DG0 v Data So	urce: ENC0		Frequency: 2225	≎r⊗
Rate: 10	ding: 🗌 Modula	ation: PCMFM >		Power: -50	⇔ r ≎
Enable	IRIG: Modulation		AWGN: 100	AWGN: 100	⇔ r ≎
	SDS:		RF	RF RF	3
■ JER: 1.000E-05 + Block	FEC: None V		Signal	Signal	
Continuous Conv	FEC:	000018			
Insert Single PCM Encod	ding: NRZL V				
	RT CH1I Control	- IX C	Coordinator		<b>-</b> ↓ ×
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	Pattern: PN23				242
	Time Limit: 1				
COM3	Count Limit: 0	Error ~	SN: R	A-064 CH0: 42.8 C CH1: 43.5 C	0 0
	Gating: Continuous	~			
	Restart Sti	op			

Figure 62: RxAn GUI, Tool Panel Docking Hints

Dragging the cursor with the Tool Panel over one of the Docking Hints displays a preview window that shows where the Tool Panel will be docked, as shown in Figure 63.

ata Generator DG0 🛛 🔻 🕂 🗙	Encoder ENC0 🗢 🖛 🔅	× Modulator MOD0 📃 🕶 早 3	× Channel CH0	- 4 × Cŀ	hannel CH1	<b>т</b> џ
20 2 40 Pattern: PN23 Rate: 10 Enable	Data Source: DG0 V Diff Encoding: CCSDS: CCS	Data Source: ENCO Modulation: PCMFM Modulation	Frequency: 2225 Power: -50	6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	Frequency: 2225 Power: -50 AWGN: 100 RF Signal	
		Arrink Data 10,00018				
СОМЗ		Pattern: PN23 Time Limit: 1 Count Limit: 0		SN: RA-064 CH	10: 43.0 C CH1: 43.8 C	□ <mark>1</mark>

Figure 63: RxAn GUI, Tool Panel Docking Preview

Releasing the mouse button causes the Tool Panel to dock in that location, as shown in Figure 64.

📶 Quasonix Receiver Analyzer				-	□ ×
<u>File Edit View Project Co</u>	nnection <u>O</u> ptions <u>T</u> ools <u>W</u> in	dow <u>H</u> elp			
Data Generator DG0 🛛 🔻 🕮 🗙	Encoder ENC0 🛛 🔻 🕸 🗙	Modulator MOD0 🛛 🔻 🕂 🗙	Channel CH0	▼ # × Channel CH1	<b>▼</b> ↓ ×
😌 👘	Ξ 😗 👘 😨 🕸	Ξ 😗 👘 😨 🕸	Ξ	8 II	8
Pattern: PN23 ~ Rate: 10 +	Data Source: DG0  V Diff Encoding:	Data Source: ENCO Modulation: PCMFM	Frequency: 2225 Power: -50		09 I \$
Enable		Modulation Scale: 1	Coordinator		▲ ù ×
BERT CH1I Control					<b>▼</b> ‡ X
≡♡					ii? 🗘 🕸
Pattern: PN23 Time Limit: 1					~
Count Limit: 0 Gating: Continuous					Error ~
COM3				SN: RA-064 CH0: 43.0 C CH1: 43.8	

Figure 64: RxAn GUI, Tool Panel Docked

Left clicking with the mouse on the Title Bar of the docked Tool Panel and dragging it out of the Main Dock, leaves it floating, as shown in Figure 65. The drop down menu options Float and Dock perform the same function.

File Edit View Project Con						
Data Generator DG0 • 4 × P Pattern: PN23 · Rate: 10 • 7 Enable	Encoder ENCO + 4 × Bata Source: DGO + Diff Encoding: IRIG: IRI	Data Source	PCMFM	Channel CH0 Frequency: 2225 Power: -50 AWGN: 100	× ↓ × 0 0 1 ÷ 0 1 ÷	Frequency: 2225 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2
R: 1.000E-05 Continuous	CCSDS: Block FEC: None Conv FEC: PCM Encoding: NRZL	Airlink Data	BERT CH1I Con	RF Signal	6 6	RF C
СОМЗ		Modul Modul.	Pattern:	PN23		2 CH0: 43.0 C CH1: 43.8 C 🔲 1
			Count Limit: Gating: Res	Continuous	Error ~	1

Figure 65: RxAn GUI, Tool Panel Floating

The four Docking Hints at the edges of the Main Dock place Tool Panels along the entire edge of the Main Dock, as shown in Figure 66.

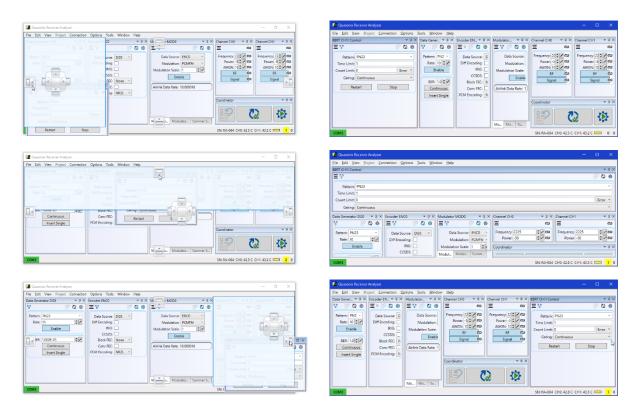


Figure 66: RxAn GUI, Tool Panels Docked on Entire Edge



The Tool Panel Docking Hint, shown in Figure 67, allows docking a Tool Panel to the top, right, left, bottom, or with the Tool Panel the Docking Hint is covering.



Figure 67: RxAn GUI, Tool Panels Docking Hint

A Tool Panel can be docked to the left, top, right, bottom, (shown in Figure 68) or in a Tabbed Panel Group, as shown in Figure 69.

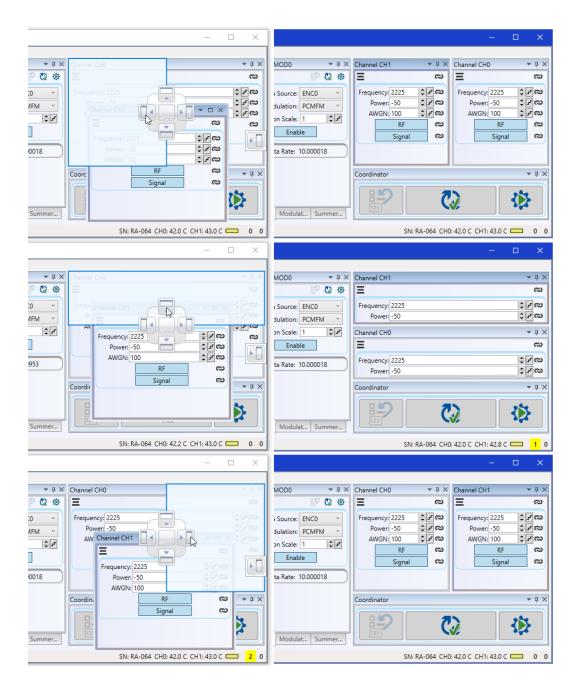


Figure 68: RxAn GUI, Tool Panels Docked in Different Locations

		- 🗆	×		-	□ ×
- ↓ × 2 😧 🏟	Channel CH0	~	ې بر ع	- ∓ × ? 🗘 🏟	Channel CH1	× ¤ ×
FM ~ FM ~ FM ~	Frequency: 2225 Power: -50 AW Channel CH1 Frequency: 2225 Power: -50 AWSN: 100		2	0 ~ IFM ~ D018	Frequency: 2225 Power: -50 AWGN: 100 Sighur Channel CH1 Channel CH0	0 0 0 0 0
Summer	Coordin Signal SN: RA-064 CH0: 41.5 C CH	8 9 X		Summer	Coordinator	• # ×

Figure 69: RxAn GUI, Tool Panels Docked in a Tabbed Panel Group

The order of Tool Panels in a Tool Panel Group can be rearranged by left clicking on the Tool Panel tab, then dragging it left or right, as shown in Figure 70.

	-	- o x	- 1	x c
	Channel CH1  Frequency: 2225 Power: -50 AWGN: 100  RF Signal  Channel CH1 Channel CH0	a a a a a a a a	Channel CH1 Channel CH1 Frequency: 2225 Power: -50 AWGN: 100 RF Signal Channel CH0 Channel CH1	
Summer	Coordinator	2.2 C 0 0	Summer	• # ×

Figure 70: RxAn GUI, Tool Panels Reordering a Tabbed Panel Group

A Tabbed Panel Group can also become a Floating Panel Group, as shown in Figure. Floating Panel Groups support all of the docking combinations as the Main Dock.

- 🗆 X	- 🗆 X
dow Help	dow Help
↓ × Modulator MOD0 ▼ ↓ × Coordinator ▼ ↓ ×	↓× Modulator MOD0 ▼
Image: Set of the set of	B     58     Comparing the second sec

Figure 71: RxAn GUI, Tool Panels Tabbed Panel Group to Floating Panel Group

## 6.2.3.3.3 Sizing Tool Panels

Docked Tool Panels can be resized by using the Resizing Bars between the Tool Panels, as shown in Figure 72. Moving the mouse over a Resizing Bar displays the double headed arrow.

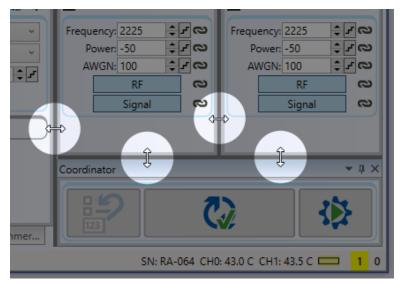


Figure 72: RxAn GUI, Sizing Tool Panels

Left clicking on the Resizing Bar displays a preview of where the Resizing Bar will have an effect, as shown in Figure 73.



Figure 73: RxAn GUI, Resizing Bar Locations

Dragging the Resizing Bar causes all of the Tool Panels on either side to be resized, as shown in Figure 75 and Figure 75.

		—	□ ×			-	×
	Channel CH0 ▼ 0 2 E Frequency: 2225 ↓ r Power: -50 ↓ r AWGN: 100 ↓ r Signal 0	Channel CH1		Channe V 4 X Frequency: 22 Power: -5 AWGN: 10 RF Signal	Channel CH1 Frequency: 2225 Power: -50 AWGN: 100 RI Sign		3
	Coordinator		- # ×	Coordinator			ųΧ
			₩	123			
SN: RA-064 CH0: 43.0 C CH1: 43.8 C 🔲 1 0			SN: RA-064 CH0: 43.0 C CH1: 43.8 C 🔲 0 0				

Figure 74: RxAn GUI, Resized Tool Panels and Adjacent Panels

– 🗆 X	- 0
Channel CH0 T X Channel CH1 T X	X Channel CH0
Channel CH0 ▼ ↓ × Channel CH1 ▼ ↓ ×	Channel CH0 • 4 × Channel CH1
Frequency:         2225         Image: Power:         Power:         -50         Image: Power:	Frequency:         2225         ↓ I          ▶         Frequency:         2225         ↓           Power:         -50         ↓ I          ♥         Power:         -50         ↓
AWGN: 100 \$7 8 AWGN: 100 \$7 8	AWGN: 100 \$1 @ AWGN: 100 \$
RF © RF ©	RF လ RF
Signal 🝋 Signal 🝋	Signal လ Signal
Coordinator V 4 X	
	Coordinator
123	
SN: RA-064 CH0: 43.0 C CH1: 43.8 C 🔲 0 0	SN: RA-064 CH0: 43.2 C CH1: 44.0 C
– 🗆 X	- 0
Channel CH0 • # × Channel CH1 • # ×	▼ # × Channel CH0 ▼ # × Channel CH1
Channel CH0 ▼ ↓ × Channel CH1 ▼ ↓ ×	
Frequency:         2225 <b>↓ ↓ ∧ ↓ ∧ ↓ ∧ ↓ ∧ ↓ ∧ ↓ ∧ ↓</b> <t< td=""><td>Frequency:         2225         I         I         Frequency:         2225         ↓           Power:         -50         ↓         I         Power:         -50         ↓</td></t<>	Frequency:         2225         I         I         Frequency:         2225         ↓           Power:         -50         ↓         I         Power:         -50         ↓
AWGN: 100 \$ 7 8 AWGN: 100 \$ 7 8	AWGN: 100 \$ 7 0 AWGN: 100 \$
	RF 🕲 RF
Signal 🔉 Signal 🔊	Signal လ Signal
<b>↔</b>	
Coordinator 👻 🕂 🗙	Coordinator
	er S
SN: RA-064 CH0: 43.0 C CH1: 43.8 C 🛄 0 0	SN: RA-064 CH0: 43.0 C CH1: 43.8 C

Figure 75: RxAn GUI, Resizing Bar Locations

## 6.2.3.3.4 Example Custom Layout

Custom layouts may serve several purposes, including simplifying complex or unusual functionality, or visually mirroring signal flow to more closely match the functional block diagram. Figure 76 depicts a combination of these.

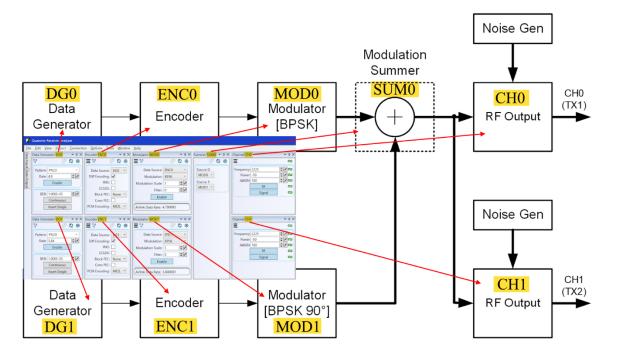


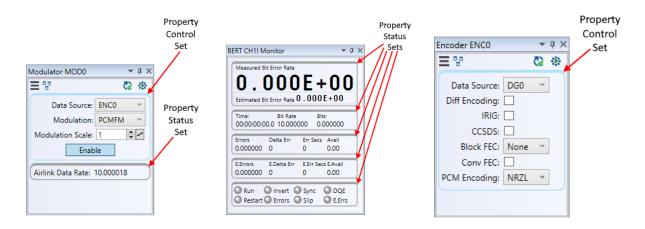
Figure 76: Example Basic Data Flow (AQPSK) with Associated RxAn Panels

## 6.2.3.4 Common Tool Panel Functionality

All Tool Panels have similar sets of functionality. Not all Tool Panels have the same functionality.

### 6.2.3.4.1 Property Control Sets

Sets of property controls are surrounded by a light blue border. Sets of property status displays are surrounded by a light gray border. Both types are shown in Figure 77.







### 6.2.3.4.2 Numeric Up/Down (NUD) Controls

Most numeric property fields have Numeric Up/Down controls (NUDs). Numeric Up/Down controls have a Text Field, Up/Down Buttons, and a Step Size button, as shown in Figure 78.



Figure 78: RxAn GUI, Numeric Up/Down Controls

#### 6.2.3.4.2.1 Text Field

A value can be entered directly into a Text Field. Hovering the mouse cursor over the numeric Text Field displays a Tool Tip that shows the units of the property being modified, as shown in Figure 79.

Channel CH0	<b>▼</b> ‡ ×
Ξ	9
Frequency: 2225	50
Power: -50	50
AWGN: 100 In MI	Hz ல
RF	୍ଷ
Signal	େ

Figure 79: RxAn GUI, Tool Panel Text Field

If a value is entered that is out of range, an error message displays while the cursor is in the Text Field of the Property Control. Moving the cursor to another Property Control leaves a red border around the Property Control to indicate the value is invalid, as shown in Figure 80. Returning the cursor to the Property Control displays the error message again.

# 3<sup>rd</sup> Generation Receiver Analyzer

Channel CH0 🛛 🔻 🕂 🗙	Channel CH1 🛛 🔻 🕂 🗙	Channel CH0 🛛 🔻 🕂 🗙	Channel CH1 🛛 🔻 🕂 🗙
Carrier frequency	minimum 200.0000 MHz	9 I	S 2
Frequency: 199 🗘 🕫 🔊	Frequency: 2225 🗘 🕫	Frequency: 199 🗘 🞜 ல	Frequency: 2225 🗘 🖍 ல
Power: -50 🗘 🞜 🝽	Power: -50 🗘 🗲 🕶 🝽	Power: -50 🗦 🖍 😋	Power: -50 🗘 🗗 😋
AWGN: 100 🗘 🖍 🝋			
RF လ	RF လ	RF 🝋	RF လ
Signal 😡	Signal 😡	Signal 🔊	Signal 🔊

#### Figure 80: RxAn GUI, Invalid/Out of Range Displays

## 6.2.3.4.2.2 Step Up/Down

The value in the text field can be stepped up or down by using the Up/Down buttons, the up and down keyboard arrow keys, or the mouse scroll wheel.

### 6.2.3.4.2.3 Step Size

The Step Size button displays the Step Size Editor, as shown in Figure 81. This sets the amount the Control Property will change by when the Numeric Up/Down control is stepped. While there are no hard limits on the step value, step values that exceed the range of the Control Property being stepped are not practical.

<b>▼</b> ‡ ×	Chann	el CH1	▼ ‡ ×
0	Ξ		9
5 CO	Frequ	uency: 2225 💲	S CO
Step:	0.5	MHz ~	ŝ
		DE	6
8			8
	<u> </u>		
	ව ව Step:	RO Frequencies	Frequency:    2225    Step:    0.5    MHz

Figure 81: RxAn GUI, Step Size Editor

The Step Size Editor will have zero or more Drop Down menus, as shown in Figure 82. These Drop Down menus set the units for the step size, or the step type.



# 3<sup>rd</sup> Generation Receiver Analyzer

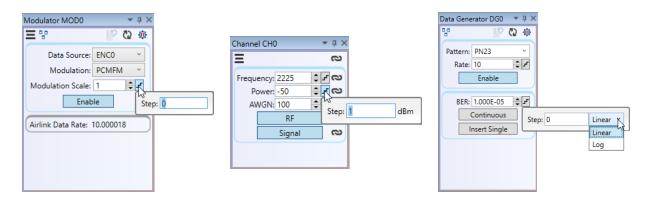


Figure 82: RxAn GUI, Step Size Editor Drop Down Menus

## 6.2.3.4.3 Changed Property Indication

When a user makes a change to a property (Check Box, Enable Button, Numeric Field, etc.), that change is not immediately applied to the RA hardware. It is considered a pending change. When a property has a pending change, it has a yellow background for text fields, or a yellow border for check boxes and toggle buttons, as shown in Figure 83.

Data Generator DG0 🛛 🔻 🕂 🗙	Data Generator DG0 🛛 🔻 🕂 🗙	Data Generator DG0 🛛 👻 🕂 🗙
የ 🔮 🐌 🕸	😵 🗳 🖓	😌 👘 🖗
Pattern: PN23 ~ Rate: 10 + r Enable	Pattern: PN15 ~ Rate: 1 +	Pattern: PN15 ~ Rate: 10 <b>‡</b>
BER: 1.000E-05	BER: 1.000E-05	BER: 1.000E-05

Figure 83: RxAn GUI, Changed Property Indicators

## 6.2.3.4.4 Pending Changes

When a user makes a change to a property (check box, Enable button, numeric field, etc.), that change is not immediately applied to the RA hardware. It is considered a 'pending' change. A property has a pending change when it has a yellow background (for text fields) or a yellow border (check boxes and toggle buttons). In the upper right-hand corner of each Tool Panel is a set of three icons Revert, Apply, and Auto-Apply, as shown in Figure 84.

For global pending changes control, refer to the Coordinator in section 6.4.10.

I	Revert	Appl	y /	Auto-Apply
				/
				/
BERT CH1I Contro	ol 🔪	-	ηX	
<b>≡</b> %		ି 🖏	10	
Pattern: Pl	N15		~	
Time Limit: 0				
Count Limit: 0		Bit	~	
Gating: C	ontinuous		~	
Restart	S	itop		

Figure 84: RxAn GUI, Change Icons

### 6.2.3.4.4.1 Revert

The Revert icon is enabled if there are pending changes in the Tool Panel, as shown in Figure 85. Clicking on it reverts any changes in the Tool Panel to the state of the RA hardware.

BERT CH1I Control	≁ û ×
<b>= *</b>	R (1) 🔅
Pattern: PN15	Revert changes
Time Limit: 5	
Count Limit: 0	Bit ~
Gating: Continuou	s ~
Restart	Stop

Figure 85: RxAn GUI, Revert Icon

### 6.2.3.4.4.2 Apply

The Apply icon has a red exclamation point to indicate there are pending changes that need to be applied, as shown in Figure 86. Clicking it applies the changes in the Tool Panel to the RA hardware. The Apply icon has a green checkmark when there are no pending changes. This only applies to the pending changes in the Tool Panel and not others.

## 3<sup>rd</sup> Generation Receiver Analyzer

BERT CH1I Con	trol	- ų ×	
≡ %	0 12	200	
Pattern:	PN15	Apply	changes
Time Limit:	5		
Count Limit:	0	Bit ~	
Gating:	Continuous	Ŷ	
Restar	t St	top	

Figure 86: RxAn GUI, Apply Icon

Circular Arrows/Apply Button – Used to apply changes made in the panel where the icon resides

• When the Apply button contains a green check mark, there are no pending changes to apply, as shown in Figure 87.



Figure 87: Apply Button with No Changes Pending

• When the Apply button contains a red exclamation point, as shown in Figure 88, there are pending changes. Clicking on this icon applies any pending changes in that panel only. Pending changes in other panels are not applied.

If there are pending changes, a Revert button displays to the left of the Apply button, also shown in Figure 88. Clicking on the Revert button, restores the previous settings to that panel only.



Figure 88: Apply Button with Pending Changes

#### 6.2.3.4.4.3 Auto-Apply

Clicking on the Auto-Apply icon toggles the Auto-Apply state of the Tool Panel On and Off.

A green arrow in the Auto-Apply icon indicates that Auto-Apply is Off. A red pause symbol indicates that Auto-Apply is On, as shown in Figure 89. When Auto-Apply is On, the Apply icon is disabled.

## 3<sup>rd</sup> Generation Receiver Analyzer

BERT CH1I Contro	ol		<b>v</b> 1	φ×
≡ 🕫		<b>P</b> (	2	<b>R</b>
Pattern: P	N15			Auto apply
Time Limit: 5				
Count Limit: 0		Bit		~
Gating: C	ontinuou	s		~
Restart		Stop		

Figure 89: RxAn GUI, Auto-Apply Icon

**Gear/Auto Apply Button** – Toggle button; Clicking on this button puts the panel in Auto Apply mode. Clicking on it again, takes it out of Auto Apply mode. Any changes made to properties are automatically applied to the RA hardware (after a short delay).

• When a green arrow displays in the gear, as shown in Figure 90, it is **not** in Auto Apply mode.

≡ %	© (*)
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Figure 90: Gear with Green Arrow—Not in Auto Apply

• When a red 'pause' symbol displays in the gear, as shown in Figure 91, Auto Apply mode is enabled.

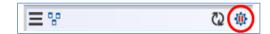


Figure	91 · Gear	with Red	Pause S	vmbol_Auto		Iode Enabled
Iguie	JI. Gear	with Neu	rause o	yiiiboi—Auto	πρριγι	

• When Auto Apply mode is enabled, the Apply button is greyed out and is disabled, as shown in Figure 92.

≡ 99	(D) (D)

Figure 92: Apply Button Grey and Disabled

### 6.2.3.4.5 Hamburger Menu

Panels with a 'Hamburger menu" (three lines), shown in Figure 93, display a secondary menu providing configuration settings or the ability to perform other actions, as shown in Figure 94. These settings are generally less frequently used and/or advanced properties.

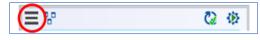


Figure 93: Hamburger Icon

BERT CH1I Control	<b>→</b> ‡ ×
<b>3</b> ,8	📰 😳 🌸
Errored Seconds Threshold:	1.00E-5
Clock Slip Ref:	DG0 ~
Operation Group:	Group 0 ~
Restart	Stop

Figure 94: RxAn GUI, Hamburger Icon

## 6.2.3.4.6 Three Panel Control Group Icon

3

Three connected or unconnected panels indicate modules that are to be configured together; Control Groups are a construct of the RxAn software that is used to allow multiple instances of the same module type to be configured the same. Figure 95 shows unconnected boxes indicating that this panel is not grouped. Figure 96 shows connected boxes and control group number 0. This indicates the panel is in control group 0, and will be configured the same as other panels in control group 0. Control groups are discussed in detail in section 6.4.1.



Figure 96: Three Panels Icon with Connected Boxes and Control Group Number

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# 6.3 RxAn Detailed Menu Descriptions

The top line of the RxAn GUI window includes a Menu Bar which provides access to common functions and keyboard shortcuts to the functions.

### 6.3.1 Menu Bar

RxAn GUI has a variety of menus available on the top of the screen: File, Edit, View, Project, Connection, Options, Tools, Window, and Help. These menus provide some common functions and keyboard shortcuts to the functions.

#### 6.3.1.1 Connection Menu

The Connection menu, shown in Figure 97, is used to refresh the status of the connected RA, scan, locate a communications port, display communication statistics, and set a monitor rate. Communication status information is also provided on the bottom left and right of the RxAn, as shown in Figure 99.

Connection		Options	Tools
	Refresh		
	Ports		•
	Statistics		•
	Monitor Rate		

Figure 97: Main Menu Toolbar, Connection Menu

• Refresh – This pulls all of the available information from the RA hardware and updates the RxAn GUI. This happens when the RxAn.exe software is started, when the COM port is connected (or reconnected), or when a manual refresh is performed.

A user may want/need to do this because there is no blinking yellow communication status in the right side of the status bar at the bottom of the screen, there are unexplained pending changes, or the current displayed configuration isn't making sense.

• Ports - Use the drop down menu to scan or select a communications port, as shown in Figure 98.

Connection Options	Tools	Window Help
Refresh		
Ports	×	Scan Ports
Statistics	×	COM1
Monitor Rate		COM10
interinter nates.		COM11
		COM12
		COM13
		COM14
		COM15
		COM3
		COM8
		COM9

Figure 98: Connection Menu, Ports Drop Down Menu



During the first RxAn.exe run after installation, it automatically connects to "found" RA hardware.

Each time RxAn starts after that, it attempts to reconnect to RA hardware using the port used the last time the software was run.

• Scan Ports – Examines all of the known communication ports on the computer and populates the list; the known ports are scanned when RxAn.exe is started or when the user performs a manual scan. The COM port name displays in bold text if an RA responds on that COM port.

A user may want to rescan for ports if the RA appears to be connected to a port (COM port status on left side of the status bar at the bottom of the screen is green, as shown in Figure 99), but it doesn't seem to be communicating with the RA (no blinking yellow communication status in the right side of the status bar at the bottom).

#### **COM Port Status**

Green indicates the RA has that port open. It doesn't mean there is RA hardware on the other end of the port. If it is red, it means the RA cannot connect to the port because it is unavailable, or the user has closed the port. Clicking on the COM port status will close/open the port.

When the port is opened, a refresh is performed automatically. The refresh is intended to occur when there is a possibility that the RA hardware configuration was changed outside the knowledge of the RxAn software. For example, a user can close the port, connect to the RA hardware via terminal, make configuration changes, open the port, and those changes will be reflected in the software.

DQE CH1I DQE CH2I DQE CMBI	BERT BERT BERT	
СОМЗ		SN: RA-064 CH0: 42.8 C CH1: 43.5 C 🔲 0 0

Figure 99: Status Bar with COM Port and Channel Status

COM ports may have the following states:

- If the port name is grey, it is unavailable (in use somewhere else).
- If it is bold, it indicates RA hardware has responded to a version query indicating RA hardware is attached to the port, as shown in Figure 98.
- If it is neither, the port is available but doesn't appear to have RA hardware attached to it.
- If it is checked, it is the current port the RA is using to communicate with the RA hardware.

It is possible to have a check beside a port that is not bold because either the user selected a port that did not appear to have RA hardware attached to it, or the port did not appear to have RA hardware attached to it during the port scan. This can happen if the port is in use when the RxAN.exe is started and cannot be scanned.

Clicking on a checked port will close/open the port.

#### **Communication Status**

The last two characters/boxes on the right end of the status bar indicate communication status, as shown in Figure 100. If the box is yellow and has a number in it, it means there are messages queued for processing. The number of messages queued is the number displayed.

The left box of the pair indicates messages coming *from* the RA hardware and the right box indicates messages going *to* the RA hardware. The RA hardware communicates to the RA software autonomously, so the left box should always be blinking. If it's not, a Connection > Refresh may restart it.

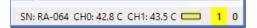


Figure 100: Status Bar with Serial Number, Channel Status, and Communication Status

• Statistics – This option is a diagnostic tool primarily used by Quasonix to access communication statistics sent between the RA hardware and the RxAn GUI. Use the drop down menu to select statistics for the RxAn or the target RA, as shown in Figure 101.

Conr	onnection Options		Tools	Window	Help
	Refresh	ı			
	Ports		•		
	Statisti	cs	•	RxAn	1
	Monito	or Rate	Ļ	RA	

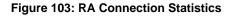
Figure 101: Connection Menu, Statistics Drop Down Menu

- RxAn Statistics the RxAn maintains, as shown in Figure 102, Connection Statistics window
  - Reset Buttons Reset specific statistics
- RA Statistics the targeted RA hardware maintains and can send to the RxAn for display, as shown in Figure 103, Connection Statistics RA window
  - Rate Sets the rate the RA hardware reports communication statistics
  - Run Button Enables repetitive reporting of those statistics at the specified rate
  - Refresh Button Does a single poll for the statistics (when the repetitive reporting is turned off)
  - Reset Button Resets the accumulated statistics in the RA hardware

	Count	Count/s	
RA Bad CRC:	0	0.000	Reset
RA Short Frame:	0	0.000	Reset
RA Retry:	0	0.000	Reset
RA Rx Overrun:	0	0.000	Reset
Host Transaction Timeout:	0	0.000	Reset
Host Bad CRC:	0	0.000	Reset
Host Short Frame:	0	0.000	Reset
Host Discard:	0	0.000	Reset
Host Canceled:	0	0.000	Reset
Tx Bytes:	283635	45.296	Reset
Rx Bytes:	23614682	3771.249	Reset
Host Transaction Ok:	491	0.078	Reset
RA Transaction Ok:	33322	5.323	Reset
Total Transaction Ok:	33813	5.400	Reset
	Reset All		

- 🗆 X								
0 0 0								
0 106 0 0 22584								
0								
Rate: 100 Run Refresh Reset								

Figure 102: Host Connection Statistics



Monitor Rate – Use the Connection drop down menu to select Monitor Rate, as shown in Figure 104. The
Monitor Rate window, shown in Figure 105, sets the delay between reports from the RA hardware to the
RxAn GUI. It defaults to 100 ms. There should be no reason to change this unless the PC that RxAn is
running on cannot keep up with the incoming traffic from the RA hardware.

Con	nection	Options	Tools
	Refresh	ı	
	Ports	•	
	Statisti	•	
	Monito		

Figure 104: Connection, Monitor Rate...

Monitor Rate						
Monitor Rate:	100					
Cancel	Ok					

Figure 105: Monitor Rate Selection

### 6.3.1.2 Project Menu

The Project Menu in the Toolbar lets the user associate multiple files, such as window layouts, measurement scripts, notes, etc., with a Project.

The Project drop down menu consists of four options: Add New, Add Existing, Add Current, and Remove, as shown in Figure 106. Other files may be associated with a project. These files can be measurements (.rms), measurement results (.rmr), notes (.txt), and window layout files (.layout), as shown in Figure 107. This is a way to associate files with a project, so the user has quick access to them.

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- Add New Creates new measurement or notes file
- Add Existing Adds an existing measurement or notes file to the project for easy access
- Add Current If there is a file (.rmr or .txt) currently opened in an editor, it may be added to the project.
- Remove Removes an associated file from the project; the file is not deleted

Proj	ect Connection	Options	Tools	Window	Help								
	Add New	•	Meas	surement	•		Proj	ect Connection	Optio	ns	Tools Window	Help	
								Add New	•		Measurement	•	RxAn Measurement
	Add Existing		Note	S	•			Add Existing		63	Notes	•	
	Add Current							Add Current					
								Remove					
	Remove												
						J							

Figure 106: Project, Add New Drop Down Menu

Figure 107: Add New, RxAn Measurement Selection

The Project Menu is greyed out if no project is loaded.



Figure 108: Project Greyed Out

#### 6.3.1.2.1 Project Files

Project files contain all of the RA settings in effect at the time the project file was saved, including the RxAn GUI layout. This is particularly useful for establishing a known-good repeatable configuration for measuring or to leave a "breadcrumb" during troubleshooting or analysis. Certain state information, such as what measurement was most recently loaded in the Measurement panel, is not saved. Since layouts can also be saved separately, it is easy to load a project and then to load an alternate layout if desired. There is no practical limit to the number of projects that may be saved for later use.

The provided projects contain some settings that are required for use with the provided measurement scripts. It is strongly advised that these projects, or projects derived from them, be used when running these measurements. **These baseline projects should never be modified.** 

To access a saved project, go to File>Open Project, and select from one of the available project files, as shown in Figure 109. The specified project displays in the RxAn GUI, as shown in Figure 110.

📶 Open			×
← → • ↑ <mark>]</mark> « 3_0_10_2023-03-28_1	127 > Resources > Projects	<ul><li>・ ひ / Search Project</li></ul>	cts
Organize   New folder		* *	- 🔳 🕜
🔚 Contacts	^ Name	Date modified	Туре
o Creative Cloud Files	Basic.rxan	3/2/2023 12:12 PM	RXAN File
E. Desktop	BasicSTC.rxan	3/2/2023 12:12 PM	RXAN File
Documents	Complete.rxan	3/2/2023 12:12 PM	RXAN File
Downloads	CompleteSTC.rxan	3/2/2023 12:12 PM	RXAN File
🛼 Favorites	~ <		
File name:		<ul> <li>All Files (*.rxan;*.xr</li> </ul>	nl) ~
		Open	Cancel

Figure 109: File>Open Project, Example Available Project List

A few project files are provided as possible starting points. Basic.rxan and Complete.rxan provide a single modulated signal (PCMFM at 10 Mb/s to start) and three BERTs configured for measuring a dual-channel receiver with combiner. The only difference between these projects is the layout: Basic.rxan uses Basic.layout, which provides access to all common RA functions. Complete.rxan uses Complete.layout, which adds multipath capability. For additional information about project layouts, refer to section 10, Appendix A.

💋 Quasonix Receiver Analyzer				– 🗆 X
<u>File Edit View Project Connection Options Tools W</u> indow	<u>H</u> elp			
Measurement • 4 × Data Generator DG0 •	₽ × Encoder ENC0 ▼ ₽ ×	Modulator MOD0 🛛 👻 A 🗙	Channel CH0 👻 🕂 🖓	< Channel CH1
Measurement • 0 × Data Generator DG0 • = (1) 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	🕸 🗄 🖧 🐁 🖏 🔅	<b>Ξ 😗</b> 🔮 🕲 🕸	9 1	9 E
None     Pattern:     PAU       Start:     Enable     Rate:     10       Enable     Start:     Enable       BR:     1.000E-05     Complete       Complete     Continuous     Insert Single       DQE CH11     P ×       Frame Length: Auto Detect Frame Counts     Pattern:     PN23	Stop         Frores         Deta Err         E           0.000000         0         0           EErrors         E.Deta Err         E           0.000153         0         0           Run         Silp         Silp           Netrat Council         EErrs         0           Frons         Silp         Silp           Signs         Signs         Silp	782E-13         Estimated Bit Error Ra           Bits         1.56e+08           1r5ce + Avail         Time: Bit R           1.00         Errors Delta Er           0.000000         0           Errors EDelta I         Elerors EDelta I		153 0 0 1.00 n Slip start DQE etc EErrs ors
COMB			SN: RA-064 C	HU: 43.2 C CH I: 44.0 C L I 0

Figure 110: Example Basic.rxan Settings

### 6.3.1.3 Options Menu

The Options menu, shown in Figure 111, currently provides one selection, Output > Carrier Suppression. Checked is On, and not checked is Off.

Options	Tools	Win	dow	Help
Οι	utput	×	~	Carrier Suppression

Figure 111: Options Output Drop Down Menu, Carrier Suppression Checked

### 6.3.1.3.1 Carrier Suppression

Carrier leakage is an undesired sine wave embedded within the transmitted signal at the synthesized RF carrier frequency. It is caused by small imperfections in the analog RF modulation process. Its amplitude is generally around 30 dB below the modulated signal, but it is present even when the signal is turned off (in other words, set to an amplitude of 0). It acts as a form of interference and is therefore detrimental to generating an ideal modulated RF output.

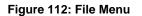
The RA mitigates carrier leakage by digitally modulating the desired signal with a center frequency offset by 20 MHz from the carrier frequency. The entire RF output is then counter-offset by the same amount, so the desired signal is centered at the user-selected frequency and the carrier leakage appears 20 MHz away. Using this method, the carrier leakage falls outside the band of interest for the desired signal.

**Note:** This method is not suitable for over-the-air transmission, as the carrier leakage and other image products appear as interfering signals in channels adjacent to the desired signal. In this case, carrier suppression should be disabled (not checked).

#### 6.3.1.4 File Menu

The File menu, shown in Figure 112, provides the ability to create, open, and save RA projects. Different file types may be created, as shown in Figure 113: Measurement (.rms), Notes (.txt), Measurement results (.rmr), and the other related files may be viewed using the File > Open option, then selecting the desired file to open.

File Edit View Project Cor	File	Edit	View	Project	Connec	tion	Options	Tools
New Project Open Project Save Project Save Project as		Open Save Save	Project. Project Project Project	t				
Open Save Save as Close Exit		New Open Save Save Close Exit	as			Me Not	asurement tes	*







### 6.3.1.5 Edit Menu

The Edit menu, shown in Figure 114, is used to cut, copy, or paste content in all editable fields. It also includes the ability to undo an action. For example, if a user has the Channel CH0 panel open, it is possible to select text from the Frequency field, then cut, copy, or paste, using the Edit menu options or the associated shortcut keys.

Edit	View	Project	Conr
	Cut	Ctrl+	х
	Сору	Ctrl+C	
	Paste	Ctrl+	V
	Undo	Ctrl+	Z

Figure 114: Edit Menu

#### 6.3.1.6 View Menu

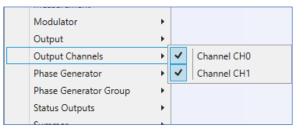
The View menu provides access to all of the configuration and status panels, as shown in Figure 115. Each of the options provides access to a quick selection menu. Figure 116 shows the Output Channel drop down menu options. In Figure 117, CH0 and CH1 are both selected, as indicated by the checkmarks. This causes the Channel CH0 and Channel CH1 configuration panels to display, as shown in Figure 118 (and described in detail later in this manual).

View	Project C	onnection	Ontions	1	View	Project	Connection	Options	Tools	Window	Help
VICTV	BERT Control		options			BERT Control		•			
		·				BERT Grou	ID.	•			
	BERT Group					BERT Mor	·				
	BERT Monito		•								
	Channel Mixe	er	•			Channel N	Aixer	•			
	Data Generat	tor	•			Data Gen	erator	•			
	Data Sync		•			Data Sync		•			
	DQE					DQE		•			
	Encoder		+			Encoder		•			
	Indirect Path		•			Indirect Path		•			
	IO		•			IO		•			
	Measuremen	nt	•			Measuren	nent	•			
	Modulator		•			Modulato	r	•			
	Output		•			Output		•			
	Output Chan	inels	•			Output Channels		+	Cł	nannel CH0	
	Phase Genera	ator	•			Phase Generator		•	Cł	nannel CH1	
	Phase Genera	ator Group	•		Phase Generator Group		• ]				
	Status Outpu	ıts	•		Status Outputs		•				
	Summer		•			Summer		•			

Figure 115: View Menu

Figure 116: View > Output Channels Drop Down Menu

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Channel CH0	<b>▼</b> ‡ ×	Channel CH1	- † ×
Ξ	0	Ξ	0
	61 61	Frequency: 2225 Power: -50	60 L \$
AWGN: 100	S ⊳ 1	AWGN: 100	≎ <b>∙</b> ≎
RF	0	RF	0
Signal	୍ଷ	Signal	୍ଷ

Figure 118: Channel CH0 and Channel CH1 Configuration Screens

#### 6.3.1.7 Tools Menu

The Tools menu is shown in Figure 119. There are six Tools selections: RA Configuration, Firmware, Calibration, Properties, Logs, and API Server. An example of the RA Configuration drop down menu is shown in Figure 120.

Tools Window Help		Tools Window Help		
RA Configuration	•	RA Configuration	•	Reset
Firmware	•	Firmware	•	Save
Calibration	•	Calibration	•	Load
Properties	•	Properties		1
Logs	•			
API Server	•	Logs		

Figure 119: Tools Menu

Figure 120: Tools > RA Configuration Drop Down Menu

- RA Configuration These options pertain to the settings stored in the RA hardware; available options are Reset, Save, and Load
  - Reset Loads all factory RA default parameters
  - Save Saves the current configuration to flash memory

The RA always powers on with the last saved configuration.

• Load – Loads a selected configuration from flash memory

After a Reset or Load, the RxAn GUI is automatically updated, the same as from the RA Menu Toolbar Connection > Refresh option.

• Firmware – The Firmware > Update option is used to update the RA 3.0 firmware. Refer to section 12, Appendix C for detailed information about this function.

Tools	Window H	elp		
	RA Configurati	ion 🕨		
	Firmware	×	Update	
	Calibration	•		
1	Properties	+		
1	Logs	+		
	API Server	+		
	Plugins	+		

Figure 121: Tools > Firmware Drop Down Menu

• Calibration – Cable Compensation, Power, and Frequency

Tools	Window Help		
	RA Configuration	•	
	Firmware	•	
	Calibration	•	Cable Compensation
	Properties	•	Power
	Logs	•	Frequency
	API Server	•	

Figure 122: Tools > Calibration Drop Down Menu

• Cable Compensation – Compensates for losses in the cables used to connect the RA to the receiver; this calibrates the specified output power to the end of the cable (input to the receiver) rather than the output of the RA. This can account for using different cables, the addition of splitters, couplers, etc.

The cable calibration table is stored in the RA in two fashions. One is in volatile memory. This is the table that is actively used when power levels are changed. If the RA is powered off, this table is lost. The second is in non-volatile memory. This table, if present, is loaded into volatile memory at power up.

Each RA channel can have its own cable calibration table. Each table can contain up to 317 entries. Each entry contains the frequency and the cable loss (or gain) at that frequency. Frequencies are any valid RA output frequency (from 200 - 2500 MHz and 4400 - 5250 MHz).

The RA has a maximum output power of -10 dBm. Any negative offset (loss) effectively takes away from the maximum available.

1. When the RA Cable Compensation screen opens, it is empty, as shown in Figure 123. To load a cable compensation file, use the File drop down menu, shown in Figure 124, to select Open.

2. Select the desired file, such as RA Cable Compensation.csv, then click on the Open button. One cable compensation file must be loaded for each channel.

📶 RA Cable	Compensatio	n	-	×
File RA				
CH0	CH1			
Frequency		Offset		
CH0				
CHU				

📶 RA Cable Compensati			
File	RA		
	Open		
	Save As		
	Close		

Figure 123: RA Cable Compensation

Figure 124: RA Cable Compensation, File Drop Down Menu

Channel 0 Frequency and Offset values populate the RA Compensation screen, as shown in Figure 125.

📶 RA Cable	Compensatio	n	-	×
File RA				
CH0	CH1			
Frequency		Offset		
200		-0.1		
500		-0.14		
800		-0.19		
1100		-0.21		
1400		-0.23		
1800		-0.24		
2200		-0.24		
2500		-0.26		
4400		-0.32		
4700		-0.33		
4940		-0.34		
5250		-0.35		
CH0				

Figure 125: RA Cable Compensation with CH0 Frequency and Offset Values

• Frequency (MHz) – Frequencies measured

• Offset

Each channel has its own list of frequencies and offsets. The CH0 tab displays the cable loss offset for each listed frequency on Channel 0. The CH1 tab shows the cable loss offset for each listed frequency on Channel 1

3. From the RA drop down menu, shown in Figure 126, select Write.

RA	
	Read
	Write
	Clear
	Save
	Erase
	Load

Figure 126: RA Cable Compensation, RA Drop Down Menu

4. Click on the tab for Channel CH1, then repeat steps 1-3.

Read, Write, and Clear pertain to the volatile memory.

- Read Reads the cable compensation table from the RA; This allows it to be saved to a .csv file.
- Write Writes the cable compensation table to the RA volatile memory
- Clear Clears the calibration table from RA volatile memory; This removes all cable calibration entries from non-volatile memory.

Save, Erase, and Load pertain to the non-volatile memory.

- Save Saves the current cable calibration table to RA non-volatile (flash) memory
- Erase Erases the cable calibration table stored in non-volatile memory; This means it will not be reloaded at power on. This does not affect the currently loaded (volatile) cable calibration table.

The current cable calibration table in non-volatile must be erased before a new one can be saved. This is a safeguard step to help prevent unintentionally overwriting the stored cable calibration table.

• Load – Loads the cable calibration table stored in non-volatile memory into the volatile memory

If a cable calibration table has been loaded from non-volatile memory into volatile memory, either at power on or from the Load menu entry, the indicator on the lower left side of the Status Bar should display in green, as shown in Figure 127. This means cable compensation is active.

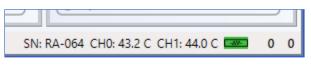


Figure 127: RA Cable Compensation Indicator

• Power – Disabled by default; usage under advice and supervision of Quasonix support only

- Frequency Disabled by default; usage under advice and supervision of Quasonix support only
- Properties Tags and Changed
  - Tags Usage under advice and supervision of Quasonix support only
  - Changed Usage under advice and supervision of Quasonix support only
- Logs Two types of log files may be accessed in the folder where the RA stores the files, Application and Measurement; These would only be used if Quasonix support requested the files for troubleshooting purposes.

Tools	Window Help		
	RA Configuration	۲	
	Firmware	۲	
	Calibration	۲	
1	Properties	×,	
	Logs	۲	Application
	API_Server	۲	Measurement

Figure 128: Tools, Logs Drop Down Menu

• API Server – Refer to section 13, Appendix D for additional information about this function.

Tools	Window	Help		
RA Configuration			•	
Firmware			•	
	Calibration			
	Properties			
	Logs			
	API Server		•	Start
Plugins		•	Stop	
				Status

Figure 129: Tools > API Server Drop Down Menu

#### 6.3.1.8 Window Menu

The Window menu is shown in Figure 130. The Layout selection consists of Load, Save As, and Default. The RxAn.exe lets the user pick and choose which panels to display and their configuration at any given time. These panel groups and their current configuration settings may be saved for future use. While a layout is part of a project file, layouts can also be loaded and saved separately. The current window layout is saved as part of the project file.

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Window Help		
Layout	•	Load
		Save As
		Default

Figure 130: Window Menu > Layout Drop Down Menu

- Load Loads a selected (previously saved) RxAn GUI configuration
- Save As Saves the current configuration to a user-selected file name
- Default Loads the factory default layout (panels and configurations) for the RxAn GUI

When a user opens a file (File > Open) either results, or a measurement, or notes, etc. The opened file displays in the Windows menu. Selecting that entry brings that editor to the front, as shown in Figure 131.

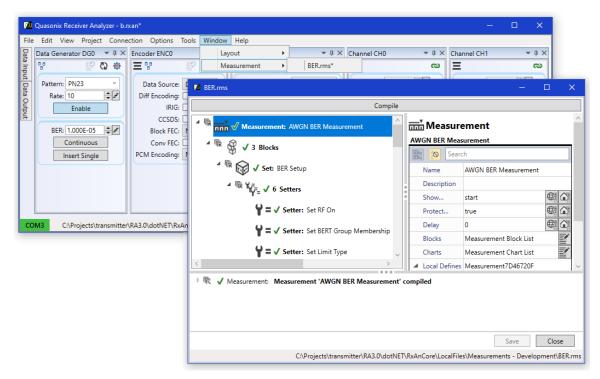


Figure 131: Window Menu > Editor Layout

#### 6.3.1.9 Help Menu

The Help menu provides access to the user manual and RxAn version information.

The user manual for RxAn is installed as part of the application. The Help menu, Manual selection, shown in Figure 132 opens the folder containing the user manual in <install directory>\Manual. By default, that is C:\Program Files\Quasonix\RxAn\Manual.

The Help menu, About selection, also shown in Figure 132, provides information about RxAn software and RA firmware VERSIONS.

The About screen provides the RA software version number and date along with the hardware versions and the copyright statement (Figure 133).



Figure 132: Help Menu

Figure 133: About Screen

## 6.4 Detailed Panel Descriptions

The RxAn GUI provides panels for Channel Mixer, Data Generator, Encoder, Indirect Path, IO (Data Input and Data Output), Modulator, Phase Generator, Phase Generator Group, Output Channels, and Summer. These panels may be accessed via the View Menu, as described in the following sections. In addition, panels may be assigned to control groups.

Analyzer functionality is presented in BERT Control, BERT Group, BERT Monitor, Data Sync, DQE, and Measurement panels beginning in section 6.4.13.

## 6.4.1 Control Groups

The RA hardware contains various modules that can be configured. There is, generally, a panel in the RxAn GUI that pertains to each of the different types of modules in the hardware. In many cases there are multiple instances of those types of modules. Data Generator, Encoders, Modulators, BERTs, etc. It is common, especially for BERTs, that the configuration of the separate instances of these modules should be configured the same. Rather than make the user go to the panel for each instance of a module and configure it separately, instances of modules can be linked into what is called a Control Group.

Changing the value of a property in one instance of a module in a Control Group changes the value of the property in all of the other instances of the modules in the same control group. The user can have multiple Control Groups made of different instances of the same type of module. For example, the user may want all of the instances of BERTS pertaining to I data in one Control Group, and all of the instances of BERTs pertaining to Q data in a different Control Group.

Clicking on the Three Panel icon opens a window with a Control Group drop down menu, as shown in Figure 134.

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Data Generator DG0						
89	8					
Control Group:	None Y					
Rate: 10	None	Ľ				
	Group 0	t				
	Group 1	F				

Figure 134: Example Data Generator Control Group Drop Down Menu

If the selection in the drop down menu is None, that module instance is not part of any Control Group. Control Groups are identified by number, from 0 to the maximum number of Control Groups for that module type. There will be half the number of control groups as there are instances of that module type. For BERTs there are eight (8) BERTs, so there are four (4) Control Groups; there are 12 Phase Generators, so there are six (6) Control Groups. This is because there must be a minimum of two modules in a Control Group for that Control group to do anything useful, so the maximum possible number of groups is the number of modules divided by two.

If an instance of a module is in a Control Group, the Three Panel icon shows lines between them indicating it is connected to other modules, and the Control Group ID (number) displays, as shown in Figure 135. This is so the user can see at a glance which Control Group, if any, that instance of the module is in.

Data	Gene	rator	DG0	
먺				

Figure 135: Example Data Generator Connected Three Panel Icon with Control Group 1

Not all module types can participate in a Control Group. Some do not because they are a single instance module such as Data Input, and others, such as BERT Operation Group modules, do not because they have no properties to set. So having them in a Control Group is meaningless.

### 6.4.2 IO

The RA provides one set of input clock and data and one set of output clock and data.

The View menu provides access to the IO panels, as shown in Figure 136. Two IO panels, Data Input and Data Output, are used to configure IO parameters.

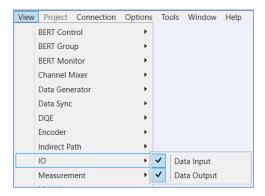


Figure 136: View, IO Drop Down Menu

### 6.4.2.1 Data Input

The Data Input panel is used to configure the external data input, as shown in Figure 137.

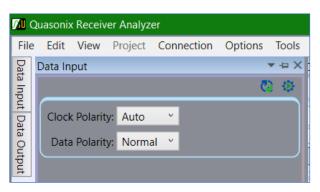


Figure 137: Data Input Panel

• Clock Polarity – In a synchronous system, incoming data changes on one edge of the clock signal and is sampled by the RA on the other edge, as shown in Figure 138. If this is incorrect, it can result in unreliable data input. Clock polarity ensures the data is sampled on the proper clock edge.

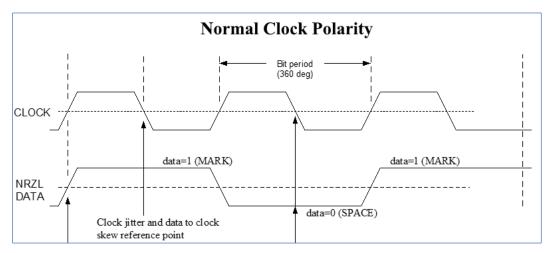


Figure 138: Normal Clock Polarity Diagram

Valid selections are Normal, Inverted, and Auto, as shown in Figure 139.

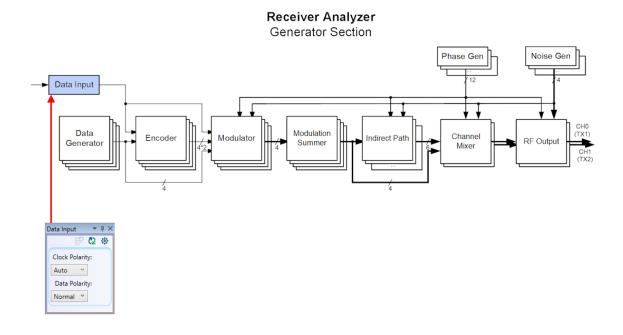
- Normal The RA samples data on the falling edge.
- Inverted The RA samples data on the rising edge.
- Auto Attempts to determine the clock edge automatically by measuring the timing between changes in data and the changes in the clock.
- Data Polarity Valid selections are Normal and Inverted, as shown in Figure 140; useful when the data stream starts out inverted, in effect, un-inverts the data

Data Input		•	₽×	Data Inp	ut		-	Þ
		0	-4\$⊁				۵	3
Clock Polarity:	Auto 👻			Clock	Polarity: Auto	~		
Data Polarity:	Normal			Data	Polarity: Normal	v		
Inverted				Normal				
	Auto				Inverted			

Figure 139: Clock Polarity Drop Down Menu

Figure 140: Data Polarity Drop Down Menu

The Data Input panel is associated with the Data Input block in the RA signal generation section, as shown in the diagram in Figure 141.



#### Figure 141: Signal Generation, Data Input – Data Input Panel

#### 6.4.2.2 Data Output

Data Output is the RA hardware output of clock and data. Its source can be the data input, any data generator, any encoder, and any input to the Analyzer Section (after potential decapsulation).

The Data Output panel includes selections for Data Source, Clock Polarity, and Data Polarity, as shown in Figure 142.

<b>70</b> C	Juasonix	Receiv	er Analyz	er			
File	Edit	View	Project	Conn	ection	Options	Tools
Dat	Data Ou	utput					▼ += X
Data Input Data						द	
put				_			
Da	Data	Source	e: EXT	~			
	Clock	Polarit	y: Norm	al ~			
Output	Data	Polarit	y: Norm	al ~			

Figure 142: Data Output Panel

• Data Source – Select from data sources from Data Generators 0-3, both sub-channels for Encoders 0-3, External, any of the eight analyzer inputs, or None, as shown in Figure 143

When EXT is selected, the external data input is routed to the data output.

• Clock Polarity – Valid selections are Normal and Inverted, as shown in Figure 144.

• Data Polarity –Valid selections are Normal and Inverted, as shown in Figure 144; useful when the data stream starts out inverted, in effect, un-inverts the data

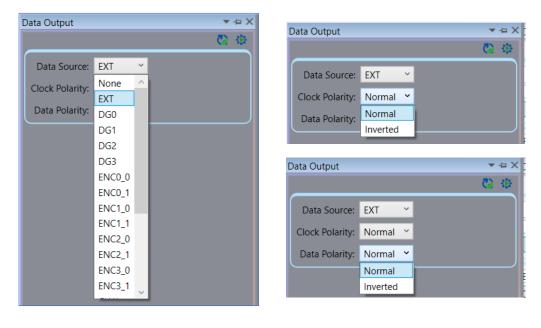


Figure 143: Data Source Drop Down Menu

Figure 144: Clock Polarity and Data Polarity Drop Down Menus

### 6.4.3 Status Outputs

The Receiver Analyzer supports a total of eight (8) outputs that can be used to monitor real-time status of conditions in the Receiver Analyzer. These can be recorded with a Data Acquisition System or monitored on an oscilloscope.

They are divided into four signals each, in two connectors, J9 and J19, on the back panel, as shown in Figure 145.



Figure 145: Back Panel with J9 and J19 Labeled

The outputs can be sourced from ten signal types. Each signal type can come from one or more signal sources. The first nine are digital in nature, and the tenth (Decapsulator DQM) is analog. An optional DAC module can be attached to J19 to provide eight analog outputs. The DAC module uses three of the digital outputs to drive the DACs. There are three 'classes' of signal sources, those with no individual channel sources - None, those from the RF output channels - CH1I/CH1Q/CH2I/... BSSI/BSSQ.

A signal type must be placed on an appropriate output type (analog or digital) and must be matched with an appropriate channel source.

The analog outputs are updated 200 thousand times per second and can be sourced from any signal type. If it is a Digital signal type, the output will go from GND and Vcc on the DAC outputs.

Three of the digital outputs can be utilized to drive an optional DAC. This functionality must be enabled. When the analog outputs are enabled, three of the output status lines (J19 P11, J19 P12, and J19 P13) are unavailable for digital use.

Signal	Туре	Source Channels
Fixed 0	Digital	None
Fixed 1	Digital	None
RF On	Digital	CH0, CH1
Signal On	Digital	CH0, CH1
AM Trigger	Digital	CH0, CH1
Clock Sync	Digital	CH1I, CH1Q, CH2I, CH2Q, CMBI, CMBQ, BSSI, BSSQ
Data Sync	Digital	CH1I, CH1Q, CH2I, CH2Q, CMBI, CMBQ, BSSI, BSSQ
Bit Error	Digital	CH1I, CH1Q, CH2I, CH2Q, CMBI, CMBQ, BSSI, BSSQ
DQE Trigger	Digital	CH1I, CH1Q, CH2I, CH2Q, CMBI, CMBQ, BSSI, BSSQ
DQE Lock	Digital	CH1I, CH1Q, CH2I, CH2Q, CMBI, CMBQ, BSSI, BSSQ
User Digital	Digital	None
DQE Metric	Analog	CH1I, CH1Q, CH2I, CH2Q, CMBI, CMBQ, BSSI, BSSQ
User Analog	Analog	None

Т	able	3:	Status	Outputs
	abio	۰.	oluluo	Outputo

- Fixed 0 always produces a low on a digital output and GND on an analog output.
- Fixed 1 always produces a high on a digital output and Vcc on an analog output.
- RF On follows the state of Channel 0 and Channel 1 RF On/Off: a low/GND for Off and a high/Vcc for On.
- Signal On follows the state of Channel 0 and Channel 1 Signal On/Off: a low/GND for Off and a high/Vcc for On.
- AM Trigger follows the AM added in the Channel 0 and Channel 1 Channel Mixer AM modulation: a low/GND when the modulation is below the average level, and a high/Vcc when the AM modulation is above the average level. This is useful to synchronize the change in RF output level with a potential change in signal quality from the receiver(s) under test.
- Clock Sync follows the Clock Slip status displayed in the BERT monitors: a low/GND for clock slip and a high/Vcc for clock sync.

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- Data Sync follows the Synced status display in the Data Sync panels. Data is synced when the user-selected synchronization data pattern is detected in the receiver output data: a low/GND when data sync is not detected and a high/Vcc when data sync has been detected.
- Bit Error follows the internal individual bit error detection inside the BERTs: a low/GND with no error for a bit and a high/Vcc when a bit is detected in error.
- DQE Trigger is toggled between low and high for every DQE frame that is decoded.
- DQE Decapsulation Lock follows the Locked status in the DQE panels: a low/GND when DQE synchronization has not been established and a high/Vcc when DQE synchronization has been established. This shows a single bad DQE frame.
- User Digital represents a user settable digital value. This can be set in the Status Outputs hamburger menu or in a measurement.
- DQE Metric follows the internal DQE decapsulation and produces an analog value that matches the data quality metric for that frame: GND for a data quality metric of 0 and Vcc for a data quality metric of 65,535.
- User Analog represents a user settable analog value. This can be set from 9 to 0.99 in the Status Outputs hamburger menu or in a measurement.

The View menu provides access to the Status Outputs panel, as shown in Figure 146. Status Outputs are controlled using the Status Outputs panel, shown in Figure 147.

View	Project Connection	Options	Tools	Window	Help
	BERT Control	+			
	BERT Group	•			
	BERT Monitor	•			
	Channel Mixer	•			
	Data Generator	•			
	Data Sync	•			
	DQE	•			
	Encoder	•			
	Indirect Path	•			
	IO	•			
	Measurement	•			
	Modulator	•			
	Output	•			
	Output Channels	•			
	Phase Generator	•			
	Phase Generator Group	•	_		
	Status Outputs	•	✓ Sta	atus Output	ts
	Summer	•			

itatus Outp	atus Outputs 🔹 🔻 🗸 🗸						
Ξ				?			
Outputs	;						
C1P6:	Bit Error	v	CH1I	v			
C1P11:	RF On	×	CH0	×			
C1P12:	Data Sync	~	CH1I	~			
C1P13:	Data Sync	~	CMBI	×			
C2P6:	AM Sync	~	CH0	~			
C2P11:	RF On	~	CH1	×			
C2P12:	Data Sync	~	CH2I	~			
C2P13:	Data Sync	~	BSSI	۷			
ANA0:	AM Sync	~	CH0	~			
ANA1:	DQE Metric	~	CH1I	×			
ANA2:	DQE Metric	~	CH2I	~			
ANA3:	DQE Metric	~	CMBI	~			
ANA4:	DQE Metric	~	CMBI	~			
ANA5:	DQE Metric	~	CMBQ	×			
ANA6:	DQE Metric	~	BSSI	×			
ANA7:	DQE Metric	~	BSSQ	~			

#### Figure 146: Status Outputs Drop Down Menu

Figure 147: Status Outputs

The Status Outputs panel shows the list of available outputs, the currently selected signal type, and the signal source. Outputs highlighted in green are available for use, while outputs highlighted in yellow are not.

To enable the analog outputs for use, click on the Analog Outputs Enable check box in the Status Outputs panel hamburger menu, as shown in Figure 148 and Figure 149.

tatus Outputs 👻 🕂					
Ξ		17	0		
Analog (	Outputs Enable:				
Dig	ital User Value:				
Ana	log User Value:	<b>1</b>			
CIPI2:	Data Sync 🔹	СНП			
C1P13:	Data Sync 🛛 👻	CMBI Y			
C2P6:	Bit Error Y	CH2I Y			
C2P11:	RF On Y	CH1 Y			
C2P12:	Data Sync 🛛 👻	CH2I Y			
C2P13:	Data Sync 🛛 👻	BSSI Y			
ANA0:	DQE Metric V	CH1I Y			
ANA1:	DQE Metric V	CH1Q Y			
ANA2:	DQE Metric V	CH2I Y			
ANA3:	DQE Metric V	CH2Q Y			
ANA4:	DQE Metric V	CMBI ~			
ANA5:	DQE Metric V	CMBQ Y			
ANA6:	DQE Metric 🕑	BSSI Y			
ANA7:	DQE Metric V	BSSQ ¥			

Figure 148: Analog Outputs Enable Check Box

itus Outp	outs		<b>→</b> ‡
			17 🖏
Analog (	Outputs Enable:	~	
Dig	ital User Value: [		
Ana	log User Value:	0 🗘 🕹 🗸	
CIPIZ:	Data Sync 🔹	СНП	
C1P13:	Data Sync 🛛 🗡	CMBI Y	
C2P6:	Bit Error *	CH2I Y	
C2P11:	RF On Y	CH1 Y	
C2P12:	Data Sync 🛛 👻	CH2I Y	
C2P13:	Data Sync 🛛 👻	BSSI Y	
ANA0:	DQE Metric V	CH1I Y	
ANA1:	DQE Metric \vee	CH1Q Y	
ANA2:	DQE Metric V	CH2I Y	
ANA3:	DQE Metric Y	CH2Q Y	
ANA4:	DQE Metric V	CMBI ~	
ANA5:	DQE Metric V	CMBQ ~	
ANA6:	DQE Metric Y	BSSI ~	
ANA7:	DOE Metric V	BSSO Y	

Figure 149: Analog Outputs Enabled

The analog outputs displays in green indicating they are available for use, and the three digital signals used to control the optional DAC now display in yellow indicating they are no longer available for use.

If an incompatible signal source is selected for the selected signal type, the source is shown in red, and the error is displayed in a tool tip over the incorrect channel, as shown in Figure 150.

Status Out	puts		- † ×
Ξ			🗗 😳
Outputs	5		
C1P6:	Fixed 0	Ŷ	CH1I Y
C1P11:	RF On	~	C Invalid channel for signal Fixed 0
C1P12:	Data Sync	~	Christer for signal fixed o
C1P13:	Data Sync	~	CMBI Y
C2P6:	Bit Error	~	CH2I Y
C2P11:	RF On	~	CH1 Y
C2P12:	Data Sync	~	CH2I Y



When a correct channel can be selected, the error goes away. The actual Status Output signal is undefined if there is an error.

Status Out	puts		<b>▼</b> ∓ ×
Ξ			ii? 🖏
Output	s		
C1P6:	Fixed 0	Ŷ	CH1I ~
C1P11:	RF On	~	None
C1P12:	Data Sync	~	CH0
C1P13:	Data Sync	×	CH1 CH1I
C2P6:	Bit Error	×	CHIQ
C2P11:	RF On	×	CH2I
C2P12:	Data Sync	×	CH2Q

Status Outp	outs		<b>▼</b> 早 >	×
Ξ			12 🖓	1
Outputs	5			١
C1P6:	Fixed 0	×	None ~	I
C1P11:	RF On	~	CH0 Y	I
C1P12:	Data Sync	~	CH1I Y	I
C1P13:	Data Sync	~	CMBI Y	I
C2P6:	Bit Error	~	CH2I Y	I
C2P11:	RF On	~	CH1 Y	I
C2P12:	Data Sync	×	CH2I Y	I

Figure 151: Channel Selection With and Without Errors

There are two user settable Status Outputs, one digital and one analog. These are accessed in the Status Outputs hamburger menu. The digital output is set using the Digital User Value check box (resulting in 0 or 1), and the analog is set using the Analog User Value field. Type or step through a value from 0 to 0.998.

	🔺 џ 🗙
	ି 🖏
Analog Outputs Enable: 🗹	
Digital User Value: 🗹	
Analog User Value: 0	
CIPI2: Data Sync CHII	
C1P13: Data Sync Y CMBI Y	
C2P6: Bit Error Y CH2I Y	
C2P11: RF On Y CH1 Y	
C2P12: Data Sync Y CH2I Y	
C2P13: Data Sync Y BSSI Y	
ANA0: DQE Metric Y CH1I Y	
ANA1: DQE Metric Y CH1Q Y	
ANA2: DQE Metric Y CH2I Y	
ANA3: DQE Metric Y CH2Q Y	
ANA4: DQE Metric Y CMBI Y	
ANA5: DQE Metric Y CMBQ Y	
ANA6: DQE Metric Y BSSI Y	
ANA7: DQE Metric Y BSSQ Y	

Status Outputs **-** ↓ × Ξ ٢) Analog Outputs Enable: 🗹 Digital User Value: 🗹 Analog User Value: 0.125 CIPI2: Data Sync CHII C1P13: Data Sync 💙 CMBI C2P6: Bit Error Y CH2I v C2P11: RF On Y CH1 C2P12: Data Sync 🛛 🗡 CH2I C2P13: Data Sync 🛛 🗡 BSSI ANA0: DQE Metric Y CH1I ANA1: DQE Metric Y CH1Q Y ANA2: DQE Metric Y CH2I v ANA3: DQE Metric Y CH2Q Y ANA4: DQE Metric Y CMBI ANA5: DQE Metric Y CMBQ Y ANA6: DQE Metric Y BSSI v ANA7: DQE Metric Y BSSQ Y

Figure 152: Digital User Value Check Box

Figure 153: Analog User Value Field

#### 6.4.4 Data Generator DG0-DG3

The Data Generator provides a known data pattern and, optionally, a known rate of random errors.

The View menu provides access to the Data Generator panels, as shown in Figure 154.

View	Project	Connection	Options	Tools	Window	Help	
	BERT Cont	trol	•				
	BERT Grou	ıp	•				
	BERT Mon	itor	•				
	Channel M	lixer	•				
	Data Gene	erator	×	Da	ita Generat	or DG0	
	Data Sync		•	Da	ita Generat	or DG1	
	DQE		•	Da	ita Generat	or DG2	
	Encoder		•	Da	ita Generat	or DG3	

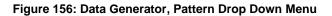
Figure 154: View, Data Generator Drop Down Menu

The Data Generator panel, shown in Figure 155, is used to set parameters for the internal data generators. It also includes a BER error generator.

Data Generator DG0	<b>▼</b> ‡ ×
80	🔅 🚯
Pattern: PN23 Rate: 0.9999999 Enable	~ ↓↓
BER: 1.000E-05 Continuous Insert Single	

Data Gene	rator DG0	<b>▼</b> ‡
8		C2 🦚
Pattern:	PN23	~
Rate:	PN6	
	PN9	
	PN11	
BER:	PN15	
	PN17	
	PN20	
	PN23	
	PN31	





- Pattern Sets the data pattern generated by the RA, as shown in Figure 156; This is a fixed pattern or a pseudorandom pattern that repeats based on the chosen pattern/sequence (a shorter pattern looks more regular, a longer pattern looks more random)
  - PN6 Pseudorandom pattern 2<sup>6</sup>-1 bits in length

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- PN9 Pseudorandom pattern 2<sup>9</sup>-1 bits in length
- PN11 Pseudorandom pattern 2<sup>11</sup>-1 bits in length
- PN15 Pseudorandom pattern 2<sup>15</sup>-1 bits in length
- PN17 Pseudorandom pattern 2<sup>17</sup>-1 bits in length
- PN20 Pseudorandom pattern 2<sup>20</sup>-1 bits in length
- PN23 Pseudorandom pattern 2<sup>23</sup>-1 bits in length
- PN31 Pseudorandom pattern 2<sup>31</sup>-1 bits in length
- Any binary pattern between 2 and 32 bits may be typed directly into the Pattern field.
- Rate Data rate in Mbps Typing a number in this field sets the data rate

Step – Sets step size and unit in Hz, kHz, or MHz, as shown in Figure 157.

• Enable button – Enables or disables the data generator; disabling the data generator effectively produces an all zeroes (0) pattern

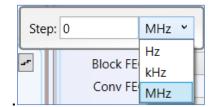


Figure 157: Rate, Step Selection Window

• BER – Used to add a known bit error rate to the test data stream, shown in Figure 158; type a bit error rate to adjust the rate

Step – Sets the step size and the desired step type: Linear or Log, as shown in Figure 159.

Pattern: PN23 V Rate: 0.9999999 V Enable BER: 1.000E-05 V Continuous			
Pattern: PN23 Rate: 0.9999999 + -	Data Gene	erator DG0	<b>-</b> ↓ ×
Rate: 0.9999999 + -	89		🕸 🚯
Continuous		0.9999999	
Continuous			
	BEK:		
Insert Single		Insert Single	

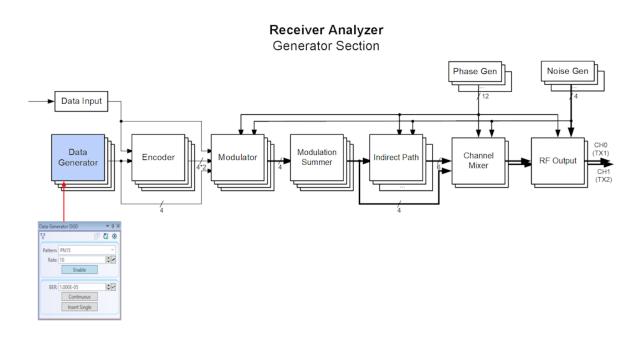


#### Figure 158: Data Generator, BER

Figure 159: BER, Step Selection Window

- Continuous button Continuously adds bit errors at the specified bit error rate
- Insert Single button Click on this button to add a single bit error to the test stream

The Data Generator panel is associated with the Data Generator in the RA signal generation section, as shown in the diagram in Figure 160.



#### Figure 160: Signal Generation, Data Generator – Data Generator Panel

#### 6.4.5 Encoder ENC0-ENC3

The Encoder panel configures the encoder module. The encoder module provides all functions necessary to convert raw binary data in NRZ-L format to the necessary binary format for modulation. Some of these functions are essentially standard, such as differential encoding for PSK-type modulations. Other functions are applicationspecific, such as forward error correction (FEC). Many forms of encoding change the effective bit rate of the encoded data, either by converting each bit to a di-bit such as in bi-phase encoding, or by adding header and/or redundant bits, such as attached sync markers (ASMs) and parity bits as in low-density parity check (LDPC) encoding. In the case of space-time coding (STC), one binary data stream is encoded into two separate orthogonal binary data streams, each containing the original binary data information with added pilot bits.

The View menu provides access to the Encoder panel, as shown in Figure 161.

The Encoder panel, shown in Figure 162, is used to set encoding parameters. It includes data source, differential encoding, IRIG and CCSDS randomization, block and convolutional FEC, and PCM encoding parameters.

View	Project	Connection	Options	То	ols	Window	Help
	BERT Cont	trol	•				
	BERT Grou	ıp	•				
	BERT Mon	itor	•				
	Channel N	lixer	•				
	Data Gene	erator	+				
	Data Sync		•				
	DQE		•				
	Encoder		•		En	coder ENC	0
	Indirect Path		•		En	coder ENC	1
	IO		•		En	coder ENC	2
	Measurem	nent	•		En	coder ENC	3

Encoder ENC0	<b>-</b> ↓ ×
<b>=</b> 92	🖏 🚯
Data Source: DG0 V Diff Encoding: IRIG: CCSDS: Block FEC: None V Conv FEC: PCM Encoding: NRZL V	

Figure 161: Encoder Drop Down Menu

Figure 162: Encoder ENC0 Panel

• Data Source – Select from data sources from Data Generators 0-3, External, or None

When EXT is selected the RA uses the external data input. If None is selected, it is as if the data and clock are disconnected; the resulting encoded data rate will be 0 Mbps, and the resulting output will be an unmodulated carrier.

- Diff Encoding When checked, differential encoding is enabled. It is automatically enabled for \*PSK modes unless LDPC or Reed-Solomon block encoding is used
- IRIG When checked, the IRIG randomizer is enabled (for non-LDPC operation). Enabling IRIG 15-stage randomization ensures the pattern never has too many ones or zeroes in a row.
- CCSDS When checked, CCSDS randomization is enabled. (for LDPC operation). Enabling randomization ensures the pattern never has too many ones or zeroes in a row.
- Block FEC (Forward Error Correction) 'Block' means bits are grouped into a block and extra information is added to each block that the receiver can use to correct errors. The correction is done in the receiver on a block by block basis.

Only Encoder ENC0 supports block FEC. Valid selections from the drop down menu are None, R-S (Reed-Solomon), or IRIG LDPC.

- Conv FEC Convolutional FEC is a continuous process across the stream of bits. When checked, convolutional encoding is enabled.
- PCM Encoding Select the PCM Encoding value from the drop down menu, shown in Figure 163; default setting is NRZL

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Possible settings include:

- NRZ-L: Non-return-to-zero, level
- NRZ-M: Non-return-to-zero, mark
- NRZ-S: Non-return-to-zero, space
- Biphase-S: Biø, space
- DM-M: Delay modulation (Miller code), mark
- DM-S: Delay modulation (Miller code), space
- RZ: Return-to-zero
- Biphase-L: Bio, level
- Biphase-M: Biø, mark
- M2-M: Modified delay modulation (Miller squared code), mark
- M2-S: Delay modified modulation (Miller squared code), space

Encoder E	NC0			<b>▼</b> ‡ ×
≡₩				۱ 😳
Data S	Source:	DG0	~	
Diff End	oding:			
	IRIG:			
(	CCSDS:			
Blog	ck FEC:	None	v	
Cor	nv FEC:			
PCM End	oding:	NRZL	٠	
		NRZL		
		NRZM		
		NRZS		
		RZ		
		BIPL		
- 1 ×	BERT C	BIPM		
<u>ن</u> دې		BIPS		
C2	Meas	DMM		Rate
~	<b>I</b>	DMS		0E+0
		M2M		
rror ~	Estim	M2S		Rate U. JUUE +

Figure 163: Encoder PCM Encoding Drop Down Menu

#### **PCM Framing**

Quasonix RAs can encode PCM frames as defined by IRIG 106-17 Chapter 4 and Appendix 4-A. The RA can generate a large subset of possible frame configurations, including sync word and subframe without commutation.

Quasonix RAs can generate PCM frames using external (user) data or internal test patterns for the frame payload. Within the RA, this functionality is referred to as the PCM Framer.

The PCM Framer supports fixed-length PCM frame generation with the following parameters:

- Major frame length up to 256 minor frames
- Minor frame length up to 16,384 bits
- Minor frame sync pattern 16 to 33 bits (user-selectable pattern and length)
- Optional subframe ID (SFID) insertion (word 1 position only)

The resulting PCM frame format appears as shown in Figure 164.

М	inor Frame M	aximum Leng	th, <mark>N Words</mark> o	r B Bi	ts
	Class I: Shall not	exceed 8192 bits	s nor exceed 102	4 word	s
	Class II: Shall no	t exceed 16,384	bits		
	Word 1	Word 2	Word 3		Word N-1
Minor Frame Sync Pattern	SFID = 1	Data 1	Data 2	•••	Data N-2
Minor Frame Sync Pattern	SFID = 2	Data N-1	Data N		Data 2N-4
Minor Frame Sync Pattern	SFID = 3	Data 2N-3	Data 2N-2		Data 3N-6
				-	•
Minor Frame Sync Pattern	SFID = Z	Data (Z-1)(N-2)+1	Data (Z-1)(N-2)+2	•••	Data (Z-1)(N-2)+N-2

Figure 164: Frame Format with SFID Insertion Enabled

\*Major Frame Length = Minor Frame Maximum Length multiplied by Z

There is no mechanism to align specific sets of serial bits to Data words, and there is no mechanism to align specific data words to a given position within the frame format. Therefore, the PCM Framer cannot generate frames with data parameters suitable for decommutation. It can, however, be used to measure frame synchronization and link quality via SFID verification.

### **Encoder Hamburger Menu**

Provides access to additional parameters used to configure parameters for Reed-Solomon, LDPC, PCM Framing, etc. The Encoder Hamburger Menu, Figure 166, is accessed via the icon on the Encoder ENC0 panel, as shown in Figure 162.

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C∂ ↔ urce: DG0 ✓ ding: □		Data Polarity: R-S Interleave:	Data Polarity: Normal
ding:		R-S Interleave:	D. C. Instanlanum 1
			K-S Interleave:
		LDPC Class:	LDPC Class: IRIG
IRIG: SDS:		LDPC Mode:	LDPC Mode: 4k Rate 2/3
FEC: None Y		PCM Framing Mode:	PCM Framing Mode: Disabled
FEC:		-	Minor Frame Length: 2048
ling: NRZL ¥		, , ,	
		PCM Framing Header:	PCM Framing Header: 1110101110010000
		Optimal Framing Header:	Optimal Framing Header: 16 Set
		Conv Symbol:	Conv Symbol:
FEC: ding: NRZL V		Major Frame Length: PCM Framing Header: Optimal Framing Header:	Major Frame Length: PCM Framing Header: 1110101110010000 Optimal Framing Header: 16 Set

Figure 165: Encoder ENC0 Panel

Figure 166: Encoder Hamburger Menu

• Data Polarity – Values are Normal or Inverted; useful when the data stream starts out inverted, in effect, un-inverts the data; use the drop down menu to select, as shown in Figure 167

Data Polarity:	Normal	~
R-S Interleave:	Normal	
LDPC Class:	Inverted	_
LDPC Mode:	4k Rate 2/3	Ŷ
PCM Framing Mode:	Disabled	~
Minor Frame Length:	2048	
Major Frame Length:		
PCM Framing Header:	1110101110010000	16
Optimal Framing Header:	16 Set	
Conv Symbol:		

Figure 167: Encoder Hamburger Menu, Data Polarity Drop Down Menu

- R-S Interleave Controls the interleaving depth, in Reed-Solomon codewords; Maximum value of eight (8); An interleave depth of 1 is equivalent to no interleaving.
- LDPC Class Selects the class of LDPC encoding, either standard IRIG or CCSDS, as shown in Figure 168

Data Polarity:	Normal	Ŷ
R-S Interleave:	1	
LDPC Class:	IRIG	Ŷ
LDPC Mode:	IRIG	
PCM Framing Mode:	CCSDS Disabled	
Minor Frame Length:	2048	
Major Frame Length:		
PCM Framing Header:	1110101110010000	16
Optimal Framing Header:	16 Set	
Conv Symbol:		

Figure 168: Encoder Hamburger Menu, LDPC Class Drop Down Menu

• LDPC Mode – Selects an LDPC mode, as shown in Figure 169

Data Polarity:	Normal ~
R-S Interleave:	1
LDPC Class:	IRIG ~
LDPC Mode:	4k Rate 2/3 ~
PCM Framing Mode:	4k Rate 1/2
Minor Frame Length:	1k Rate 1/2
Millior Halle Lengui.	4k Rate 2/3
Major Frame Length:	11. D-+- 2/2
DCM Eraming Handar	1k Rate 2/3
PCM Framing Header:	4k Rate 4/5
Optimal Framing Header:	1k Rate 4/5
Conv Symbol:	

Figure 169: Encoder Hamburger Menu, LDPC Drop Down Menu

- PCM Framing Mode The RA can emulate basic PCM encoder frame formatting as described in IRIG 106 Chapter 4. This capability is useful for system level measurement, such as verifying decomm lock. Valid options are Disabled, Enabled, and Sub Frame, as shown in Figure 170.
  - Disabled The data is transmitted with no PCM frame-level formatting.
  - Enabled PCM framing per the parameters described.
  - Sub Frame If enabled, an additional word is added to each minor frame that increments each minor frame, from 1 up to the major frame length. Presently, the subframe ID word is always the same length as the minor frame sync word, and it appears in the word immediately following the minor frame sync word.

Data Polarity:	Normal ~
R-S Interleave:	1
LDPC Class:	IRIG ~
LDPC Mode:	4k Rate 2/3 ~
PCM Framing Mode:	Disabled Y
Minor Frame Length:	Disabled
Major Frame Length:	Enabled
PCM Framing Header:	Sub Frame
Optimal Framing Header:	16 Set
Conv Symbol:	

Figure 170: Encoder Hamburger Menu, PCM Framing Mode Drop Down Menu

- Minor Frame Length Used to enter the length of the minor frame (including sync word and subframe ID word, if enabled), up to 16384 bits
- Major Frame Length Used to enter the number of minor frames in a major frame, up to 256; This parameter only affects the count range of the subframe ID, if enabled.
- PCM Framing Header Used to enter the frame header (user pattern), or select the IRIG standard (by length). Minimum PCM Framing Header length is 16.
- Optimal Framing Header The 'optimal patterns' are a set of sync words published in IRIG 106, Chapter 4, Appendix 4-A, Table A-1. There is one optimal pattern recommended for each pattern length from 16 bits to 33 bits. Because these are generally used in the telemetry community, the RxAn provides a selection based on length, so the user does not have to type (and possibly mis-type) the whole pattern.

Click on the Set button to display the selected frame header in the PCM Framing Header field.

• Conv Symbol – The Space Network Users' Guide (NASA 450-SNUG) defines two different methods for generating quadrature symbols (variants of QPSK) when using convolutional encoding.

The first method is for I and Q data streams to be independently encoded. In this method, two convolutional encoders are used, one for I data and one for Q data, as shown in Figure 171. This method is used when Conv Symbol is NOT checked.

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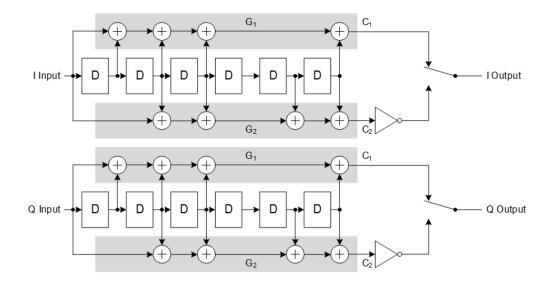


Figure 171: I and Q Data Streams Independently Encoded

The second method is for I and Q data to be created from the G1 and G2 generators, respectively, of a single convolutional encoder, as shown in Figure 172. In this method, only one convolutional encoder is used for both the I and Q data. This method is used when Conv Symbol is checked.

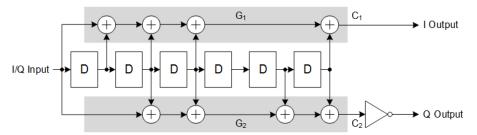
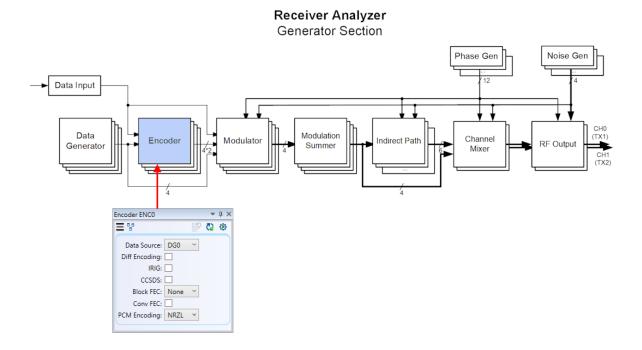


Figure 172: I and Q Data from Single Convolutional Encoder

The Encoder panel is associated with the Encoder block in the RA signal generation section, as shown in the diagram in Figure 173.





### 6.4.6 Modulator MOD0-MOD3

The modulator takes the incoming data stream and generates a signal phase trajectory based on the modulation mode, modulation index, and filtering.

The View menu provides access to the Modulator panel, as shown in Figure 174.

The Modulator panel, shown in Figure 175, is used to configure parameters for the base band modulator. It includes Data Source, Modulation, Modulation Scale, an Enable button, and Airlink (over the air) Data Rate display.

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**-** д ×

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View	Project Connection	on Options	Tools	Window	Help
	BERT Control	•			
	BERT Group	•			
	BERT Monitor	•			
	Channel Mixer	•			
	Data Generator	+			
	Data Sync	+			
	DQE	•			
	Encoder	•			
	Indirect Path	•			
	10	•			
	Measurement	•			
	Modulator	•	M	odulator M	OD0
	Phase Generator	•	M	odulator M	OD1
	Phase Generator Gro	up 🕨	M	odulator M	OD2
	RF Output	•	M	odulator M	OD3
	~				

Figure 174: Modulator Drop Down Menu

AININK Data Rai	te: 1.000000	
Modulator M	Modulator M	. Summer SUM0

Figure 175: Modulator MOD0 Panel

 Data Source – Select from data sources from Data Generators 0-3, Encoders 0-3, External, or None, shown in Figure 176

Modulator MOD0

Data Source: ENCO

Modulation Scale: 1

ALL 1 D 1 D 1 4 000000

Modulation: PCMFM

Enable

Ξ 😯

When EXT is selected the RA uses the external data input. If None is selected, it is as if the data and clock are disconnected; the resulting Airlink Data Rate will be 0 Mbps, and the resulting output will be an unmodulated carrier.

When using STC modulation, the encoder produces two separate orthogonal binary data streams, one for "top" (pilot 0) and one for "bottom" (pilot 1). Each data stream requires a separate modulator. The outputs from the two modulators can be summed to create single top/bottom RF output, or may be output separately in true dual transmitter function. Only Encoder panel ENC0 supports STC or STC2.

Modulator MOD0		<b>▼</b> ‡ ×	M	lodulator N	/OD0		•	ņ
三部		ن 🚯		- 12		12	۲.	ł,
Data Source: Modulation: Modulation Scale: Ena Airlink Data Rate:	None EXT DG0 DG1 DG2 DG3 ENC0 ENC1 ENC2			Data	lulation: n Scale: Enat	ENC0.0 None EXT DG0 DG1 DG2 DG3 ENC0.0		7 ×
Modulator M M	ENC3	ummer SUM0	Ļ	/lodulat	Modula	ENC0.1	mer	

Figure 176: Data Source Drop Down Menu

## 3<sup>rd</sup> Generation Receiver Analyzer

- Modulation Waveform modes are selected using the down arrow to access the drop down menu, as shown in Figure 177. Available modes are:
  - PCM/FM (ARTM Tier 0)
  - SOQPSK (ARTM Tier I)
  - Multi-h CPM (ARTM Tier II)
  - DPM
  - STC (SOQSPK-TG STC)
  - STC2 (ARTM CPM STC)
  - Legacy (PSK) suite, which includes:
    - BPSK
    - Offset QPSK (OQPSK)
    - QPSK
    - UQPSK

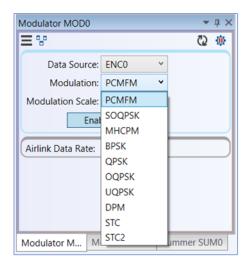


Figure 177: Modulation Drop Down Menu

• Modulation Scale – The process of modulation causes a prescribed deviation of a signal in response to an input. The input is the modulation data source. Each modulation type has a defined nominal deviation. Modulation Scale modifies that nominal deviation either up (> 1) or down (< 1).

Step – Sets the desired Modulation Scale step, as shown in Figure 178. Modulation scale is a unitless, linear value.

Step: 0	
Brep.	

Figure 178: Modulation Scale, Step Window

- Enable Button Enables or disables the Modulator. If it is disabled, it doesn't modulate, and its output is effectively zero (0); Disabling modulation achieves Carrier only
- Filter This is displayed when a legacy PSK modulation (BPSK, QPSK, OQPSK, UQPSK, or DPM) is selected. It provides control over phase trajectory shaping, with 0.0 being no shaping, and 1.0 being maximum shaping.
- Airlink Data Rate Effective data rate going into the modulator based on the source data rate and encoder settings, such as LDPC, STC, etc., in Mbps

#### **Modulator Hamburger Menu**

Provides access to additional parameters used to configure modulation Phase and Gain parameters. The Modulator Hamburger Menu, Figure 180, is accessed via the icon on the Modulator MOD0 panel, as shown in Figure 175.

Modulator MOD0		<b>-</b> ↓ ×
≡ 🛛		🚯 🥸
Data Source Modulation Modulation Scale	n: PCMFM	<ul><li></li><li></li><li></li><li></li><!--</th--></ul>
Airlink Data Rate	: 1.000000	
Modulator M	Modulator M	Summer SUM0

Figure 179: Modulator MOD0 Panel

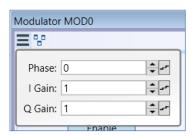


Figure 180: Modulator Hamburger Menu

• Phase (in degrees) – This sets the base phase offset of the modulator output. This is most commonly used when creating an AQPSK signal by summing two BPSK signals using two different modulators. One modulator would have a base phase offset of 90 degrees.

Step – Sets the desired step size, in degrees

• I Gain – The modulator baseband output consists of both I and Q data. I and Q gain sets the relative gain of each, with a default of one (1) or unity. It is a unitless/scalar value. This is an advanced capability.

It may be used for UQPSK, AUQPSK, or for "basic STC" (per the same-named block diagram) which has no other means of setting different amplitudes on the two ("top" and "bottom") STC signals.

Step – Sets the desired step size

• Q Gain – Refer to I Gain

The Modulator panel is associated with the Modulation block in the RA signal generation section, as shown in the diagram in Figure 181.

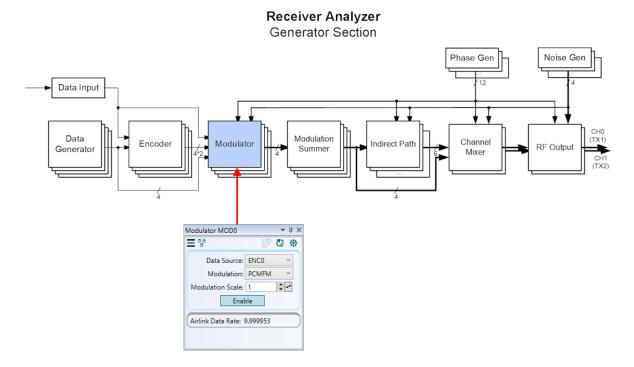


Figure 181: Signal Generation, Modulation – Modulator Panel

### 6.4.7 Summer SUM0-SUM3

Modulation Summers are tied to Modulator outputs. The Summer accepts modulated signals from two separate modulator sources and combines them into a single signal path. This is useful for combining two BPSK signals into AQPSK or combining the top and bottom signals of STC. All modulated signals must pass through a Summer. If modulation summing is not necessary, this is strictly a passthrough module.

Summer outputs connect to the Channel Mixer inputs through both direct and indirect paths. The direct path is always connected and is not configurable.

The View menu provides access to the Summer panel, as shown in Figure 182.

The Summer panel, shown in Figure 183, is used to set the two source modulators. Using the drop down menu, select from None, MOD0, MOD1, MOD2, or MOD3, for each source modulator.

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## 3<sup>rd</sup> Generation Receiver Analyzer

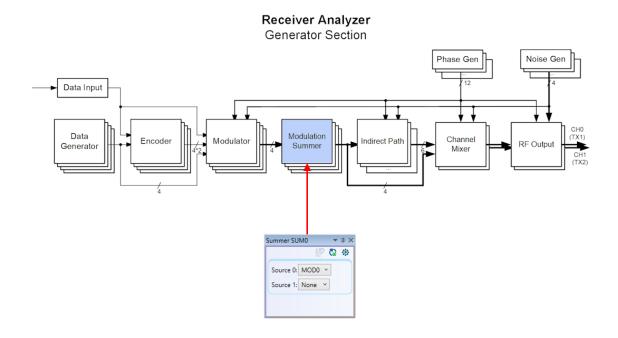
**-** ₽ × وي 🚯

						1				
View Project Co	nnection (	Options	Tools	Window	Help					
BERT Control		•								
BERT Group		•								
BERT Monitor		•				п				
Channel Mixe	r	•					Summer SU	M0		`
Data Generato	or	•								Č,
Data Sync		•					Source 0:	MODO	v	
DQE		•								
Encoder		•					Source 1:	None	~	
Indirect Path		•						None		
IO		•						MOD0		
Measurement		•						MOD1		
Modulator		•						MOD2		
Output		•					l	MOD3		
Output Chann	els	•								
Phase Genera	tor	•								
Phase Genera	tor Group	•								
Status Output	s	•					Modulator	M M	odulator M	Summer S
Summer		•	Su	mmer SUM	10		wouldtor			_ Summer St
			Su	mmer SUM	11					
			Su	mmer SUM	12					
			Su	mmer SUM	13					

Figure 182: Summer Drop Down Menu

Figure 183: Summer SUM0 Panel

The Summer panel is associated with the Modulation Summer block in the RA signal generation section, as shown in the diagram in Figure 184.



#### Figure 184: Signal Generation, Modulation Summer – Summer Panel

#### 6.4.8 Indirect Path

In wireless communication, multipath is a propagation phenomenon that results in radio signals reaching the receiving antenna by two or more paths. Causes of multipath include reflection from water bodies and terrestrial objects such as mountains and buildings. Multipath may cause constructive or destructive interference (in other words, fading).

Each path taken from a transmitting antenna to a receiving antenna is generally referred to as a ray. Each ray is affected by its propagation path and may have different frequency, phase, amplitude, and time delay.

Each Indirect Path (IPATH) may have as its source a Summer output and may be used as a Channel Mixer input. Generally speaking, the IPATH allows modifying the frequency, phase, and delay of a signal. This functionality mirrors channel effects due to signal reflection from a surface located in the vicinity of the direct propagation path.

The most common use of this functionality is to emulate multipath, where the associated Summer output represents the line-of-sight, or direct, path. Using a single IPATH together with the Summer output creates 2-ray multipath; using multiple IPATHs together with the Summer output creates 3-ray and higher multipath.

Likewise, IPATHs can be used to emulate differential channel effects on the two orthogonal component signals used in STC modulation. Here, the frequency, phase, and delay of one component signal may be varied relative to the other. STC multipath may also be emulated using additional IPATHs.

The View menu provides access to the Indirect Path panel, as shown in Figure 185.

The Indirect Path panel, shown in Figure 186, is used to set Mode parameters, as well as those for Source, Frequency, Phase, Delay, and Mode. A menu, accessed via the Hamburger icon, allows selection of a Phase Generator source.

### 3<sup>rd</sup> Generation Receiver Analyzer

View	Project	Connection	Options	Tools	Window	Help	
	BERT Cont	rol	•				
	BERT Grou	р	•				
	BERT Moni	tor	•				
	Channel M	lixer	•				
	Data Gene	rator	•				
	Data Sync		•				
	DQE		•				
	Encoder		•				
	Indirect Pa	th	۰.	Inc	direct Path	IPATH0	
	Ю		•	Inc	direct Path	IPATH1	
	Measurem	ent	•	Inc	direct Path	IPATH2	
	Modulator		•	Inc	direct Path	IPATH3	
	Phase Gen	erator	•	Inc	direct Path	IPATH4	
	Phase Gen	erator Group	•	Inc	direct Path	IPATH5	

Indirect Path IPA	TH0	<b>▼</b> ∓ X
三部		ا 🕼
Mode Off Static Dynamic Pause	Source: None Freq: 0 Phase: 0 Delay: 0	

Figure 185: Indirect Path Drop Down Menu

Figure 186: Indirect Path IPATH0 Panel

- Mode Sets the operating mode for this indirect path. There are three mode options within IPATH: Off, Static, and Dynamic.
  - Off Setting the IPATH mode to Off effectively bypasses the IPATH, resulting in a copy of its input signal with no frequency, phase, or delay offset.
  - Static Setting the IPATH mode to Static disables the frequency offset, resulting in a static phase and delay offset.
  - Dynamic Setting the IPATH mode to Dynamic enables the frequency offset, which advances the phase at the selected rate.
  - Pause Button In Dynamic mode, clicking on Pause sets the phase to the selected value and holds it there.

In Dynamic mode, clicking Pause again allows the phase to advance at the selected rate.

• Source – Selects which Summer output to generate an indirect path from

Valid options are None, SUM0, SUM1, SUM2, or SUM3.

- Freq Frequency, in Hz; In Dynamic mode, this is the rate that the phase offset advances Step – Sets the desired step size and the step size unit (Hz, kHz, or MHz)
- Phase The base phase offset of the indirect path Step – Sets the desired phase step, in degrees
- Delay The delay, in nanoseconds, of the signal through the indirect path Step – Sets the delay step in nanoseconds or µs

			Indirect Path I	PATH0	<b>→</b> ‡ ×
Indirect Path IPATH0		▼ ₽ ×	= 99		🕸 🕥
Mode Off Static Dynamic Pause Delay	: None : SUM0		Phase Gen: O Static Dynam Pause	PG4     ✓       None     PG0       PG0     PG1       PG2     PG3       PG3     PG4       PG5     PG6       PG7     PG8       PG9     PG10	
			nitor	PG11	▼ ‡ ×

Figure 187: Source Drop Down Menu

Figure 188: Phase Gen Drop Down Menu

#### Indirect Path Hamburger Menu

• Phase Gen – Setting the phase source in the IPATH Hamburger menu is an advanced capability, explained in the general description of Phase Generators in section 6.4.12.

The Indirect Path panel is associated with the Indirect Path block in the RA signal generation section, as shown in the diagram in Figure 189.

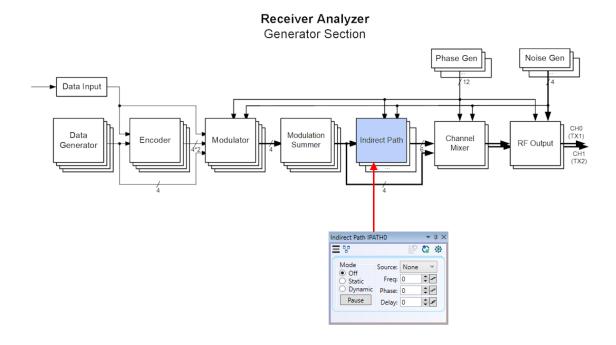


Figure 189: Signal Generation, Indirect Path – Indirect Path Panel

#### 6.4.9 Channel Mixer

All modulated signals, direct and indirect, are connected to a Channel Mixer. The Channel Mixer scales, and then combines, all signals into a single signal path. This process emulates the reception of multiple signals, whether direct rays, reflected rays, or interfering sources at a receive antenna. That data path can then have an amplitude offset/sweep applied to the signal, which can be used to emulate the effects of conical scan antenna tracking.

Each Channel Mixer output is directly tied to the corresponding RF Output.

The View menu provides access to the Channel Mixer panel, as shown in Figure 190.

The Channel Mixer panel, shown in Figure 191, is used to select the signal sources that make up the final output signal and set their relative scales. A menu, accessed via the Hamburger icon, provides the ability to set parameters for signal summing mode and amplitude modulation.

View	Project Connection	Options	Tools Window Help
	BERT Control	•	
	BERT Group	+	
	BERT Monitor	•	
	Channel Mixer	•	Channel Mixer CH0
	Data Generator	+	Channel Mixer CH1

Figure 190: Channel Mixer Drop Down Menu

### 3<sup>rd</sup> Generation Receiver Analyzer

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SUM1 1
SUM3 1
IPATH1 1
IPATH3 1
IPATH5 1

Figure 191: Channel Mixer CH0 Panel

#### **Channel Mixer Hamburger Menu**

The Channel Mixer Hamburger menu is used to set parameters for signal summing mode and amplitude modulation, as shown in Figure 192.

Channel Mixer CH0		џ×	(	Channel Mixer CH0		ч×
三部	i? 🗘	۰		<b>-</b> %	ii? 🖏	<u>ش</u>
Summing Mode: Total Powe	r ~			Summing Mode:	Total Power 🗸	
Frequency Offset Source: PG10	~			Frequency Offset Source:	Magnitude 1	
AM Source: PG2	~			AM Source:	Total Power	
AM Frequency: 0	÷.			AM Frequency:	0	
AM Waveform: Square	~			AM Waveform:	Square Y	
AM Depth				AM Depth		
By Index: 0	÷ 4			By Index: 0	÷.	
By Ratio: 0	÷.			By Ratio: 0	÷.	

Figure 192: Channel Mixer Hamburger Menu

Figure 193: Channel Mixer, Summing Mode Drop Down Menu

- Summing Mode Signal summing mode relative to output power; The Signal Summing mode affects scaling of output signals as well as scaling of additive noise (AWGN).
  - Total Power Signals are scaled such that the final signal results in the output power set in the RF Output panel; Selecting Total Power scales the input signals such that their sum equals the total output power in the RF Level setting. Noise is added relative to this power. This mode is most useful for composite signals like STC.
  - Magnitude 1 All signals are summed assuming they have an incoming relative signal level of 1.0. This may result in a final output signal that may be higher (or clipped) or lower than the output power set in the RF Output panel. Noise is added relative to a power level of 1.0. This mode is most useful for multipath, so noise can be scaled relative to the direct path regardless of whether other rays add constructively or destructively.

• Frequency Offset Source – Selects the Phase Generator used to generate the carrier suppression offset; This should never be changed.

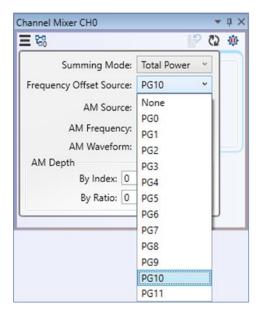


Figure 194: Frequency Offset Source Drop Down Menu

• AM Source – Enables amplitude modulation with selection of a Phase Generator from the drop down menu, as shown in Figure 195, or disables AM insertion by selecting None from the drop down menu.

Channel Mixer CH0	<del>▼</del> ∓ ×
三路	ii? 🗘 🔅
Summing Mode:	Total Power ~
Frequency Offset Source:	PG10 ~
AM Source:	PG2 ~
AM Frequency: AM Waveform: AM Depth By Index: 0 By Ratio: 0	None PG0 PG1 PG2 PG3 PG4 PG5
	PG6 PG7 PG8 PG9 PG10 PG11

Figure 195: AM Source Drop Down Menu

• AM Frequency – Sets the frequency of the amplitude modulation, in Hz, as shown in Figure 196 Step – Sets the step size, in Hz, kHz, or MHz

Channel Mixer CH0	<del>▼</del> ‡ ×	
三號	ii? 🖏 🕸	
Summing Mode:	Total Power 🗸	
Frequency Offset Source:	PG10 ~	
AM Source:	PG2 ~	
AM Frequency:	0 🗘	
AM Waveform:	Square Step: 0	Hz ~
AM Depth		Hz
By Index: 0	÷-1	kHz
By Ratio: 0	₹.	MHz

Figure 196: AM Frequency, Step Drop Down Menu

• AM Waveform - Sets the AM modulation waveform to either a sine or square wave, as shown in Figure 197

Channel Mixer CH0	<b>~</b> џ ×
田路	🛃 🗘 🌞
Summing Mode:	Total Power V
Frequency Offset Source:	PG10 ~
AM Source:	PG2 ~
AM Frequency:	0 + -
AM Waveform:	Square Y
AM Depth	Sine
By Index: 0	Square
By Ratio: 0	÷ 4

Figure 197: AM Waveform, Drop Down Menu

• AM Depth – Sets the AM modulation index; the AM depth can be set in either a percentage or a ratio in dB. They both set the same parameter, the AM depth. Setting one will modify the other.

Step By Index – Sets the step size as a percentage, as shown in Figure 198.

Channel Mixer CH0	<del>▼</del> ∓ ×	
三 88	ii? 🖏 🌞	
Summing Mode:	Total Power 🗸	
Frequency Offset Source:	PG10 ~	
AM Source:	PG2 ~	
AM Frequency:	<b>1</b>	
AM Waveform:	Square Y	
AM Depth		
By Index: 0	÷-	
By Ratio: 0	Step: 1	%

Figure 198: AM Depth, Step Drop Down Menu

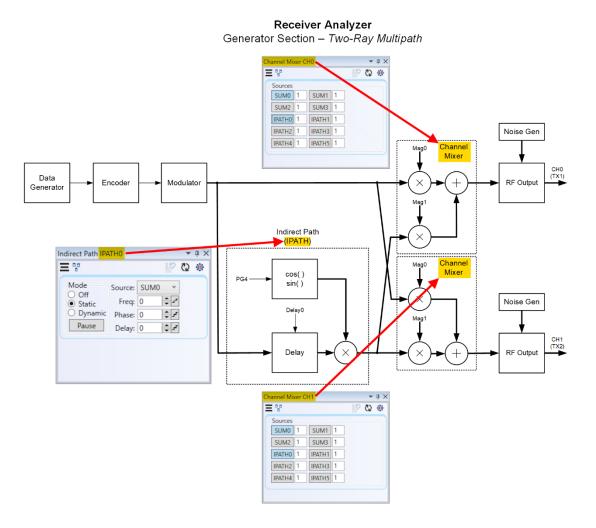
Step By Ratio – Sets the step size as a ratio in dB, as shown in Figure 199.

Channel Mixer CH0		<b>→</b> ₽ ×	
三 88	12 (	2 🕸	
Summing Mode:	Total Power 🗸		
Frequency Offset Source:	PG10 ~		
AM Source:	PG2 ~		
AM Frequency:	<b>ک</b> ا		
AM Waveform:	Square <sup>v</sup>		
AM Depth			
By Index: 0	÷.		
By Ratio: 0	÷		
	St	ep: 1	d

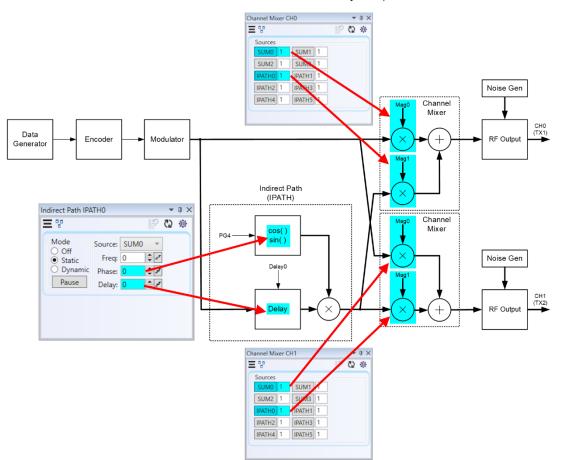
Figure 199: AM Depth, By Ratio, Step Drop Down Menu

The diagram shown in Figure 200, illustrates how the IPATH and Channel Mixer panels relate to the Different Magnitude/Phase/Delay flow within the RA.

The diagram shown in Figure 201, shows the relationship of the parameters within the module to the block diagram.







**Receiver Analyzer** Generator Section – *Two-Ray Multipath* 

Figure 201: Example Data Flow (STC), Relationship of Parameters Within the Module to the Diagram

The Channel Mixer panel is associated with the Channel Mixer block in the RA signal generation section, as shown in the diagram in Figure 202.

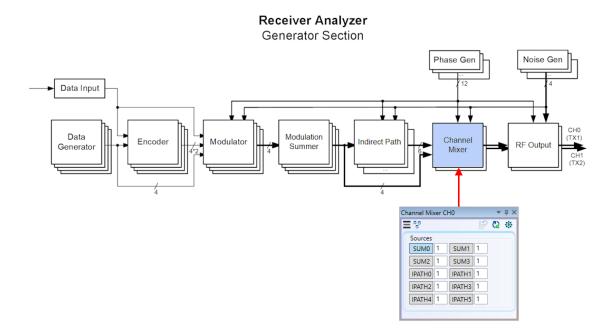


Figure 202: Signal Generation, Channel Mixer– Channel Mixer Panel

### 6.4.10 Output

The View menu provides access to the Output > Coordinator panels, as shown in Figure 203.

#### 6.4.10.1 Coordinator

The Coordinator controls all *global* pending changes. The Coordinator panel functions just like the buttons described in section 6.2.3.4.4, *except* the buttons in this panel apply to **all** panels.

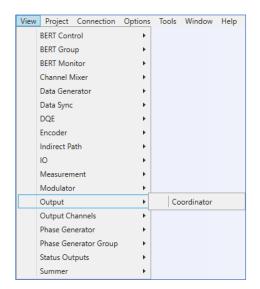


Figure 203: View, Output Drop Down Menu

The Coordinator panel, shown in Figure 204, consists of three sections: Revert Changes button, Apply Changes button, and Auto Apply Changes button.

- Revert Changes Button Greyed out if there are no pending changes, yellow when changes pending; clicking on yellow button restores previous settings
- Apply Changes Button Grey and disabled in Auto Apply mode, red exclamation point when changes pending, green check mark when no changes pending
- Gear/Auto Apply Button Enabled when red 'pause' symbol is present; disabled when green triangle is present



Figure 204: Coordinator, Revert Changes and Apply Changes in Yellow

### 6.4.11 Output Channels

Output Channels control the conversion of the digital signal to an actual RF output signal. It sets the carrier frequency, output power, and noise level.

The View menu provides access to the Output Channels panels, as shown in Figure 205.

The Channel CH0 and Channel CH1 panels, shown in Figure 206, are used to set Frequency, Power, and AWGN parameters. A menu, accessed via the Hamburger icon, provides the ability to select which Bit Rate is used to set the AWGN noise level in Eb/N0 and the Noise Source.

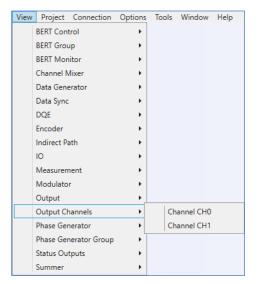


Figure 205: View, Output Channels Drop Down Menu

Channel CH0	▼ ‡ ×	Channel CH1	- <b>∓</b> ∓ ×
Ξ	0	Ξ	0
Frequency: 2225	S CO	Frequency: 2225	69 L \$
Power: -50 🗘	5 (O)	Power: -50	S L
AWGN: 100 🗘	ч 🙁	AWGN: 100	S T ⇔
RF	୍ଷ	RF	0
Signal	୍ଷ	Signal	8

Figure 206: Channel CH0 and Channel CH1

• Frequency – Frequency, in MHz, of the RA output signal

Step - Sets the step size used by Frequency in Hz, kHz, or MHz

• Power – The power level output of the signal, in dBm, sent by the RA

Step - Sets the step size for the power level, in dBm

- AWGN AWGN (Additive White Gaussian Noise) may be digitally added to the RF output.
- Chainlink Icon Links the associated parameter between CH0 and CH1
- RF Button The RF button turns the entire RF output On/Off. When RF is off, the receiver being measured has no input signal of any kind. This control is useful when measuring without added noise, such as when using the receiver's front end to set the noise floor.
- Signal Button The Signal button turns the modulated signal On or Off, but it has no effect on AWGN generated by the RA. When Signal is off, the receiver being measured has input noise *x* dB below the Power level, where *x* is the AWGN setting in dB Eb/N0. This control is useful when measuring with added

noise. It can also be used to output noise only at a known level by setting the Power as desired and AWGN to 0 dB Eb/N0.

#### 6.4.11.1 AWGN Bit Rate

True Additive White Gaussian Noise (AWGN) has theoretically infinite bandwidth and infinite noise peaks. Practical implementation of the noise generator, however, imposes limits on bandwidth and peak noise.

Noise bandwidth is automatically adjusted based on bit rate to provide an essentially flat noise spectrum in the bandwidth of interest. No user settings are required for proper operation.

AWGN in Eb/N0 is generated relative to the bit width in the signal. When multiple bit rates are present, such as AQPSK, this determines which of the bit rates, highest or lowest, is used as the reference.

AWGN Bit Rate:	Highest 💙
Noise Source:	Highest
Noise Source.	Lowest

Figure 207: Channel CH0/CH1 Hamburger Menu, AWGN Bit Rate Drop Down Menu

### 6.4.11.2 Noise Source

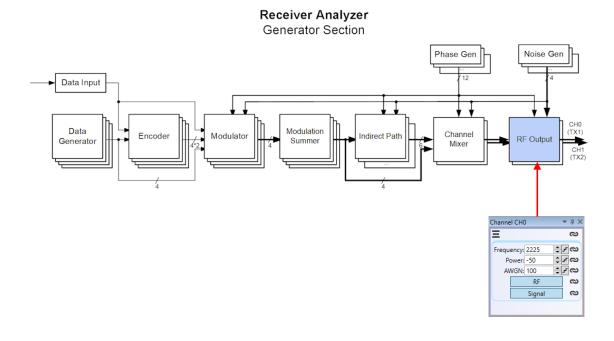
The RA provides a pool of Noise Generators, all with equal functionality. For most purposes, the RA firmware allocates Noise Generators as needed, and no user selection or special configuration is required. Advanced users or complex measurement scripts may reallocate Noise Generators to specific functions as desired. **However, extreme care must be taken to ensure required standard functionality is not disturbed.** 

The Noise Source drop down menu is used to select from four different noise sources within the RA hardware, or None. However, there is usually no need to change this from the default setting.

AWGN Bit Rate:	Highest	-
Noise Source:	NG2	-
RF	None	
	NG0	
	NG1	
	NG2	
	NG3	

Figure 208: Channel CH0/CH1 Hamburger Menu, Noise Source Drop Down Menu

The Output Channels panel is associated with the RF Output block in the RA signal generation section, as shown in the diagram in Figure 209.



#### Figure 209: Signal Generation, RF Output – Channel CH0 Panel

#### 6.4.12 Phase Generator PG0-PG9

Each Phase Generator has an output that represents either a static phase (for example, 90 degrees) or time-varying phase (for example, 10 Hz). The phase/frequency is programmable to provide automatic sweeping capability between two settable endpoints for a fixed or infinite number of iterations. This phase/frequency can be used in a variety of ways. Common uses include:

- Indirect Path Phase/frequency offset for multipath ray emulation
- Channel Mixer AM frequency

Less common or "behind the scenes" uses include:

- Modulator Deterministic data rate variation or modulation phase noise
- RF Output RF frequency offset relative to the RF synthesizer frequency

The RA provides a pool of Phase Generators, all with equal functionality. For most purposes, the RA firmware allocates Phase Generators as needed, and no user selection or special configuration is required. Advanced users or complex measurement scripts may reallocate Phase Generators to specific functions as desired. **However, extreme care must be taken to ensure required standard functionality is not disturbed.** 

Multiple Phase Generators may be placed into Operation Groups so that their initial phase can be synchronized. This is useful for measurements like break frequency, which uses two Phase Generators of equal frequency but 180 degrees out of phase to modulate the two RF output channels.

Noise Generators can also be used as phase sources in all places where Phase Generators may be used. This may be useful for generating random data rate or modulation phase noise, among other things.

The View menu provides access to the Phase Gen panel, as shown in Figure 210.

The Phase Generator Panel is used to set parameters for frequency sweeping. In general, the frequency sweep begins at a given frequency offset and starts sweeping up or down until it deviates from the center frequency by a limit amount, at which point it starts sweeping the reverse direction. Each progression between center and limit values is considered <sup>1</sup>/<sub>4</sub> of a sweep cycle. The process of sweeping between upper and lower limits continues until the selected number of sweep cycles has completed.

The Phase Gen panel, shown in Figure 211, sets parameters for Offset, Limit, Rate, Count, Direction, Start and Stop Position, and Mode. A menu, accessed via the Hamburger icon, provides the ability to set parameters for Frequency, Phase, and Operation Group.

All of this is advanced capability.

view	Project Connection Options	s loois window Help			
	BERT Control				
	BERT Group		Phase Gen PG0		▼⇔X
	BERT Monitor				
	Channel Mixer		<b>=</b> <del>2</del>		🕸 🕥
	Data Generator				
	Data Sync 🕨		0.6	0	Mode — Mode
	DQE •		Offset:	0 🗘 🕶	• Off
	Encoder •		Limit:	0	O On
	Indirect Path		Rate:	0	<ul> <li>Continuous</li> </ul>
	IO +				Enable
	Measurement •		Count:	0	
	Modulator •				
	Output +		Direction:	Up 🗸	Running
	Output Channels		Charle Danibian	Middle Y	Sweeps: 0
	Phase Generator	Phase Gen PG0	Start Position:	Middle *	Sheepsire
	Phase Generator Group	Phase Gen PG1	Stop Position:	Middle ~	Restart
	Status Outputs	Phase Gen PG10			
	Summer •	Phase Gen PG11			
		Phase Gen PG2	_		
		Phase Gen PG3	0	tep: 0	MHz Y
		Phase Gen PG4	3	tep. 0	
		Phase Gen PG5		Enable	Hz
		Phase Gen PG6			kHz
		Phase Gen PG7	~	Running	MHz
		Phase Gen PG8			
		Phase Gen PG9			

Figure 210: View, Phase Generator Drop Down Menu



• Offset – The Offset value may be set manually, and during a sweep it updates to indicate the current sweep frequency offset from center.

Step - Sets the offset step size and unit (Hz, kHz, or MHz), as shown in Figure 211

- Limit Sets the allowed deviation from the center frequency
- Rate Sets the rate at which the frequency sweeps in both the up and down directions
- Count Sets the count of sweep cycles that occurs prior to sweep termination
- Mode There are three sweep modes: Off, On, and Continuous.

## 3<sup>rd</sup> Generation Receiver Analyzer

- Off Disables the frequency sweep and sets the Offset to 0.0 MHz.
- On Allows the frequency sweep to proceed for the selected number of sweep cycles
- Continuous Overrides the Count value and enables sweeping for an indefinite time
- Enable Button Alternately enables and pauses the sweep
- Direction Selects which direction (Up/Down) the sweep will go when a sweep is restarted
- Start Position Selects the starting frequency offset (Upper limit/Middle/Lower limit) when a sweep is restarted
- Stop Position Selects the stopping frequency offset (Upper limit/Middle/Lower limit). Any sweep pattern with different Start and Stop Positions will run for the integer number of cycles specified, plus as many <sup>1</sup>/<sub>4</sub> cycles as needed to terminate at the Stop Position.
- Sweeps Displays the current number of remaining sweep cycles
- Restart Button Resets the sweep starting frequency and sweep count

Phase Gen PG0		<b>▼</b> -⊐ X
= 2		() ()
		Mode
Offset:	0 🗘 🕶	• Off
Limit:	0	🔾 On
Rate:	0	<ul> <li>Continuous</li> </ul>
Count:	0	Enable
Direction:	Up 🖌	Running
Start Position:	Up	Sweeps: 0
Stop Position:	Down	Restart

Phase Gen PG0		▼ += X
Ξ.°°		C) 🔅
Limit:	0	Mode Off On Continuous Enable
Direction:	Up Y	C Running
Start Position:	Middle 💙	Sweeps: 0
Stop Position:	Upper	Restart
	Middle	)
	Lower	

Figure 212: Phase Gen, Direction Drop Down Menu



## 3<sup>rd</sup> Generation Receiver Analyzer

Phase Gen PG0		→ ×
		C) 🔅
Offset:	0	(Mode
		• Off
Limit:	0	O On
Rate:	0	<ul> <li>Continuous</li> </ul>
Count:	0	Enable
Direction:	Up ~	Running
Start Position:	Middle Y	Sweeps: 0
Stop Position:	Middle 🖌	Restart
	Upper	
	Middle	
	Lower	

Figure 214: Phase Gen, Stop Position Drop Down Menu

Phase Gen PG0		▼ +⊐ X
= :		C2 🔅
Frequency MHz: Phase:		
Operation Group:	None	tinuous
	None	
Direction: Up	Group 0	nning
Start Position: Mid	Group 1	os: O
	Group 2	a de la della d
Stop Position: Mic	Group 3	estart
	Group 4	
	Group 5	

Figure 215: Phase Gen Hamburger Menu, Operation Group Drop Down Menu

#### Phase Gen Hamburger Menu

The Hamburger menu, is used to set the base Frequency and Phase, as shown in Figure 215. These are added to the current sweep frequency/phase and may be used to offset the sweep center frequency and/or starting phase.

• Frequency – Set the time-varying phase in Hz

Step – Sets the frequency unit and step size in Hz, kHz, or MHz

• Phase – Sets a base-phase offset in degrees

Step – Sets the phase size in degrees

• Operation Group – Configures Phase Generators into specific groups to synchronize their phase to each other

The Phase Gen panel is associated with the Phase Gen block in the RA signal generation section, as shown in the diagram in Figure 216.

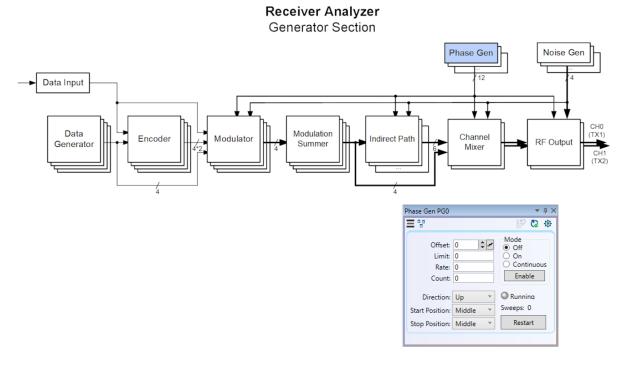


Figure 216: Signal Generation, Phase Gen – Phase Gen Panel

### 6.4.12.1 Phase Generator Operation Group Panel

Operation Groups are a RA hardware construct that the RA hardware uses to coordinate the operation of multiple instances of the same module type. For example, Phase Generators can be configured to synchronize their phase to each other. Note, this differs from Control Groups, which are an RxAn software construct that allows multiple instances to be configured identically through the GUI (refer to section 6.4.1).

The View menu provides access to the Phase Gen Operation Group panels, as shown in Figure 217.

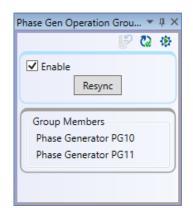
The Phase Gen Operation Group panel, shown in Figure 218, is used to enable all phase generators in the Operation Group and to resynchronize their phase.

- Enable Check Box Enables all of the phase generators in the Operation Group
- Resync Button Resynchronizes the phase of all of the phase generators in the Operation Group
- Group Members A list of phase generators that have been assigned to this Operation Group via the Phase Generator control panel Hamburger menu Operation Group parameter

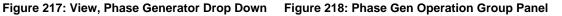


### 3<sup>rd</sup> Generation Receiver Analyzer

View	Project	Connection	Options	Tools	Window	w Help
	BERT Con	trol	•			
	BERT Grou	up				
	BERT Mor	nitor				
	Channel M	Aixer	•			
	Data Gen	erator	•			
	Data Sync		•			
	DQE		•			
	Encoder		•			
	Indirect P	ath	•			
	IO					
	Measuren	nent				
	Modulato	r				
	Output		•			
	Output Cl	hannels	•			
	Phase Ger	nerator	•			
	Phase Ger	nerator Group	•	Ph	iase Gen	Operation Group GRP0
	Status Ou	tputs	•	Ph	iase Gen	Operation Group GRP1
	Summer		•	Ph	iase Gen	Operation Group GRP2
				Ph	iase Gen	Operation Group GRP3
				Ph	ase Gen	Operation Group GRP4
				Ph	ase Gen	Operation Group GRP5



Menu



#### 6.4.13 Analyzer Functions

The RA has eight (8) clock and data inputs. The RA provides real-time status for all modules, including the following functions and indicators, as shown in Figure 219.

- BERT Monitor - Bit error rate, error count, errored seconds, link availability, and equivalent estimations of the activity based on DQM; relative to BERT restart
  - Measurement Interval Displays Elapsed time, bit rate, and bit count; relative to BERT restart ٠
- BERT Indicators Data inversions, bit errors, pattern sync loss, clock slip, DQE sync loss, and estimated • bit errors based on DQM
- BERT Control Sets data patterns, time limits, and bit error count limits •
- BERT Operation Group Synchronizes the completion of all the BERTs in the group •
- DQE Extracts DQM information from DQE frames to facilitate DQM verification testing and estimated • error counts, even with unknown data patterns

<u>File Edit View Project Connect</u>	on <u>O</u> ptions <u>T</u> ools <u>W</u> indow <u>H</u> elp	
DQE CH1I 👻 A 🗙	BERT CH1I Control 🗢 🕂 🗙	BERT CH1I Monitor 👻 🗸 🗙
53 🔛 🖓 🔅	三端 🔮 🖏	Measured Bit Error Rate
Frame Length: Auto Detect Frame Detect: ✓ Frame Counts: ✓ Decapsulate: Auto ✓ Reset ● Locked ● Decap Detected Length Frames 4096 30266663 Locked 30266663	Pattern: PN23 Time Limit: 1 Count Limit: 0 Error Gating: Continuous Restart Stop	<b>1.697E-03</b> Estimated Bit Error Rate 1.697E-03         Time:       Bit Rate         00:01:10:11.9       9.999994         4.21e+10         Errors       Delta Err         Errors       Delta Err         Errors       E.Delta Err         Errors       Sign         QUE       Restart         Errors       Silp

Figure 219: Examples of BERT Related Panels

#### 6.4.13.1 BERT Functions

BERT functionality is divided between BERT configuration and BERT monitoring. BERT configuration is separate from BERT monitors because BERTs are commonly configured in a control group, whereas BERT monitors are only useful if displayed separately.

In addition to providing basic BERT status information and indicators, the BERT Channel Monitor and BERT Channel Control panels provide equivalent estimations of the status data based on DQM.

### 6.4.13.1.1 BERT Monitor Panel

The BERT Monitor panel displays the status of the BERTs.

The View menu provides access to the BERT Monitor panels, as shown in Figure 205.

V	/iew	Project	Connection	Options	То	ols	Window	Help	
		BERT Cont	trol	•					
		BERT Grou	qu	•					
		BERT Mon	nitor	×		BEF	RT BSSI Mo	nitor	
		Channel N	/lixer	•		BEF	RT BSSQ M	onitor	
		Data Gene	erator	•		BEF	RT CH1I M	onitor	
		Data Sync		•		BEF	RT CH1Q N	Ionitor	
		DQE		•		BEF	RT CH2I M	onitor	
		Encoder		•		BEF	RT CH2Q N	Ionitor	
		Indirect Pa	ath	•		BEF	RT CMBI M	onitor	
		Ю		•		BEF	RT CMBQ N	Monitor	

Figure 220: View, BERT Drop Down Menu

Five measured parameters from the BERT Channel Monitor panel are described here and highlighted in Figure 221.

- Bit Error Rate Continuous display of the bit error rate for the duration of a measurement
- Error Count Continuous display of the channel error count for the duration of a measurement
- Delta Err The number of errors detected since the last screen update. When all of the deltas are added up, the result is the total number of errors.
- Err Secs Errored Seconds; the number of severely errored seconds in the measurement interval; A severely errored second is one where the bit error rate for that second exceeds the Errored Second Threshold set in the BERT Control Hamburger menu for that BERT.
- Avail Link Availability; An overall measure of link quality; It is a number between 0.0 and 1.0, and is

$$1 - \frac{errored \ seconds}{total \ seconds}$$

for the measurement interval.

Five parameters from the BERT Channel Monitor panel that provide *estimated* status information are described here and highlighted in Figure 221. 'E.' in the BERT area is an abbreviation for 'Estimated'.

- Bit Error Rate Bit error rate, based on DQM extracted from DQE.
- E.Errors Bit errors based on DQM extracted from DQE
- E.Delta Err Indicates the number of errors since the last screen update. When all of the deltas are added up, the result is the total number of errors.
- E.Err Secs Errored Seconds; the number of severely errored seconds in the measurement interval based on the DQM
- E.Avail Link Availability; An overall measure of link quality based on the DQM

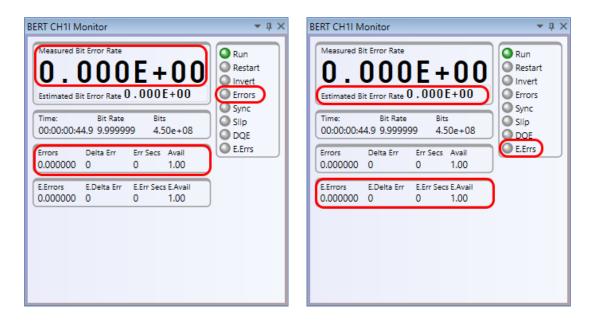
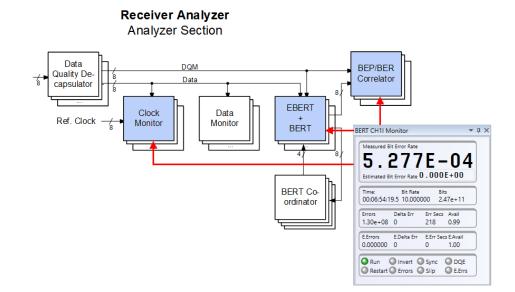


Figure 221: BERT Monitor with BERT Functions Circled

Figure 222: BERT Monitor with Estimated BERT Functions Circled

The BERT Monitor panel is associated with the Clock Monitor, EBERT + BERT, and BEP/BER Correlator blocks in the RA analyzer section, as shown in the diagram in Figure 223.



#### Figure 223: Analyzer, Clock Monitor/EBERT-BERT/BEP/BER Correlator – BERT Monitor Panel

The measurement interval displays show information about the current measurement interval.

- Time Displays the elapsed time for the measurement interval, in the format Days Hours, Minutes, Seconds, Milliseconds
- Bit Rate Continuous display of the channel bit rate for the measurement interval
- Bits Continuous display of the channel bit count for the measurement interval

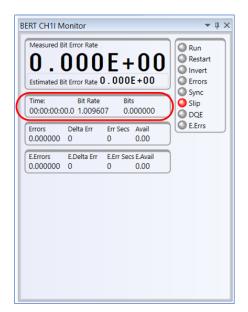


Figure 224: BERT Monitor with Measurement Interval Displays Circled

### 6.4.13.1.1.1 BERT Status Indicators

Indicators in the BERT Channel Monitor panel are described here and are shown in Figure 225. All inactive indicators are grey.

- Run Active green, the BERT is running (measuring)
- Restart Active green, the BERT has restarted, either from a manual restart or after the completion of a time or count limit
- Invert Data Inversion; When the data is inverted, the indicator is active blue.
- Errors Bit Errors; Active red means bit errors were detected in the last observation interval.
- Sync Pattern Sync Loss; Active red means the BERT has lost synchronization with the specified pattern.
- Slip Clock Slip; BERT slips are detected when the received data clock either advances or retards relative to its associated source clock by a pre-programmed amount. If Slip is intermittently indicated, the receiver output clock may have a relatively high degree of jitter. If Slip is continuously indicated, the receiver may have lost lock on its RF input signal. An active red indicator means the incoming clock and the transmit clock are not synchronous (slipping past one another). The relative clock source is selected in the associated BERT Control Panel's Hamburger Menu.
- DQE An active red indicator means the BERT Data Quality Decapsulator has lost DQE frame lock.
- E.Errs Estimated Errors; An active red indicator means the BERT error estimator has estimated that there are errors.

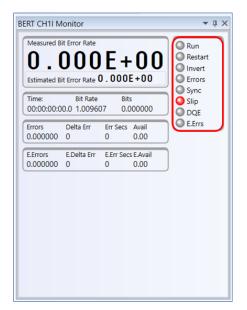


Figure 225: BERT Monitor with BERT Indicators Circled

### 6.4.13.1.2 BERT Control Panel

The View menu provides access to the BERT Control panels, as shown in Figure 226.

View	Project Connection	Options To	ools Window Help
	BERT Control	Þ	BERT BSSI Control
	BERT Group	•	BERT BSSQ Control
	BERT Monitor	•	BERT CH1I Control
	Channel Mixer	•	BERT CH1Q Control
	Data Generator	•	BERT CH2I Control
	Data Sync	•	BERT CH2Q Control
	DQE	•	BERT CMBI Control
	Encoder	•	BERT CMBQ Control

Figure 226: View, BERT Control Drop Down Menu

The BERT Control panel, shown in Figure 227, is used to set data patterns, time limits, and bit error count limits. A menu, accessed via the Hamburger icon provides fields for Errored Seconds Threshold, the Clock Slip Reference, and BERT Operation Group.

## 3<sup>rd</sup> Generation Receiver Analyzer

BERT CH1I C	ontrol		-	ņх
三 %			C2	<u>ښ</u>
Patter	n: PN23			~
Time Lim	it: 1			
Count Lim	it: 10000		Error	~
Gatin	g: Continuo	us		~
F	lestart	St	ор	

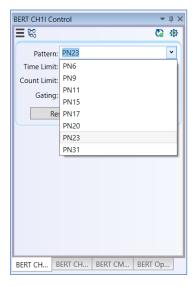


Figure 227: BERT CH1I Control Panel

Figure 228: BERT CH1I Control, Pattern Drop Down Menu

- Pattern Sets the data pattern used by the BERT; This is a user pattern or a pseudorandom pattern that repeats based on the chosen pattern/sequence (a shorter pattern looks more regular, a longer pattern looks more random).
  - PN6 Pseudorandom pattern 2<sup>6</sup>-1 bits in length
  - PN9 Pseudorandom pattern 2<sup>9</sup>-1 bits in length
  - PN11 Pseudorandom pattern 2<sup>11</sup>-1 bits in length
  - PN15 Pseudorandom pattern 2<sup>15</sup>-1 bits in length
  - PN17 Pseudorandom pattern 2<sup>17</sup>-1 bits in length
  - PN20 Pseudorandom pattern 2<sup>20</sup>-1 bits in length
  - PN23 Pseudorandom pattern 2<sup>23</sup>-1 bits in length
  - PN31 Pseudorandom pattern 2<sup>31</sup>-1 bits in length
  - Any binary pattern between 2 and 32 bits may be typed directly into the Pattern field.

### 3<sup>rd</sup> Generation Receiver Analyzer

BERT CH1I C	ontrol		<b>→</b> ậ >
≡₩			🖏 🎲
Patterr	n: PN23		~
Time Limi	t: 1		
Count Limi	t: 0		Error 🛩
Gating	g: Continuo	us	Bit
P	estart	Sto	Error
	estart	510	E.Error
BERT CH	BERT CH	BERT CM E	ERT Op

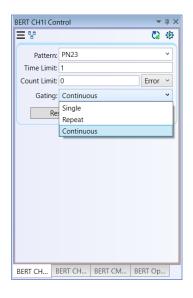


Figure 229: BERT CH1I Control, Error Drop Down Menu

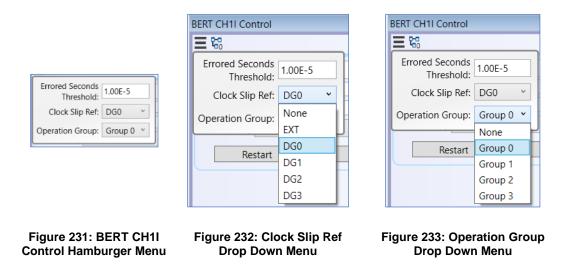
Figure 230: BERT CH1I Control, Gating Drop Down Menu

- Time Limit (In seconds) Sets a specific time limit for the measurement interval
- Count Limit (Bits) Sets a specific number of bits, bit errors, or estimated bit errors on which to terminate the measurement interval
  - Count Limit Drop Down Menu
    - Bit The interval is complete after the number of Count Limit bits
    - Error The interval is complete after the number of Count Limit errored bits
    - E.Error The interval is complete after the number of Count Limit estimated error bits
- Gating Determines the BERT interval
  - Single Runs a single measurement interval; The BERT must be restarted manually.
  - Repeat Automatically restarts the BERT when an interval completes
  - Continuous Continuously runs the BERT unless manually restarted or Count and Time limits are ignored
- Restart button Starts (or Restarts) the BERT interval
- Stop button Stops the BERT

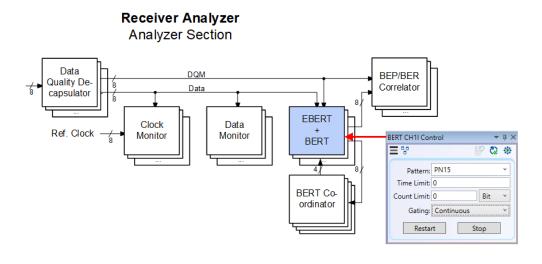
#### **BERT Control Hamburger Menu**

The BERT Control Hamburger menu is used to set three additional BERT parameters: Errored Seconds Threshold, Clock Slip Reference, and Operation Group, as shown in Figure 231.

- Errored Seconds Threshold The bit error rate within a one second span that defines the second to be errored; Used to determine link availability
- Clock Slip Ref Timing reference used to determine clock synchronization (slip); this should match the data source for the generated signal
- Operation Group The BERT Operation Group this BERT is a member of, if any



The BERT Control panel is associated with the EBERT + BERT block in the RA analyzer section, as shown in the diagram in Figure 234.



#### Figure 234: Analyzer, EBERT-BERT – BERT Control Panel



#### 6.4.13.1.3 BERT Operation Group Panel

BERT Operation Groups are used to synchronize the completion of all the BERTs in the group. When the operation group is complete, all BERTs in the group are stopped. This allows automated data collection from multiple BERTs.

Operation Groups are a RA hardware construct that the RA hardware uses to coordinate the operation of multiple instances of the same module type. For example, BERTs can be configured to declare completion as a set. Note, this differs from Control Groups, which are an RxAn software construct that allows multiple instances to be configured identically through the GUI (refer to section 6.4.1).

The View menu provides access to the BERT Operation Group panels, as shown in Figure 235.

The BERT Operation Group panel, shown in Figure 236, is used to set parameters for BERT Operation Group Completion Mode.

• Completion Mode - Drop down menu selections Any or All

Any specifies the measurement interval of the operation group complete when any of the BERTs in the group are complete.

All specifies the measurement interval of the operation group complete when all of the BERTs in the group are complete.

- Restart Button Restarts all BERTs in the BERT operation group
- Stop Button Stops all BERTs in the BERT operation group
- Running Indicator When this indicator is active green, the BERTs in the operation group are running/enabled.
- Group Members List of BERTS that have been assigned to this operation group using the Operation Group drop down menu in the BERT Control panel Hamburger menu

View	Project Connection	Options	Tools Window Help
	BERT Control	•	
	BERT Group	Þ	BERT Operation Group GRP0
	BERT Monitor	•	BERT Operation Group GRP1
	Channel Mixer	•	BERT Operation Group GRP2
	Data Generator	+	BERT Operation Group GRP3

Completion Mode: All   Restart Stop  Running Group Members BERT CH11 BERT CH12 BERT CH21 BERT CH20 BERT CMB1 BERT CMB0 BERT BSS1 BERT BSS0	BERT Operation Group G	RPO 🔻 🕂 🗙
Restart     Stop       Running     Group Members       BERT CH11     BERT CH12       BERT CH21     BERT CH21       BERT CH2Q     BERT CM20       BERT CMB1     BERT CMB2       BERT BSS1     BERT BSS1		¢ (۵)
Running     Group Members     BERT CH11     BERT CH1Q     BERT CH2I     BERT CH2Q     BERT CH2Q     BERT CMBI     BERT CMBQ     BERT BSSI	Completion Mode: All	¥
Group Members BERT CH11 BERT CH1Q BERT CH21 BERT CH2Q BERT CMB1 BERT CMBQ BERT BSS1	Restart	Stop
	Group Members BERT CH11 BERT CH1Q BERT CH2I BERT CH2Q BERT CMBI BERT CMBQ BERT BSSI	

Figure 235: View, BERT Group Drop Down Menu

Figure 236: BERT Operation Group Panel

The BERT Operation Group panel is associated with the BERT Coordinator block in the RA analyzer section, as shown in the diagram in Figure 237.

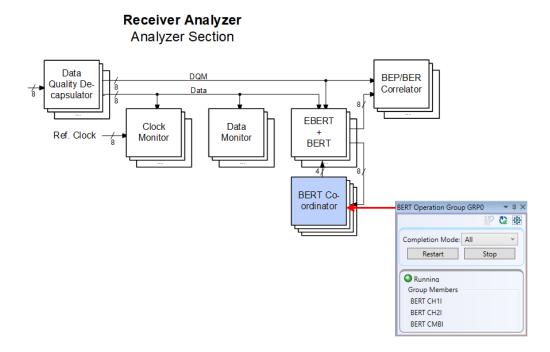


Figure 237: Analyzer, BERT Coordinator - BERT Operation Group Panel

#### 6.4.13.2 Data Sync Panel

Data synchronization uses pattern matching to detect when the receiver output matches–or fails to match–a predefined pattern. Because the RA generates the RF signal into the receiver under test, it can measure the amount of time required from signal present to pattern match (sync time) and from signal loss to pattern mismatch (sync loss time, i.e., receiver latency). The difference of these, i.e., sync time minus latency, represents the true measure of the number of bits that may be lost from the end of a flat fade until the receiver produces usable data.

Supplied sync time measurement scripts change both RF outputs simultaneously, so sync time measurement for all inputs is initiated by a single timing event. However, in general use, the RA can produce potentially different RF outputs. Therefore, sync time measurement assumes that CH1 (and Combiner and BSS) inputs are driven from RF Output CH0; and that CH2 (and Combiner and BSS) inputs are driven from RF Output CH1. So, if the RF output signals are controlled independently, sync active and inactive time will only be measured for the preceding subsets of input channels.

The underlying mechanism for measuring sync time is based on a statistical model of errored data. It is highly robust across a broad range of error conditions as high as 1e-1 bit error rate (BER). However, measured results vary from trial to trial depending on several factors, including BER. Therefore, sync time should be measured over several trials, especially for modulations using block encoding (LDPC and STC). Moreover, because sync active time is measured from RF signal applied to valid data out of the receiver, this measurement includes any fixed latency within the receiver. To determine the true number of bits lost due to receiver synchronization, sync time should always be calculated as the measured sync time minus the average sync loss (latency) time.

The View menu provides access to the Data Sync panels, as shown in Figure 238.

### 3<sup>rd</sup> Generation Receiver Analyzer

The Data Sync panel, shown in Figure 239, is primarily intended to monitor sync and sync loss time. After being configured, the synchronization detector constantly monitors receiver synchronization. Any time RF or Signal is set active/inactive in the RF Output panel, the associated synchronization/latency time is measured and indicated in the Data Sync panel.

- Last The most recent event
- Active Average sync time
- Inactive Average sync loss (latency) time

The number of active/inactive events over which the average is calculated is also displayed as Count. All values are in units of bits.

View	Project	Connection	Options	Tools	Window	Help
	BERT Con	trol	•			
	BERT Grou	up	•			
	BERT Mor	nitor	•			
	Channel M	Aixer	•			
	Data Gen	erator	•			
	Data Sync	:	Þ	Da	ita Sync BS	SI
	DQE		•	Da	ita Sync BS	SQ
	Encoder		•	Da	ita Sync CH	11
	Indirect P	ath	•	Da	ita Sync CH	1Q
	Ю		•	Da	ita Sync CH	21
	Measuren	nent	•	Da	ita Sync CH	2Q
	Modulato	r	•	Da	ita Sync CM	1BI
	Phase Ger	nerator	•	Da	ita Sync CM	1BQ

Figure 238: Data Sync Drop Down Menu



Figure 239: Data Sync BSSI Panel

The synchronization detector is configured by selecting the pattern to detect, as shown in Figure 240. Note that, unlike the BERT, the pattern must be matched exactly; inverted patterns will not be detected. The pattern may be set explicitly in the Data Sync panel.

It may also be selected automatically. A menu, accessed via the Hamburger icon, provides the ability to set Pattern Follow to Auto, as shown in Figure 241. In this case, the pattern follows the pattern selection of the Data Generator selected in the BERT Control panel Hamburger menu Clock Slip Reference for the same channel. For example, suppose BERT CH11 has DG0 selected as the Clock Slip Reference, and DG0 is set to PN31. Then selecting Auto in the CH11 Data Generator Pattern Follow makes PN31 the CH11 data sync pattern.

- Pattern Sets the data pattern used by the RA; This is a fixed pattern or a pseudorandom pattern that repeats based on the chosen pattern/sequence (a shorter pattern looks more regular, a longer pattern looks more random)
  - PN6 Pseudorandom pattern 2<sup>6</sup>-1 bits in length
  - PN9 Pseudorandom pattern 2<sup>9</sup>-1 bits in length
  - PN11 Pseudorandom pattern  $2^{11}$ -1 bits in length

### 3<sup>rd</sup> Generation Receiver Analyzer

- PN15 Pseudorandom pattern 2<sup>15</sup>-1 bits in length
- PN17 Pseudorandom pattern 2<sup>17</sup>-1 bits in length
- PN20 Pseudorandom pattern 2<sup>20</sup>-1 bits in length
- PN23 Pseudorandom pattern 2<sup>23</sup>-1 bits in length
- PN31 Pseudorandom pattern 2<sup>31</sup>-1 bits in length
- Any binary pattern between 2 and 32 bits may be typed directly into the Pattern field.
- Reset Button Resets the Data Sync Active/Inactive statistics
- Synced Indicator When this indicator is active green, the data sync detect is active (sync pattern is matched).

SI	<b>▼</b> ↓ ×
	ن 🕼
PN23 🗸	Reset
PN6	
PN9	
PN11	
PN15	
PN17	
PN20	
PN23	
PN31	
	PN23 v PN6 PN9 PN11 PN15 PN17 PN20 PN22

Figure 240: Pattern Drop Down Menu

Pattern Follow:	Auto ~
Window Length:	4095
Threshold:	4.69999981
Arm Triggers	
RF CH0	On: 🗹 RF CH1 On: 🗹
Signal CH0	On: 🗹 🦳 Signal CH1 On: 🗹
AM Trigger CH0	On: 🗌 AM Trigger CH1 On: 🗌
Pattern Follow:	Auto
Pattern Follow: Window Length: Threshold:	
Window Length:	Auto
Window Length: Threshold: Arm Triggers	Auto
Window Length: Threshold: Arm Triggers RF CH0	Auto Explicit

Figure 241: Data Sync Hamburger Menu and Pattern Follow Drop Down Menu

The Data Sync Hamburger menu also allows Window Length and Threshold selections. These are advanced capabilities and should only be changed in consultation with Quasonix support.

Sync detect timing measures the number of bits required from an arming trigger event until data sync is detected. This is normally an RF or Signal 'On' event. Sync loss timing is the number of bits until data sync has been lost. It, by definition, starts from a synced condition.

There are three arming triggers from both CH0 and CH1 RF channels. All of the triggers from a single channel must occur (ANDed together) for the sync detect to be armed from either RF channel (ORed) to arm the sync detect. The default is RF On and Signal On AM Trigger Off, on both channels. This allows simultaneous triggering on individual channels and combiners/BSSs.

- RF On The bit counting starts when the RF is turned on. (Refer to section 6.4.11.)
- Signal On The timing starts when the Signal is turned on. (Refer to section 6.4.11.)
- AM Trigger The timing starts on the rising edge of the AM Trigger status output signal. (Refer to section 6.4.3 and section 6.4.9.)

The Data Sync panel is associated with the Data Monitor block in the RA analyzer section, as shown in the diagram in Figure 242.

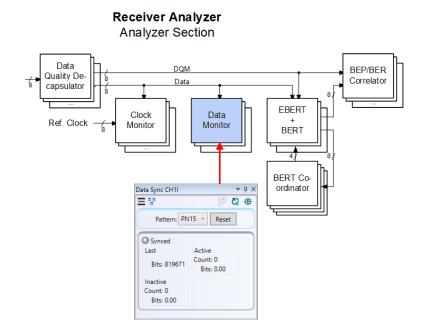


Figure 242: Analyzer, Data Monitor – Data Sync Panel

#### 6.4.13.3 DQE (Data Quality Encapsulation) Panel

Data Quality Encapsulation is the process of bundling data quality information along with payload data. This information is intended for use by a Best Source Selector (BSS) to optimally select correct payload data bits from amongst multiple streams of potentially errored payload data. Detailed information about DQE and DQM is available in IRIG 106-22, Appendix 2-G.

The View menu provides access to the DQE panels, as shown in Figure 243.

The DQE panel, shown in Figure 244, sets the Frame Length, and enables/disables other frame activity.

### 3<sup>rd</sup> Generation Receiver Analyzer

View	Project	Connection	Options	Тос	ols	Window	Help
	BERT Cont	+					
	BERT Grou	qu	+				
	BERT Mon	itor	•				
	Channel N	lixer	•				
	Data Gene	erator	+				
	Data Sync		•				
	DQE		×		DQ	E BSSI	
	Encoder		+		DQ	E BSSQ	
	Indirect Pa	ath	+		DQ	E CH1I	
	Ю		+		DQ	E CH1Q	
	Measurem	nent	+		DQ	E CH2I	
	Modulato	r	+		DQ	E CH2Q	
	Phase Ger	nerator	+		DQ	E CMBI	
	Phase Ger	nerator Group	+		DQ	E CMBQ	

Figure 243: View, DQE Drop Down Menu

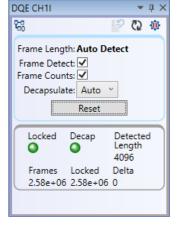


Figure 244: DQE CH1I Panel

- Frame Length Sets the DQE frame length; may be typed manually or detected automatically
- Frame Detect When checked, automatically detects the DQE frame length (generally recommended unless DQE framing is 'broken' on the receiver under test)
- Decapsulate Decapsulate has three states Off, On, and Auto. Off disables all DQE decapsulation, regardless of whether encapsulation is present. On always attempts to decapsulate DQE. This can cause issues if the receiver does not have DQE enabled. Auto attempts to detect valid DQE and, if found, automatically decapsulates.
- Frame Counts When checked, enables frame counts. Counts can be paused to verify that total frame and locked frame counts are equal by unchecking this check box. Re-checking it resets the frame counts.
- Reset Button Resets the Total Frames and Locked Frames counters
- Locked Indicator When this indicator is active green, the data quality encapsulation frame lock is active (DQE detected).
- Decap Indicator Shows decapsulation is active, either when Decapsulate is set to On, or if Decapsulate is set to Auto and DQE Lock is indicated
- Frames Shows the count of total DQE frame headers
- Locked Shows the count of the properly bounded DQE frames, with a DQE header before and after
- Delta Shows the difference between Frames and Locked; this is the number of bad or dropped DQE frames detected

The DQE panel is associated with the Data Quality Decapsulator block in the RA analyzer section, as shown in the diagram in Figure 245.

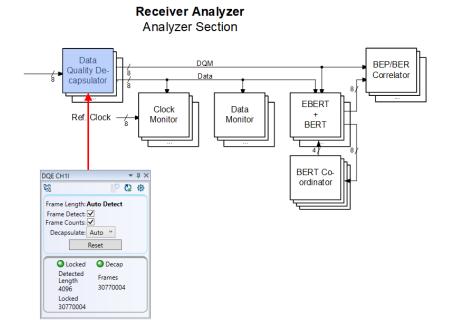


Figure 245: Analyzer, Data Quality Decapsulator – DQE Panel

## 6.5 Measurement Overview

RxAn provides scripts to facilitate automated, repeatable measurements. While the RA provides almost limitless capability for users to create their own measurements, the most common ones are provided in the included RA resources.

The provided scripts configure test-specific settings as needed prior to or during execution of the measurement. However, they also rely on other basic settings to be configured by the project file. It is strongly recommended that the provided project files, or modifications of them, be used in conjunction with the provided measurement scripts. Any required deviations from the project defaults are noted below. In all cases, basic settings in the receiver under test (frequency, mode, bit rate) must match.

Even though these measurements alter settings during execution, by default they restore all RA settings to their premeasurement state on completion. This is true whether the measurement completes normally or due to user intervention. Generally, this avoids confusion caused by residual unknown settings changes. However, it may also cause a difference in operation between manual testing and measurement script execution. For example, DQE decapsulation is enabled during all DQM measurements, whether it is enabled prior to the measurement or not. If DQE decapsulation is not enabled manually, DQE/DQM will appear to be broken even though DQE/DQM tests may indicate no problems.

#### 6.5.1 Measurement Panel

Access to measurement scripts and execution status is provided via the Measurement panel, as shown in Figure 246. This section describes the operation of the Measurement panel, while detailed explanations about automated measurements are located in section 6.6.

1. Click on the Hamburger icon in the upper left corner of the Measurement screen to access the Measurement Hamburger menu, as shown in Figure 247.

2. Click on the Load button to select and load a script.

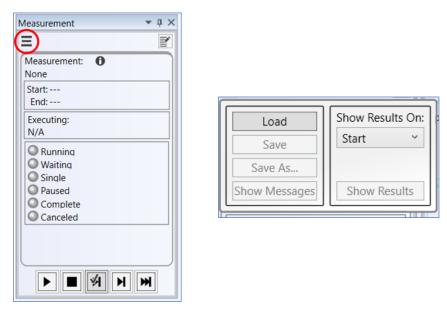
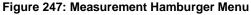


Figure 246: Measurement

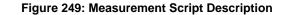


When a script is loaded, the script title displays in the Measurement panel, as shown in Figure 248. Click on the information icon (I in a circle) to display information about the selected script, as shown in Figure 249.

Measurement 💌 म 🗙
=
Measurement: BER - RF Level Sweep
Start: End:
Executing: N/A
Running Waiting Single
<ul> <li>Paused</li> <li>Complete</li> <li>Canceled</li> </ul>

Measure PCM/FM demodulator performance across a range of modulation indexes (IRIG 118-22 7.6)
6 Blocks
1 Chart
File Path: C:\Receiver-Analyzer-3\3_0_10_RC1
\2023-03-02_1435_3_0_10_RC1\Resources\Measurements
\BER_AWGN_mod_index_sweep.rms

Figure 248: Measurement Script Title



### 3<sup>rd</sup> Generation Receiver Analyzer

- 3. Commonly used variables for the loaded script are set by clicking on the Edit icon (paper and pencil) in the upper right corner of the Measurement panel.
- 4. When the Edit screen displays, in this example BER RF Level Sweep, type the desired values in each field, then click on the Close button.

DBER - RF Level Sweep			×
Name RF Level Start			>
RF Level Stop	-110		
RF Level Step	-0.5		
Time Limit	10000		
Error Limit	10000		~
		С	lose

Figure 250: Example Measurement Edit Script Parameters Window

5. To start the measurement, click on the Start button at the bottom of the Measurement screen, as shown in Figure 251. The buttons are as follows, left to right: Start, Stop, Enable Single Step, Step, and Skip this Step.



Figure 251: Measurement Buttons

- Start The Start Measurement button is used to start a new measurement. The Running indicator turns green. In addition, the Start information field is populated with the date and time the measurement was started.
- Pause After clicking on the Start button, it changes to a Pause button. This indicates the measurement is running and may be paused by clicking on this button.

When a measurement is paused, the Paused indicator changes to red. This indicates that the measurement is running, but is in a paused state and is waiting for the user to take action. Click on the Pause button. The button changes back to a Start button. Click on Start to resume.

• Stop – The Stop Measurement button immediately terminates a measurement. The Canceled indicator turns red, and all other indicators turn grey. In addition, the End information field displays the date and time the measurement was stopped.

- Enable/Disable Single Step Mode Single step can be used to debug measurement scripts
- Step Used for manual stepping
- Skip This Step A measurement step can be skipped if a measurement halts, either because of a bug in the script or the terminating condition for that step never occurs. Any data collected for that step will be marked as invalid.

When the script is running, the Measurement panel displays the Start date and time, details about the script being executed, and colored indicators. All measurement indicators are shown in Figure 252.

#### Indicators

Indicators include Running, Waiting, Single, Paused, Complete, and Canceled.

- Running When this indicator is active green, a measurement is running.
- Waiting When this indicator is active orange, the measurement engine is waiting for a step to complete.
- Single When this indicator is active orange, the measurement engine is in single step mode.
- Paused When a measurement is paused, the Paused indicator changes to red. This indicates that the measurement is running, but is in a paused state and is waiting for the user to take action.
- Complete When this indicator is active green, the measurement has completed.
- Canceled When a measurement is stopped, the indicator turns red (all other indicators turn gray).

Measurement	<b>▼</b> ↓ ×
Ξ	ľ
Measurement: () BER - RF Level Sweep	Running Waiting
Start: 3/20/2023 10:55:30 AM End:	Single
Executing: Recorder BERCH1I BER record results	Complete Canceled
	×

Figure 252: Measurement Panel with a Script Paused

The window shown in Figure 258, is titled with the name of the measurement .rmr file. This is the central collection of all charts for a set of measurement results. It shows a sample of the measurement chart(s) and provides the option of saving the image(s). Multiple charts may be arranged, displayed, and saved.

BER_AWGN_s	sweep_1_2024022	?7_170256.rmr*					-	_		×
Show All	Chart		Title							
♥ Show Shown on: Start		I Research (SORU 9 13 High	BER Curve	Save Image						
PER AWGN sugg	- 1 20240227 1	70256								
BER_AWGN_swee	ep_1_20240227_1	/0256.rmr								_
				Cascade	~	<u>A</u> rrange	<u>S</u> ave		<u>C</u> lose	

Figure 253: Example Results Document

Closing this document closes all of the charts that go with it. The user can selectively display or hide individual charts by clicking on the appropriate Show check boxes, as shown in Figure 254.

DQM_AWGN	Lstep_1.rmr		- 0	×
✓ Show All	Chart	Title		
Show Shown on: Complete		DQM Distribution	Save Image	
Show Shown on: Complete		DQM Correlation	Save Image	
C:\Projects\trans	mitter\RA3.0\dotNET\RxAnCore\LocalF	iles\Resources\Results	\examples\DQM_AWGN_step_1.rmr	
			Cascade ∽ <u>A</u> rrange <u>S</u> ave <u>C</u> lo	se

Figure 254: Example Results Document Showing Individual Charts

Selecting a chart in the results document brings the chart to the front, as shown in Figure 255.

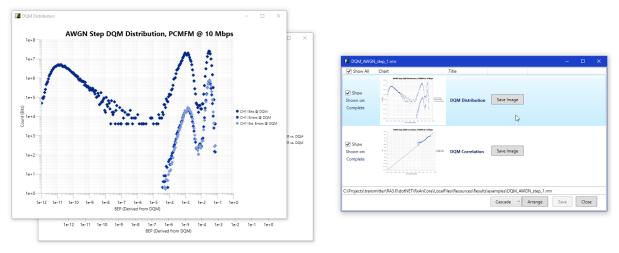


Figure 255: Example Results Document with Selected Chart in Front

The user can save the charts as images files by clicking on the Save Image button, as shown in Figure 256.

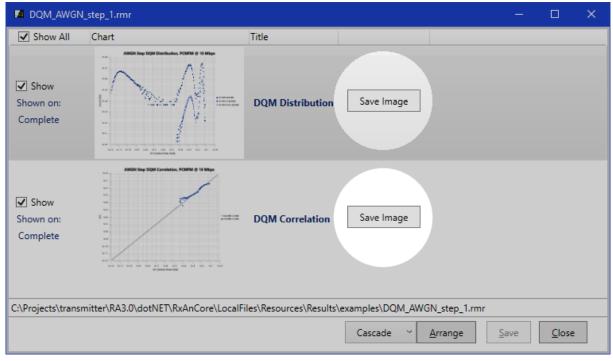


Figure 256: Example Results Document, Save Image Buttons

The following image file types are supported: .png, .jpeg, .bmp, .tiff, and .gif, as shown in the drop down menu in Figure 257.

) Music				
E Pictures				
Videos				
🟪 Windows (C:)	v <			
File name:				
Save as type:	All Files (*.png)			
	All Files (*.png)			
	PNG Files (*.png)			
<ul> <li>Hide Folders</li> </ul>	JPEG Files (*.jpg;*.jpeg)			
	Bitmap Files (*.bmp)			
	TIFF Files (*.tif)			
	GIF Files (*.gif)			
	All Files (*.*)			

Figure 257: Example Save Document, Save as Type Drop Down Menu

Some measurements produce many charts. The user can arrange the charts so that they are as visible as possible. The charts may be stacked along the left, top, right, or bottom of the current screen, or cascaded, as shown in Figure 258.

DQM_AWGN	N_step_1.rmr					—		×
Show All	Chart		Title					
✓ Show Shown on: Complete			DQM Distribution	Save Image	]			
✓ Show Shown on: Complete			DQM Correlation	Save Image	]			
C:\Projects\trans	mitter\RA3.0\dotN	IET\RxAnCore\LocalFi	les\Resources\Results	examples\DQM_A\	VGN_step_1.rm	r		
				Cascade 🔨	Arrange	Save	Close	
				Stack Left				
				Stack Top				
				Stack Right				
				Stack Bottom				
				Cascade				

Figure 258: Example Results Document, Cascade Button Drop Down Menu

The Save Document? message, shown in Figure 259, displays when the user tries to close the document without saving it first.

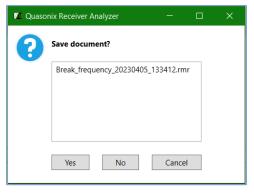


Figure 259: Save Document Message

File name default structure is: {script file name}\_yyyyMMdd\_HHmmss.rmr

### 6.6 Automated Measurements

Automated scripting allows precise measurement using many parameters with rapid acquisition of extremely large datasets. Saved results files facilitate result aggregation, analysis, presentation, and archival. The RA provides these measurement using a variety of scripts. These include:

- BER AWGN Sweep
- BER RF Level Sweep
- Sync Time
- Break Frequency
- Combiner Gain
- Equalizer Multipath Sweep
- BER AWGN Modulation Index Sweep
- DQM-AWGN Sweep
- DQM-AWGN Step
- DQM Resync
- DQM Monitor

### 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.1 BER AWGN Sweep

This measurement sweeps the RA-generated Additive White Gaussian Noise (AWGN) level across a range of Eb/N0 values and measures bit error rate (BER) at each level.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - E<sub>b</sub>/N<sub>0</sub> Start/Stop/Step E<sub>b</sub>/N<sub>0</sub> starting/stopping/step value (dB); adjust as appropriate per modulation and coding
- Setup No change from project defaults

This measurement plots a standard BER curve for each tested channel, as shown in Figure 260.

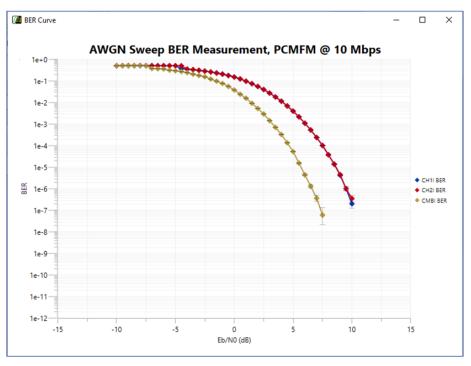


Figure 260: Example BER AWGN Modulation Index Sweep Graph

### 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.2 BER RF Level Sweep

This measurement sweeps the RF output level across a range of values and measures bit error rate (BER) at each level. This is similar to the BER AWGN Sweep test, but it uses receiver front-end noise to set the noise floor.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - RF Level Start/Stop/Step RF level starting/stopping/step value (dB); adjust as appropriate per modulation, coding, and receiver noise figure
- Setup No change from project defaults

This measurement plots a standard BER curve for each tested channel, as shown in Figure 261.

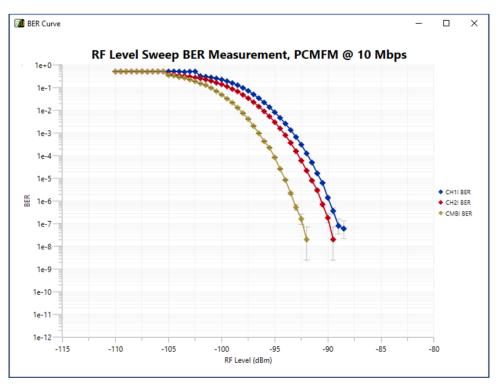


Figure 261: Example BER RF Level Sweep Graph

#### 6.6.3 Sync Time

This measurement alternately enables and disables signal output for several iterations to measure receiver synchronization time and synchronization loss time, which is a measure of receiver latency. It implements a superset of the Demodulator Acquisition Time and Flat Fade Recovery Time test in IRIG 118-22, Section 7.4.

The underlying mechanism for measuring sync time is based on a statistical model of errored data. Despite normal statistical variation, only a small number of iterations is generally required to get good results for most test conditions. However, block coding such as LDPC introduces discrete acquisition boundaries between codeblocks, requiring a large number of iterations to cover all timing relations.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - Iterations Number of sync/sync-loss measurement iterations
  - Signal On/Off Dwell Signal On/Off minimum dwell time (ms); ensure Off dwell is long enough for tracking loops to drift and for optional frequency offset to sweep
  - CH1/CH2 Frequency Offset Sweep Rate/Limit Frequency sweep rate and range; set to force acquisition at varying offsets from the center frequency
- Setup Set RA AWGN to the desired level for resynchronization; static BER should be 1e-1 or lower

This measurement plots separate histograms for sync time, as shown in Figure 262, and for sync loss time, as shown in Figure 263.

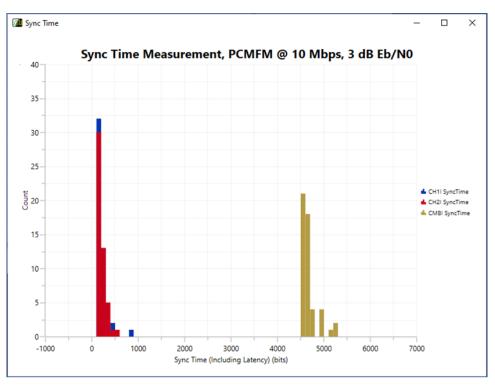


Figure 262: Example Sync Time Graph

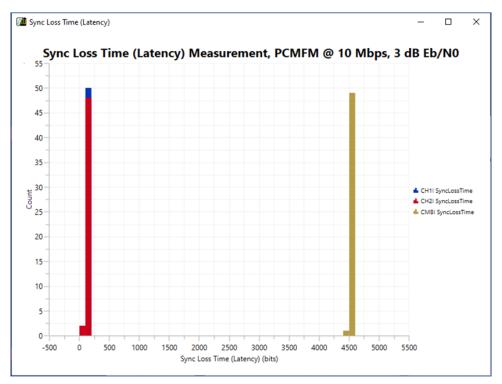


Figure 263: Example Sync Loss Time (Latency) Graph

**Note:** The difference between measured sync time and sync loss time (latency) represents the true measure of the number of bits that may be lost from the end of a flat fade until the receiver produces usable data.

These histograms present a summary view of sync time performance over all trials. More detailed analysis can be performed using data saved in .rmr and/or .csv files. In particular, average sync time, average latency, and the difference between the two may be calculated using this data, as well as min, max, variance, etc. Average sync time and average latency can also be viewed in the Data Sync panels for the channels being tested.

### 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.4 Break Frequency

This measurement sweeps the rate at which the two RF outputs are faded out of phase with respect to each other and measures bit error rate (BER) for both channels and the combiner at each fade rate. It effectively implements the Combiner Break Frequency test in IRIG 118-22, Section 5.7.

- Variations Available for 3-channel testing (2 channels plus combiner) only
- Key Parameters
  - Reference BER USER CONTROLLED target error rate for CH1/CH2 prior to test start (refer to Setup bullet)
  - Fade Rate Start/Stop/Step Fade rate starting/stopping/step value (MHz)
- Setup Set RA AWGN or RF level to achieve the reference BER on receiver channels 1 and 2. The default reference BER is 1e-4, which results in approximately 1e-6 BER for the combiner at low fade rates, as specified in IRIG 118. If a different reference BER is used, it should be changed in the measurement parameters.

This measurement plots BER relative to fade rate, as shown in Figure 264.

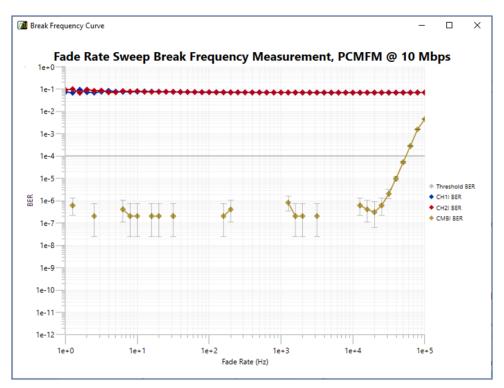


Figure 264: Example Break Frequency Graph

The break frequency is the point at which the combiner BER exceeds the reference (threshold) BER. It may be interpolated in the general case where measurement points straddle the threshold.

### 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.5 Combiner Gain

This measurement sweeps the  $E_b/N_0$  of RF channel 1 above and below the fixed  $E_b/N_0$  of RF channel 2 and measures the BER for both channels and the combiner at each Eb/N0 step. It effectively implements the Diversity Combiner Static Evaluation with Unequal RF Signal Strengths test in IRIG 118-22, Section 5.2.

- Variations Available for 3-channel testing (2 channels plus combiner) only
- Key Parameters
  - CH1  $E_b/N_0$  Start/Stop/Step Variable-channel  $E_b/N_0$  starting/stopping/step value (dB); should range from above to below static-channel  $E_b/N_0$
  - CH2  $E_b/N_0$  Static-channel  $E_b/N_0$  (dB)
- Setup No change from project defaults

This measurement plots BER relative to channel 1  $E_b/N_0$  and re-plots combiner BER relative to the calculated theoretical combiner  $E_b/N_0$ , as shown in Figure 265.

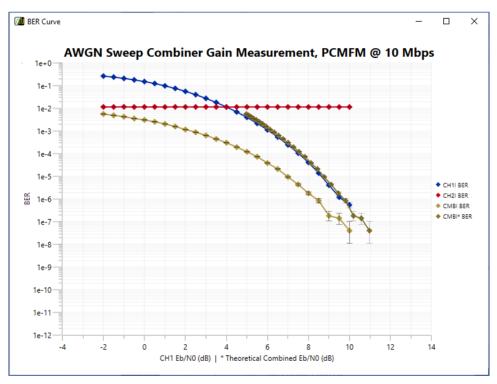


Figure 265: Example Combiner Gain Graph

Perfect combiner gain, as described in RCC 119-06 Section 4.4.12, would place the re-plotted combiner BER curve (dark gold) on top of the channel 1 BER curve (blue). Any difference shows a shortcoming in combiner gain.

#### 6.6.6 Equalizer Multipath Sweep

This measurement sweeps the amplitude and/or phase of one or two multipath reflection rays and records estimated bit error probability (BEP) based on DQM at each combination. It implements a superset of the System Bit Error Rate for Static 3-ray Channels test in IRIG 118-22, Section 10.1.

**Note:** This measurement relies on estimated bit error probability (BEP) from DQM rather than measured bit error rate (BER) to test a large parameter space in relatively short time. As such, it requires DQM to be accurate under multipath conditions. This must be verified by a separate test to ensure validity of equalizer measurement results.

- Variations Available for 1-channel testing only
- Key Parameters
  - Ray 0 Magnitude Start/Stop/Step First reflection magnitude starting/stopping/step value (relative to desired signal)
  - Ray 0 Phase Start/Stop/Step First reflection phase starting/stopping/step value (degrees)
  - Ray 0 Delay First reflection delay value (ns); ensure this is scaled properly based on bit rate
  - Ray 1 Magnitude Start/Stop/Step Second reflection magnitude starting/stopping/step value (relative to desired signal)
  - Ray 1 Phase Start/Stop/Step Second reflection phase starting/stopping/step value (degrees)
  - Ray 1 Delay Second reflection delay value (ns); ensure this is scaled properly based on bit rate

Default parameters for this measurement test the IRIG 118 "Mild", "Moderate", and "Severe" impairment static 3-ray channels across all phases of the specular reflection. These three channels correspond to ray 0 magnitudes of 0.7, 0.85, and 0.9 respectively; magnitudes of 0.75 and 0.8 are also tested. Note that any change in bit rate requires adjusting the delay parameters accordingly.

- Setup
  - Enable DQE in the receiver under test and decapsulation in the RA.
  - Enable/disable equalization in the receiver under test as desired.
  - Set RA AWGN to desired level (IRIG 118 recommends 20 dB Eb/N0).

This measurement plots BEP relative to phase at each magnitude, as shown in Figure 266 (without equalization) and in Figure 267 (with equalization).

It may be useful to compare results without equalization and with equalization. Link availability under the chosen multipath conditions may be estimated by noting the percentage of phases at which BEP exceeds 1e-5.

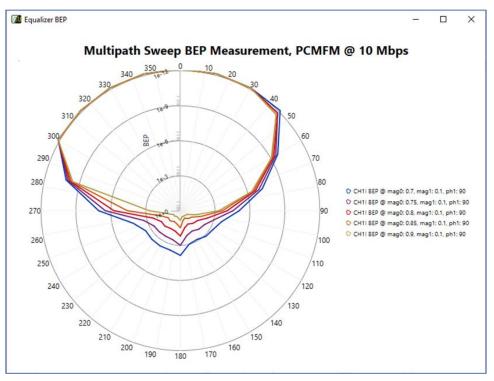
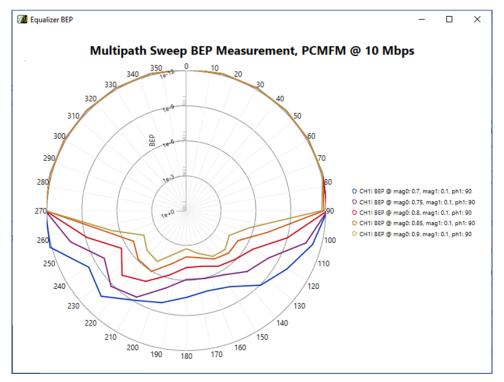


Figure 266: Example Multipath Sweep BEP Graph, Without Equalization





### 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.7 BER AWGN Modulation Index Sweep

This measurement sweeps the modulation index (PCMFM only) and  $E_b/N_0$  across a range of values and records the bit error rate (BER) at each combination. It implements a superset of the PCM/FM Demodulator Modulation Index Sensitivity test in IRIG 118-22, Section 7.6.

- Variations Available for 1-channel testing only
- Key Parameters
  - Mod Scale Start/Stop/Step Modulation scale starting/stopping/step value (dB); this scale is applied as a multiplier on the default modulation index of 0.7, so a scale range from 0.5 to 5 results in a modulation index range of 0.35 to 3.5
  - E<sub>b</sub>/N<sub>0</sub> Start/Stop/Step E<sub>b</sub>/N<sub>0</sub> starting/stopping/step value (dB)
  - Pre-BER Dwell Dwell time after setting mod scale before BER measurement (ms); ensure enough time for modulation index tracking to occur in the receiver (at high  $E_b/N_0$ ) before measuring BER
- Setup
  - Set the IF bandwidth in the receiver under test to accommodate the required bandwidth (at least double the normal bandwidth if the modulation index will exceed 1.0).
  - Enable receiver modulation index tracking or other mitigation settings as desired.

This measurement plots BER relative to modulation index at each magnitude, as shown in Figure 268.

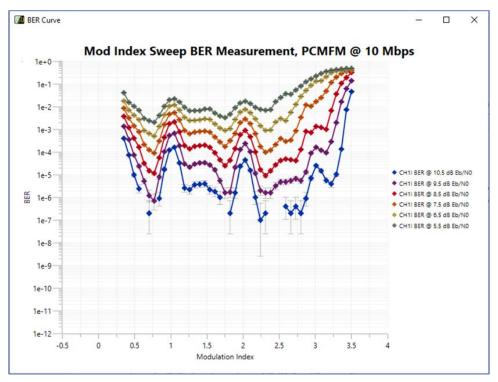


Figure 268: Example Modulation Index Sweep BER Graph

## 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.8 DQM AWGN Sweep

This measurement sweeps the RA-generated Additive White Gaussian Noise (AWGN) level across a range of  $E_b/N_0$  values to measure DQM accuracy and response to static noise conditions. It implements the BER versus Estimated BEP (DQM) with Additive Noise test in IRIG 118-22, Section 11.1.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - $E_b/N_0$  Start/Stop/Step  $E_b/N_0$  starting/stopping/step value (dB); adjust as appropriate per modulation and coding
- Setup Enable DQE in the receiver under test and decapsulation in the RA.

This measurement plots a standard BER curve for each tested channel, along with corresponding BEP curves, as shown in Figure 269.

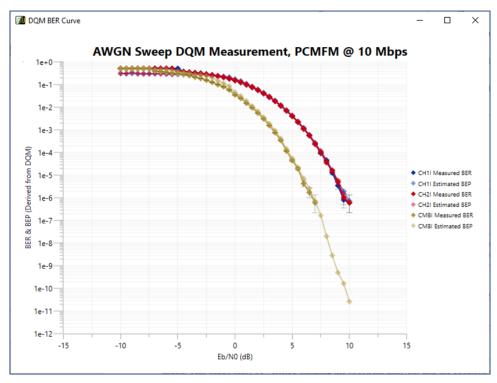


Figure 269: Example AWGN Sweep DQM Graph

Ideally, the estimated BEP curve should lie directly in line with the measured BER curve, within the extents of the error bars. These bars indicate 95% Clopper-Pearson confidence intervals based on the number of bits and the number of errors counted at each  $E_b/N_0$  level and are intended to illustrate imprecision of the measured BER due to small sample size.

When this measurement completes, it also plots distribution and correlation charts. To generate these charts, the RA accumulates information over all DQE frames in the measurement on a frame-by-frame basis. The 65,536 possible DQM values are assigned into 256 bins, each containing 256 successive DQM values. Each bin has three pieces of information associated with it:

- A count of bits from all DQE frames with DQM value in the bin
- A count of errors from all DQE frames with DQM value in the bin
- The average BEP represented by the 256 DQM values in the bin

So, for each DQM frame received, the RA counts the number of bits and the number of bit errors, and adds those to a running total for the bin associated with the DQM value in the frame. At the end of the test, each bin has a count of the total bits and the total bit errors that occurred in that bin. In addition, each bin has a count of estimated bit errors, calculated by multiplying the count of total bits by the average BEP represented by the bin. These three quantities are plotted for each bin (BEP) in the distribution chart, as shown in Figure 270.

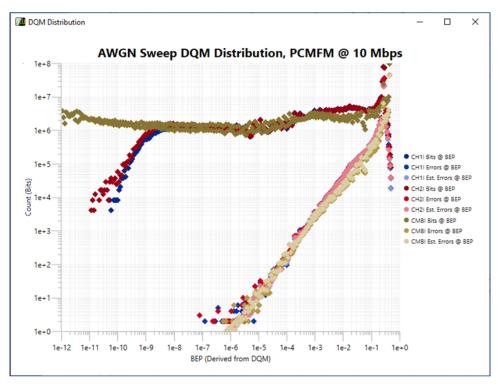


Figure 270: Example AWGN Sweep DQM Distribution Graph

The scatter plot of bit count shows how uniform the BEP estimates were over the course of the measurement. Clumps may be used to identify discrete BEP levels that occurred.

Ideally, the count of estimated bit errors should lie directly in line with the count of measured bit errors. However, near the bottom of the chart, it is not uncommon for some measured errors to appear as "outliers." These may not indicate an actual departure from the ideal. Measured errors only occur in integer quantities; each bit is either errored or not, but estimated errors can occur in fractional quantities; a BEP of 1e-6 over a DQE frame with 4096 bits indicates an estimated 0.004096 bit errors in that frame. In this example, an actual measured bit error would be expected statistically once every 250 frames.

The same information can be used to calculate the measured bit error rate (BER) for each bin as the count of errors divided by the count of total bits. The BER is plotted for each bin (BEP) in the correlation chart shown in Figure 271.

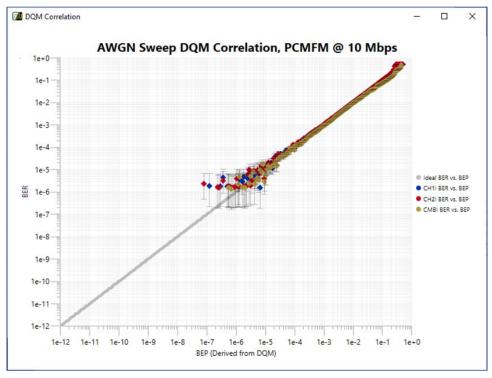


Figure 271: Example AWGN Sweep DQM Graph, BER for Each BEP

Ideally, the measured BER should lie directly on the gray 1:1 line of BER=BEP, within the extents of the error bars. Outliers identified near the bottom of the distribution plot may be judiciously discounted. However, no points should ever appear on the y-axis; the worst possible DQM 'sin' is to indicate that data is "perfect" when it in fact contains errors.

## 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.9 DQM AWGN Step

This measurement rapidly steps the RA-generated Additive White Gaussian Noise (AWGN) level across a set of  $E_b/N_0$  values to measure DQM accuracy and response to dynamic channel conditions. It implements the Estimated BEP (DQM) Step and Dwell Response test in IRIG 118-22, Section 11.2.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - Cycles Number of E<sub>b</sub>/N<sub>0</sub> cycles; each cycle is a triangle step: mid, high, mid, low, (mid, high, mid, low...)
  - Eb/N0 low, mid, high  $E_b/N_0$  levels to cycle through (dB)
- Setup Enable DQE in the receiver under test and decapsulation in the RA.

This measurement produces no chart while it is executing. When this measurement completes, it plots distribution and correlation charts as described above and shown in Figure 272 and Figure 273.

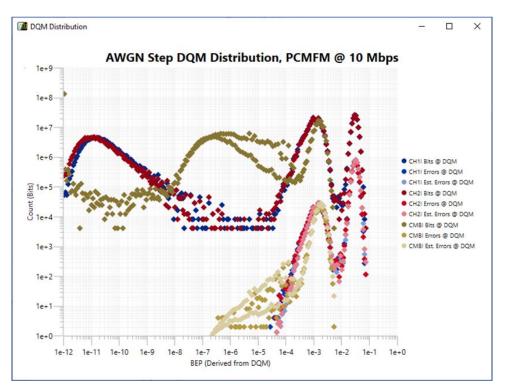


Figure 272: Example AWGN Step DQM Distribution Graph

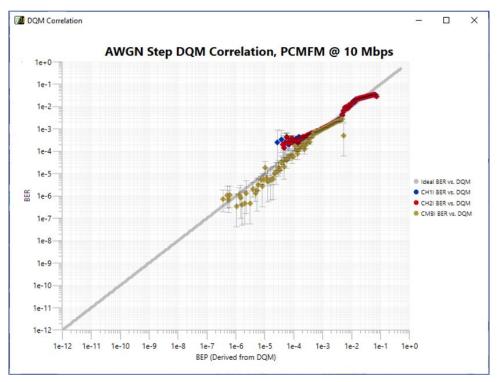


Figure 273: Example AWGN Step DQM Correlation Graph

### 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.10 DQM Resync

This measurement alternately enables and disables signal output for several iterations to measure DQM accuracy and response to lock loss and acquisition. It implements the DQM (BEP) Resynchronization Response test in IRIG 118-22, Section 11.6.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - Iterations Number of resynchronization cycles; each cycle is off, on, (off, on, ...)
  - Signal On/Off Dwell Signal On/Off minimum dwell time per step (ms); ensure Off dwell is long enough for tracking loops to drift
- Setup
  - Set RA AWGN to the desired level for resynchronization.
  - Enable DQE in the receiver under test and decapsulation in the RA.

This measurement produces no chart while it is executing. When this measurement completes, it plots distribution and correlation charts as described above and shown in Figure 274 and Figure 275.

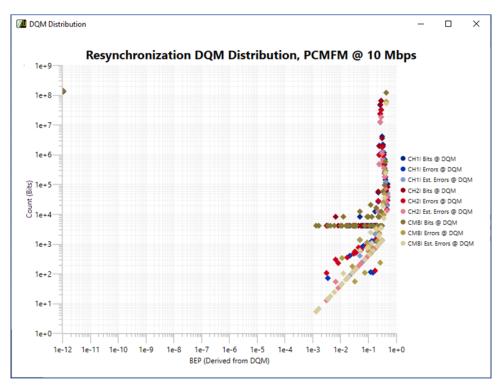


Figure 274: Example Resynchronization DQM Distribution Graph

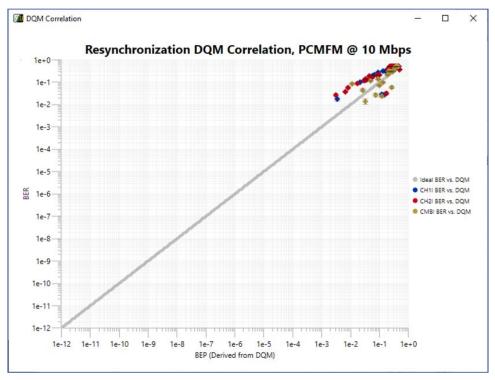


Figure 275: Example Resynchronization DQM Correlation Graph

**Note:** Because this measurement results in total sync loss in the receiver, it also results in total sync loss in the RA BERT. If receiver synchronization occurs right at the end of a DQE frame, it is possible that BERT resynchronization could span into the next DQE frame. This could result in errors appearing in a DQE frame with "perfect" DQM. The RA BERT is designed to synchronize with effectively zero delay in the absence of bit errors, so the preceding scenario is highly unlikely. However, should bit errors be noted at very low BEP, then the test may be re-run or the errors may be discounted *if* they are few in number. In any case, a large number of errors at low BEP indicates a problem with receiver DQM.

## 3<sup>rd</sup> Generation Receiver Analyzer

#### 6.6.11 DQM Monitor

This measurement allows the user to manually control test conditions to measure DQM accuracy and response to any desired channel condition(s). During the manual test, the RA monitors receiver output to compare estimated BEP performance from DQM with actual measured BER performance. Among other uses, it may be used to implement as-yet unautomated tests in IRIG 118-22, Sections 11.3-11.5.

- Variations Available for 1-channel and 3-channel testing
- Key Parameters
  - Number of Steps Number of manual test steps for complete measurement; set to 1 to run a single test condition
- Setup
  - Set RA or other signal source to the general desired conditions.
  - Enable DQE in the receiver under test and decapsulation in the RA.
  - After starting the measurement, establish the specific first test condition and then click on the Play icon in the Measurement panel. When BER measurement for that condition is complete, repeat setup, then click on Play for each subsequent test condition, until all test steps have been completed.

This measurement plots BER for each tested condition, along with corresponding BEP, as shown in Figure 276.

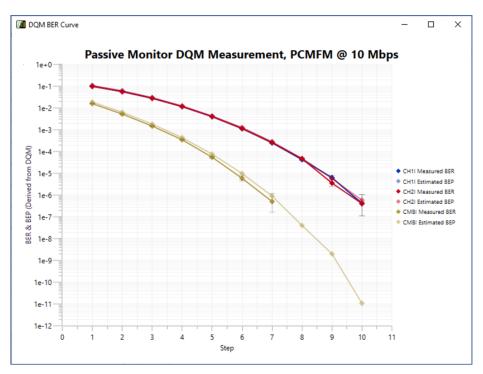


Figure 276: Example Passive Monitor DQM Graph

When this measurement completes, it also plots distribution and correlation charts over all tested conditions as described above and shown in Figure 277 and Figure 278.

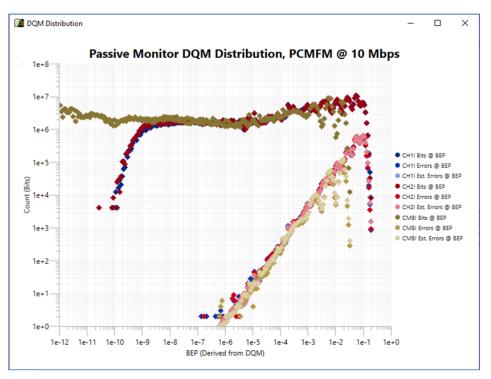
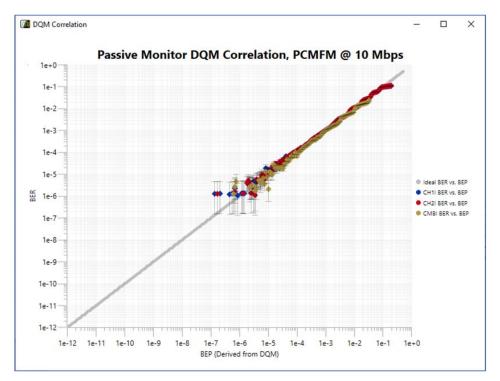


Figure 277: Example Passive Monitor DQM Distribution Graph





### 6.7 Measurement Results

One or more charts may be produced while a measurement runs or after a measurement completes. This includes a chart manager which provides simple access to view and save results. Measurement results can be saved in multiple formats. The .rmr format is the native RA measurement result file and includes all information recorded during a measurement. The .rmr files can be loaded into the RA application to display the complete results again. Use the File menu, Open option to load measurement results.

#### 6.7.1 Export Measurement Results

The RxAn measurement system can collect large and complex data sets. The RxAn Measurement Results file (.rmr) format stores this data, along with the display descriptions, so that the data can be reloaded and redisplayed, just as it was when the measurement was taken. While the .rmr file is stored in XML format and can be parsed by any XML reader, it is not directly processable in packages such as Microsoft Excel. As such, measurement data can be saved in the more common Comma Separated Value (.csv) format, which can be easily loaded into Microsoft Excel.

.csv files are inherently a two dimension table, with a single independent variable and one or more dependent variables. This can be a simple as a Bit Error Rate curve where independent variable is Eb/N0 and the dependent variable is Bit Error Rate. Even the simple Bit Error Rate measurement can produce more complex datasets, including Bit Error Probability and data from multiple channels. RxAn measurement is not limited to a single independent variable and can nest multiple variables. Measurements such as Equalizer performance have an inner loop with the IPATH phase angle that sweeps 360 degrees, and an outer loop of IPATH magnitude. A third independent variable could be added that sweeps Eb/N0 or, in PCMFM, modulation index.

These complex datasets cannot be well expressed in a single, flat two dimension table such as a .csv file. In these cases, multiple .csv files can be created from a single .rmr file to separate multiple independent variables and group dependent variables.

It should be noted that .csv files cannot be reloaded into RxAn. The user should always save results in the .rmr format first.

Datasets are configured as one or more series, each containing one or more sets of datapoints that are recorded for each measurement step. The datapoints are both the dependent and independent variables and no distinction is made.

To save results as comma separated values:

- 1. Select File>Save as...to open the Save As window.
- 2. Click on the down arrow in the Save as type field to display the drop down menu.
- 3. Select CSV Files (\*.csv), as shown in Figure 279.

• • <b>•</b>	« RxA	n > UserFiles > Resources > Results > ex	amples 〜 ට	, Search e	amples
Organize 🔻 Ne	w folder				III - (
🛃 Photography	^	Name	Date modified	Туре	Size
🎳 Prepping		BER AWGN mod index sweep.rmr	3/1/2023 7:16 PM	RMR File	578 KB
Projects		BER AWGN sweep 1.rmr	3/1/2023 8:03 PM	RMR File	70 KB
Public		BER_AWGN_sweep_3.rmr	3/1/2023 8:15 PM	RMR File	207 KB
Self Assessme	ent \	BER_RF_level_sweep_1.rmr	3/1/2023 8:21 PM	RMR File	87 KB
Software		BER_RF_level_sweep_3.rmr	3/1/2023 8:27 PM	RMR File	256 KB
-		Break_frequency.rmr	3/1/2023 8:31 PM	RMR File	330 KB
Sounds		📓 Combiner_gain.rmr	3/1/2023 8:36 PM	RMR File	184 KB
o twitter		DQM_AWGN_step_1.rmr	3/1/2023 8:48 PM	RMR File	567 KB
J Violence		DQM_AWGN_step_3.rmr	3/1/2023 8:52 PM	RMR File	1,693 KB
🛃 Work		DQM_AWGN_sweep_1.rmr	3/1/2023 8:55 PM	RMR File	708 KB
👝 OneDrive - Per	ioni	DQM_AWGN_sweep_3.rmr	3/1/2023 8:57 PM	RMR File	2,113 KB
		DQM_passive_1.rmr	3/1/2023 9:00 PM	RMR File	620 KB
💻 This PC		DQM_passive_3.rmr	3/1/2023 9:02 PM	RMR File	1,806 KB
🧊 3D Objects		DQM_resync_1.rmr	3/1/2023 9:13 PM	RMR File	560 KB
📃 Desktop		DQM_resync_3.rmr	3/1/2023 9:09 PM	RMR File	1,672 KB
🛱 Documents	~	<			>
File name:	C:\Proi	ects\transmitter\RA3.0\dotNET\RxAn\UserFiles	\Resources\Results\example	es\DOM AWGN ster	1.rmr
-		ument Files (*.rmr)			-
Save as type.		ument Files (*.rmr)			
Hide Folders	All Files RxAn M	: (*.rmr;*.xml;*.csv) leasurement Result Files (*.rmr) es (*.xml)			

Figure 279: Example Save Measurement Results Drop Down Menu Selection

An Export .csv dialog box displays, as shown in Figure 280. The contents vary depending on the results data.

🖬 Export: BER - AWGN Sweep — 🗆	🗙 🍱 Export: Equalizer - Multipath Sweep 🦳 🔲	🗙 🖬 Export: DQM Verification - AWGN Sweep 🛛 🗕 🗲
elect data to be exported	Select data to be exported	Select data to be exported
<ul> <li>BER - AWGN Sweep</li> <li>BERCHTI Select Series and Datapoint</li> <li>Series:</li> <li>BER ins:</li> <li>DateTime</li> <li>DateTime</li> <li>BER</li> <li>Errors</li> <li>BER</li> <li>LowetTror</li> <li>UpperError</li> </ul>		DOM Verification - AWGN Sweep     BERCH11 Select Series and Datapoint     Series     BER     Datapoints     Devoting     BER     Devoting     Devetror     Depertor     Depertor     Depertor     Depertor     Detapoints     Datapoints     Data
Ok Cancel	Ok Cancel	Ok Cancel

Figure 280: Examples of Export .CSV Data Selection Dialog Boxes

At least one series and one datapoint must be selected. Selecting a parent check box causes all child check boxes below it to be checked, as shown in Figure 281.

🛤 Export: BER - AWGN Sweep	-	×
Select data to be exported		
<ul> <li>BER - AWGN Sweep</li> <li>BERCH11</li> <li>Series: <ul> <li>BER</li> <li>Datapoints:</li> <li>DateTime</li> <li>AWGN</li> <li>Bits</li> <li>Errors</li> <li>BER</li> <li>LowerError</li> <li>UpperError</li> </ul> </li> </ul>		
Ok Cancel		

Figure 281: Export: BER-AWGN Sweep Data Selection with Parent Parameters Checked

If only some of the child parameters are checked, the parent check box contains a black square indicating a partial subset of children is selected, as shown in Figure 282.

🛤 Export: BER - AWGN Sweep	_	×
Select data to be exported		
<ul> <li>BER - AWGN Sweep</li> <li>BERCH11</li> <li>Series:     <ul> <li>BER</li> <li>Datapoints:</li> <li>DateTime</li> <li>AWGN</li> <li>Bits</li> <li>Errors</li> <li>BER</li> <li>LowerError</li> <li>UpperError</li> </ul> </li> </ul>		
Ok Cancel		

Figure 282:Export: BER-AWGN Sweep Data Selection with Some Parameters Checked

A simple Bit Error Rate with a single series, shown in Figure 283, produces a single series .csv export dialog box, as shown in Figure 284.

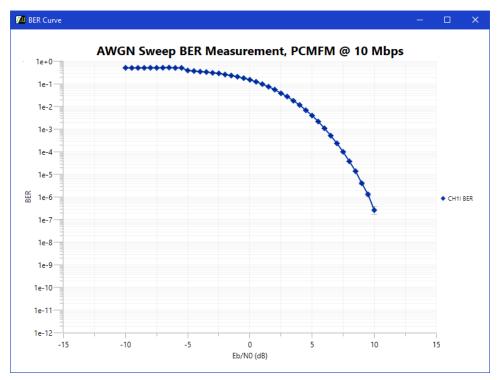


Figure 283:Simple BER with Single Series Chart

🖬 Export: BER - AWGN Sweep	-	×
Select data to be exported		
<ul> <li>BER - AWGN Sweep</li> <li>BERCH11 Select Series and Datapoint</li> <li>Series:         <ul> <li>BER</li> <li>Datapoints:             <ul> <li>DateTime</li> <li>AWGN</li> <li>Bits</li> <li>Errors</li> <li>BER</li> <li>LowerError</li> <li>UpperError</li> </ul> </li> </ul> </li> </ul>		
Ok Cancel		

Figure 284: Single Series Export .CSV Dialog Box

### 3<sup>rd</sup> Generation Receiver Analyzer

Some measurements produce multiple independent variables and a set of datapoints for each, as shown in Figure 285 and Figure 286.

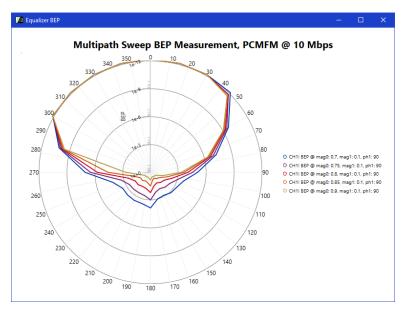


Figure 285: Measurement Chart with Multiple Independent Variables

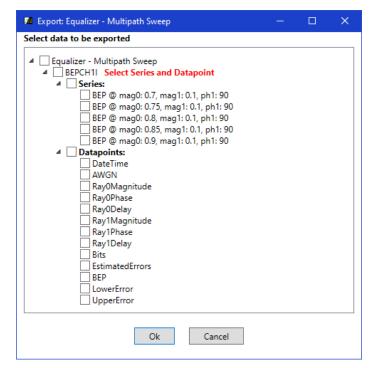


Figure 286: Export Data Selection with Datapoints for Multiple Variables

Some measurements produce complex sets of results that can be difficult to express in a single .csv file, such as the case where the Bit Error Rate measurement also collects Bit Error Probability Correlation. The BEP/BER measurement creates data that can be expressed in one or more two-dimension curves, as shown in Figure 287. The BER.BEP correlation results in an entirely different class of data (a histogram).

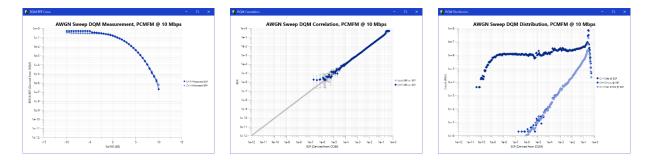


Figure 287: Complex Measurement Results Charts

Export: DQM Verification - AWGN Sweep elect data to be exported		×
DQM Verification - AWGN Sweep		
BERCH1I Select Series and Datapoint		
A Series:		
Datapoints:     DateTime		
AWGN		
Bits		
Errors		
BER		
BEPCH1I     Select Series and Datapoint		
A Series:		
BEP		
Datapoints:		
DateTime		
AWGN		
Bits		
EstimatedErrors		
BEP		
LowerError		
UpperError     DQMBINSCH1I Select Series and Datapoint		
Series:		
Datapoints:		
DateTime		
DQMCorrelator.BinIndex		
DQMCorrelator.BinValue		
DQMCorrelator.Bits		
DQMCorrelator.Errors		
DQMCorrelator.BER		
DQMCorrelator.LowerError		
DQMCorrelator.UpperError		
DQMCorrelator.EstimatedErrors		
Ok Cancel		

Figure 288: Complex Measurement Results Data Selection Parameters

Selecting multiple series and datapoints allows the option to create multiple .csv files. The names of these files will, by default, have suffixes appended to the base file name chosen by the user in the Save As dialog box, or the user may determine the name of each file. Examples are provided in Figure 289.

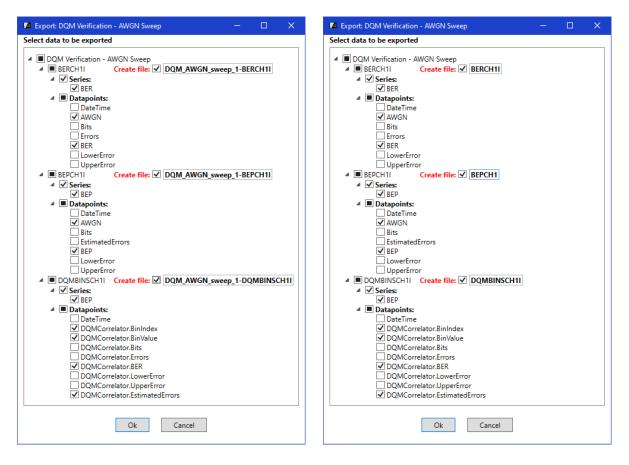


Figure 289: Option to Create Multiple .CSV Files in Data Selection Dialog Boxes

### 3<sup>rd</sup> Generation Receiver Analyzer

A unique file can be created for each series selected in a complex dataset, as shown in Figure 290.

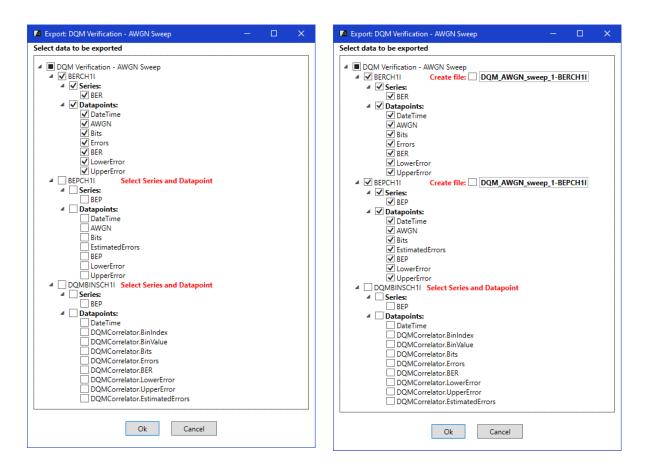


Figure 290: Option to Create Unique .CSV Files for Each Series Selected

Multiple series collected using the same independent variable can produce multiple files, as shown in Figure 291 and Figure 292.

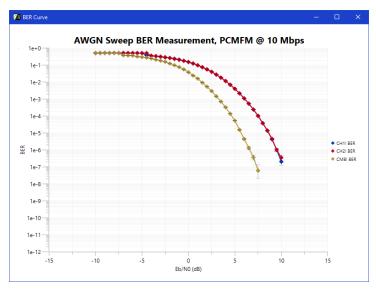


Figure 291: Multiple Series with One Independent Variable

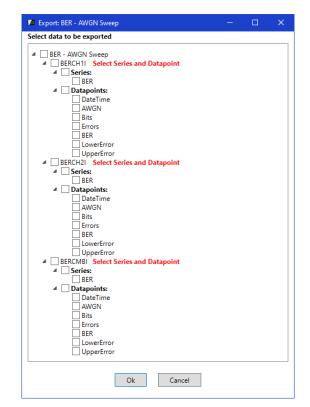


Figure 292: Export: Sweep Data Selection with Multiple Files



## 7 Maintenance Instructions

The RA requires no regular maintenance, and there are no user-serviceable parts inside.

## 8 Product Warranty

The RA carries a standard parts and labor warranty of one (1) year from the date of delivery.

### 8.1 Quasonix Limited Warranty Statement

This Limited Warranty Statement (this "Limited Warranty") applies to all hardware and software products and internal components of such products (the "Products") sold by Quasonix, or its representatives, authorized resellers, or country distributors (collectively referred to herein as "Quasonix"). EXCEPT AS EXPRESSLY SET FORTH IN THIS LIMITED WARRANTY, QUASONIX MAKES NO OTHER WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING ANY IMPLIED WARRANTIES OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE WITH RESPECT TO ANY PRODUCTS SOLD BY IT. Quasonix expressly disclaims all warranties and conditions not stated in this limited warranty. There are no warranties which extend beyond the description on the face hereof. Capitalized terms not otherwise defined herein shall have the meaning set forth in those certain General Terms and Conditions of Sale for Standard Product, as amended from time to time.

Quasonix warrants to customer that for one (1) year from the date of shipment of the Products by Quasonix (the "Warranty Period"), such Products purchased from Quasonix, or its authorized affiliate will materially conform to the specifications set forth in the applicable Quasonix Specifications, if any, and are free from defects in materials and workmanship under normal use during the Warranty Period. As used herein, "normal use" means the intended use of the Products for which it was designed by Quasonix.

This Limited Warranty extends only to the original purchaser of the Products and is not transferable to anyone who obtains ownership of the Products from the original purchaser.

Quasonix's software, whether incorporated into the Products or sold separately, is warranted solely to the extent that problems or "bugs" are found in the software and affect the functional operation of the Products. At no time shall requests for changes in the software architecture or visual esthetics be considered a warranty item.

The Products are manufactured using new materials only. Replacement parts may be new or equivalent to new. Replacement parts are warranted to be free from defects in material or workmanship for thirty (30) days or for the remainder of the Warranty Period of the Products in which they are installed, whichever is longer.

During the Warranty Period, Quasonix will repair or replace the defective Products. All components or hardware products removed from the Products under this Limited Warranty become the property of Quasonix. All warranties are limited to the repair or replacement of the Products.

In no event shall Quasonix be liable for any special, consequential, incidental or indirect damages of any kind, including, without limitation, loss of profits, loss of data, "down-time," loss of use or damage to other equipment, or personal injury or death, whether or not Quasonix has been advised of the possibility of such loss.

Notwithstanding anything to the contrary herein, Quasonix's entire liability hereunder from any cause whatsoever and regardless of the form of action shall be limited to the amount actually received by Quasonix.

Quasonix shall not be liable for a breach of the warranty set forth in this Limited Warranty unless: (i) the customer gives written notice of the defect, reasonably described, to Quasonix's Contracts Administrator within thirty (30) days of the time when customer discovers or ought to have discovered the defect and obtains a Return Materials Authorizations ("RMA") number; (ii) Quasonix is given a reasonable opportunity after receiving the notice to examine such Products and customer (if requested to do so by Quasonix) returns such Products to Quasonix's facility in Moorpark, CA, unless otherwise approved by Quasonix; and (iii) Quasonix reasonably verifies customer's claim that the Products are defective.

Subject to the foregoing, with respect to any such Products during the Warranty Period, Quasonix shall, in its sole discretion, either: (i) repair or replace such Products (or the defective part) or (ii) credit or refund the price of such

Products at the pro rata contract rate provided that, if Quasonix so requests, customer shall, at Quasonix's expense, return such Products to Quasonix.

The customer is responsible for all costs associated with packaging and shipping of the defective Products to Quasonix's facility and clearly marking or affixing the given RMA number on the shipping label. Quasonix is not responsible for any loss or damage during shipment to Quasonix's facility. Following repair or replacement of covered Products, Quasonix will assume responsibility for the costs associated with the return of the material to the customer to an address provided by the customer. Notwithstanding the foregoing, items returned to Quasonix's facility and found to be operational or otherwise not covered by this Limited Warranty shall be returned to the customer at the customer's expense.

This Limited Warranty does not apply to expendable parts, such as cables, lamps, fuses, connectors, etc. This Limited Warranty does not extend to any Products which have been damaged or rendered defective (a) as a result of accident, misuse, abuse, or external causes; (b) by operation outside the usage parameters stated in the user documentation that shipped with the Products; (c) as a result of a failure to follow the instructions in the Operations & Maintenance Manual (d) by the use of parts not manufactured or sold by Quasonix; or I by modification or service by anyone other than (i) Quasonix, (ii) an Quasonix authorized service provider, or (iii) your own installation of end-user replaceable Quasonix or Quasonix approved parts if available for the Products in the servicing country.

THE TERMS OF THE WARRANTIES CONTAINED HEREIN DO NOT IN ANY WAY EXTEND TO ANY PRODUCT OR PART THEREOF OR SOFTWARE MATERIALS WHICH WERE NOT MANUFACTURED BY SELLER OR PREPARED BY SELLER OR ANY OF ITS AFFILIATES.

These terms and conditions constitute the complete and exclusive warranty agreement between the customer and Quasonix regarding the Products purchased. This Limited Warranty is applicable in all countries and may be enforced in any country where Quasonix or its authorized affiliates offer warranty service subject to the terms and conditions set forth in this Limited Warranty.

These terms and conditions supersede any prior agreements or representations (including representations made in Quasonix sales literature or advice given to the customer by Quasonix or an agent or employee of Quasonix) that may have been made in connection with the purchase of the Products. No change to the conditions of this Limited Warranty is valid unless it is made in writing and signed by an authorized representative of Quasonix.

#### 8.1.1 Extended Warranties

Extended warranties or extra coverage are available upon request. Please contact Quasonix for details and pricing.

#### THE REMEDIES SET FORTH IN THIS LIMITED WARRANTY STATEMENT SHALL BE THE BUYER'S SOLE AND EXCLUSIVE REMEDY AND SELLER'S ENTIRE LIABILITY FOR ANY BREACH OF THE LIMITED WARRANTY SET FORTH HEREIN.

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# 9 Technical Support and RMA Requests

In the event of a product issue, customers should contact Quasonix via phone (1-513-942-1287) or e-mail (support@quasonix.com) to seek technical support. If the Quasonix representative determines that the product issue must be addressed at Quasonix, a returned materials authorization (RMA) number will be provided for return shipment.

Authorized return shipments must be addressed in the following manner:

#### Quasonix, Inc. ATTN: Repair, RMA # 6025 Schumacher Park Drive West Chester, OH 45069

To ensure that your shipment is processed most efficiently, please include the following information with your product return:

- Ship To Company name, address, zip code, and internal mail-drop, if applicable
- Attention/Contact person Name, Title, Department, Phone number, email address
- Purchase Order Number If applicable
- RMA Number provided by the Quasonix representative

Please note that Quasonix reserves the right to refuse shipments that arrive without RMA numbers.

## **10** Appendix A – RxAn Project Files

As mentioned in section 6.3.1.2, project files contain all of the RA settings in effect at the time the project file was saved, including the RxAn GUI layout. This is particularly useful for establishing a known-good repeatable configuration for measuring or to leave a "breadcrumb" during troubleshooting or analysis. Certain state information, such as what measurement was most recently loaded in the Measurement panel, is not saved. Since layouts can also be saved separately, it is easy to load a project and then to load an alternate layout if desired. There is no practical limit to the number of projects that may be saved for later use.

The provided projects contain some settings that are required for use with the provided measurement scripts. It is strongly advised that these projects, or projects derived from them, be used when running these measurements. **These baseline projects should never be modified.** 

BasicSTC.rxan is similar to Basic.rxan but uses a pair of modulators and STC encoding to generate a composite STC waveform on both RF output channels, as shown in Figure 293. This composite waveform has identical amplitude, phase, and delay on both components of the STC waveform.

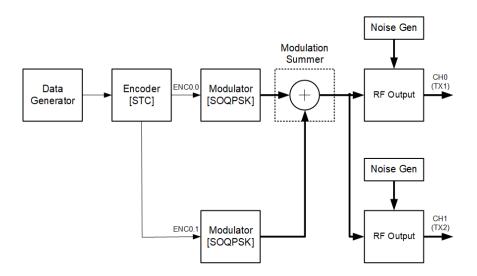


Figure 293: Example of Project BasicSTC.Rxan

CompleteSTC.rxan uses multipath capability to allow varying the amplitude, phase, or delay on either component of the STC waveform, as shown in Figure 294. By default, it still outputs the composite STC waveform on both RF output channels, but it can easily be configured to output pilot 1 on RF channel 1 and pilot 2 on RF channel 2 or vice versa.

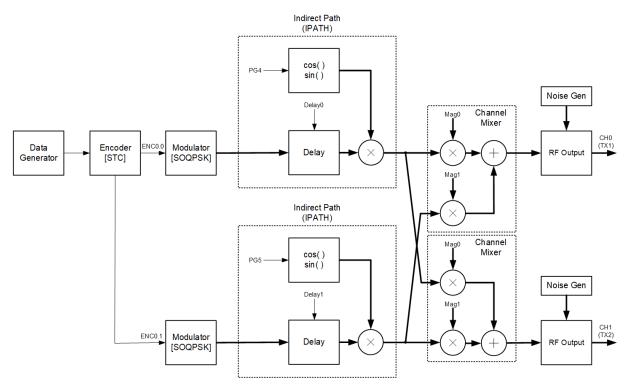


Figure 294: Example of Project CompleteSTC.Rxan

# 11 Appendix B – Data Quality Encapsulation (DQE) Concepts

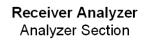
Data Quality Encapsulation is the process of bundling data quality information along with payload data. This information is intended for use by a Best Source Selector (BSS) to optimally select correct payload data bits from amongst multiple streams of potentially errored payload data. Detailed information about DQE and DQM is available in IRIG 106-22, Appendix 2-G.

Data quality is encoded as a Data Quality Metric (DQM). When calibrated per a standardized procedure, DQM based on bit error probability (BEP) allows DQE from multiple vendors to interoperate.

Bit Error Probability (BEP) is the calculated likelihood that a bit is in error. A very low BEP can be determined from only a few bits. BEP does not require any known data and can be determined quickly and accurately from demodulator statistics. It is an unbiased quality metric, regardless of channel impairments. The DQM is calculated directly from BEP.

The basic DQM calibration process is described in the following steps and illustrated in Figure 295.

- 1. Input corrupted data (with clock)
- 2. Extract the frame sync word
- 3. Measure the BER of payload data
- 4. Compare DQM (converted to BEP) to measured BER and record/store on a packet by packet basis
- 5. Post process BEP and BER to develop score



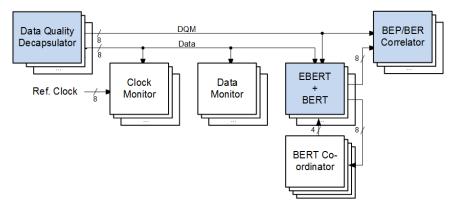


Figure 295: DQM Calibration Fixture Process

Payload data is a user selectable length with a default of 4096 bits, unless block decoding is in use. In STC mode, the default is 3200 bits. In SOQPSK/LDPC or STC/LDPC mode, the default is the selected LDPC block size. When Reed-Solomon decoding is enabled, the default is 1024 bits.

With a payload data length of 4096 bits, the network bandwidth expansion is  $\sim 1\%$ .

DQM accuracy may be verified under various channel impairments including AWGN-static level, AWGN-dynamic level (step response), dropouts, in-band and adjacent channel interference, phase noise, timing jitter, static multipath, and dynamic multipath (similar to break frequency).

## 11.1 RA BER vs. BEP Measurement Display

As noted above, interoperability of DQE from various equipment manufacturers requires that all adhere to the IRIG standard. Also, DQM values must be accurate estimates of actual bit error probability, or source selectors will suffer degraded performance. Therefore, the ability to verify DQE conformance and DQM accuracy is critical.

The RA BEP/BER Correlator is a powerful tool for measuring DQM accuracy. It works by accumulating actual error counts and estimated error counts for every DQM value received. The comparison of these indicates DQM accuracy.

DQM values have a very fine level of granularity, with 65,536 possible values representing BEPs from 0.5 to 1e-12. Accumulating statistics for each individual DQM value might take a very large amount of test time. To accelerate testing, the 65,536 possible DQM values are grouped into 256 bins, each containing 256 successive DQM values. The resulting bins still have good granularity, representing BEP steps of approximately 1.1x (e.g., one bin represents a BEP of 1.0e-6, and the next bit represents a BEP of 1.1e-6).

Each bin has three pieces of information associated with it:

- A count of bits from all DQE frames with DQM value in the bin
- A count of errors from all DQE frames with DQM value in the bin
- The average BEP represented by the 256 DQM values in the bin

For each DQE frame received, the RA counts the number of bits and the number of bit errors, then adds those to a running total for the bin associated with the DQM value in the frame.

At the end of the measurement, each bin has a count of the total bits and the total bit errors that occurred in that bin. The ratio of these counts (errors/total) is the measured BER for each bin, which can be compared with the BEP associated with that bin.

The data is plotted in two ways:

- The count of bits and bit errors for each bin, as shown in Figure 296
- The calculated BER for each bin, as shown in Figure 297

In these instances, the x axis is the BEP associated with each bin—BEP (Derived from DQM).

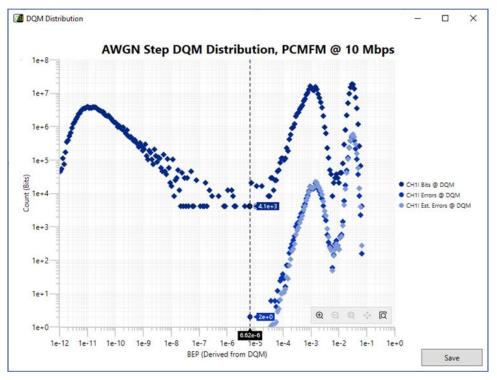


Figure 296: Count of Bits and Bit Errors in Each Bin

In Figure 296, the dark blue dots show the total count of bits in each bin, generally ranging between 4,000 and 20,000,000 in this example. The three peaks correspond to three AWGN step levels.

The light blue dots show the total count of bit errors in each bin, ranging from 2 to 600,000, in this example (Figure 296).

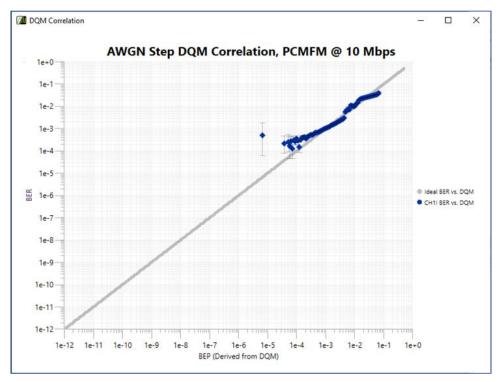


Figure 297: Calculated BER in Each Bin (Bits/Errors)

In Figure 297, the blue dots show the calculated bit error rate (BER) in each bin, ranging from 7e-6 to 6e-2 (dividing error count by the total count from Figure 296).

The error bars indicate the 95% confidence level for the actual BER if the measurement could run infinitely long and are large, where the count of bit errors is small.

The grey line shows the ideal result (BER = BEP). Deviation from the ideal result indicates suboptimal DQM estimation. The amount of deviation is a direct indicator of the potential degradation of BSS performance. At low BEP, a few outlier points may appear due to insufficient error counts for reliable statistics; these should have large error bars and are expected and normal. However, no points should lie on the y axis.

## **12 Appendix C – Firmware Updates**

### 12.1 RA 3.0 Firmware Update

The RA hardware contains an Altera FPGA with embedded microprocessor. Each requires its own firmware to operate the RA. RxAn requires a specific version of firmware. If the firmware in the RA hardware does not match the version required by RxAn, a warning is displayed, as shown in the tan banner at the bottom of Figure 298.

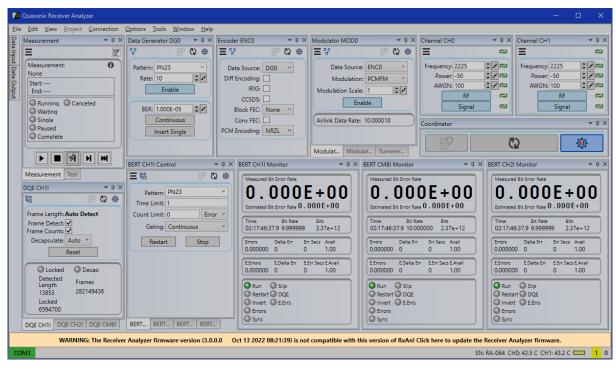


Figure 298: RxAn GUI Incompatible Firmware Warning Banner

If the firmware version is not compatible, the system still operates as best it can, but there may be unexpected errors.

The required version of firmware is embedded in the RxAn executable and can update the RA firmware, however RxAn requires a set of programming tools from Altera. These tools are available at:

 $https://downloads.intel.com/akdlm/software/acdsinst/17.0/290/ib\_installers/QuartusProProgrammerSetup-17.0.0.290-windows.exe$ 

This will download the Standalone Quartus Tools installer, as shown in Figure 299. When the download is complete, execute the file and follow the prompts. It is best to use the default options.

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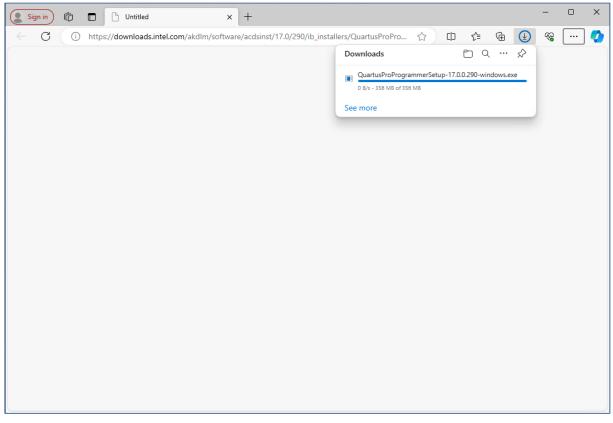


Figure 299: Download Quartus Tools Installer

To update the RA firmware, use the RxAn GUI Tools Menu on the Menu Bar (a), OR the Warning Banner (b).

a. Select Tools -> Firmware -> Update:

Tools	Window Help		
	RA Configuration		
	Firmware	×	Update
	Calibration	•	13
1	Properties	•	
1	Logs	•	
	API Server	+	
	Plugins	+	



b. Click on the version tan Warning Banner:

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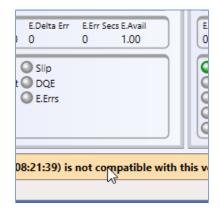


Figure 301: RxAn GUI Tan Warning Banner

Option a) and b) display the RA Firmware Update dialog box, shown in Figure 302.

	date			—		>
Tools						
V Conf	ig: C:\intelFPGA_pro\17.0\gp	orogrammer\bin64\jtagco	nfig.exe			
	er: C:\intelFPGA_pro\17.0\qp		-			
Files						
						_
	le: 🗹 C:\Users\					
-	le: 🗸 C:\Users\\\\\\.qua					
	le: C:\Users\quasor	iix\Firmware\N25Q064A.	xt			
Backup Fold	er: C:\Users\					
					Resca	n
Cables						
JTAG Cab	le: USB-Blasterll [USB-3] (50	E(BA7 FA7 FA7ES))				v
-					Resca	-
					Nesca	
Extracted Elf File Extracted Sof File Extracted Overrid						
Extracted Sof File Extracted Overrid Finding JTAG cable	le File 25					
Extracted Sof File Extracted Overrid inding JTAG cable 1) USB-Blaster [U	le File :s SB-3]	skon		_	_	
Extracted Sof File Extracted Overrid inding JTAG cable 1) USB-Blaster [U	le File :s SB-3] device chain - JTAG chain bro	oken			_	
Extracted Sof File Extracted Overrid Finding JTAG cable 1) USB-Blaster (U Unable to read 2) USB-Blaster (U	le File :s SB-3] device chain - JTAG chain bro		_	_	_	
Extracted Sof File Extracted Overric inding JTAG cable 1) USB-Blaster [U Unable to read 2) USB-Blaster [U Unable to lock of 3) USB-Blasterll [	e File <del>IS</del> SB-3] device chain - JTAG chain bro SB-4] thain - Hardware not attache USB-1]	d	_	_	-	
Extracted Sof File Extracted Overric inding JTAG cable 1) USB-Blaster [U Unable to read 2) USB-Blaster [U Unable to lock of 3) USB-Blasterll [ Unable to read	e File <del>IS</del> SB-3] device chain - JTAG chain bro SB-4] thain - Hardware not attache USB-1] device chain - JTAG chain bro	d		_		
Extracted Sof File Extracted Overrice inding JTAG cable 1) USB-Blaster [U Unable to read 2) USB-Blaster [U Unable to lock of 3) USB-BlasterII [ Unable to read 4) USB-BlasterII [	e File <del>IS</del> SB-3] device chain - JTAG chain bro SB-4] thain - Hardware not attache USB-1] device chain - JTAG chain bro USB-2]	d oken				
Extracted Sof File Extracted Overric Finding JTAG cable 1) USB-Blaster [U Unable to read 2) USB-Blaster [U Unable to lock of 3) USB-BlasterII [ Unable to read 4) USB-BlasterII [	e File <del>IS</del> SB-3] device chain - JTAG chain bro SB-4] thain - Hardware not attache USB-1] device chain - JTAG chain bro USB-2] device chain - JTAG chain bro	d oken				
Extracted Sof File Extracted Overrid finding JTAG cable 1) USB-Blaster [U Unable to read 2) USB-Blaster [U Unable to lock of 3) USB-Blasterl [ Unable to read 4) USB-Blasterl [ Unable to read 5) USB-Blasterl [	e File <del>IS</del> SB-3] device chain - JTAG chain bro SB-4] thain - Hardware not attache USB-1] device chain - JTAG chain bro USB-2] device chain - JTAG chain bro	d oken				
Extracted Sof File Extracted Overrid finding JTAG cable 1) USB-Blaster [U Unable to read 2) USB-Blaster [U Unable to lock of 3) USB-Blasterl [ Unable to read 4) USB-Blasterl [ Unable to read 5) USB-Blasterl [	le File SB-3] device chain - JTAG chain bro SB-4] shain - Hardware not attache USB-1] device chain - JTAG chain bro USB-2] device chain - JTAG chain bro USB-3]	d oken	Save	Сору	Cle	<u>a</u> r



If the Altera programming tools have not been installed, the RA Firmware Update dialog box displays an error, as shown in Figure 303.

🛚 RA Firmware Upda	te					_		Х
Tools								
Config:								
	[							***
Programmer:								
Files								
✓ Elf File:	✓ C:\Users\\.qu	asonix\Firmware\ra.el	flash					
	C:\Users\							
-	C:\Users							
-	C:\Users\							
<ul> <li>Backup Folder:</li> </ul>	C:\USEIS\							
							Rescar	n
Cables								
JTAG Cable:								~
A Cable.								_
							Rescar	n
irmware Revision: 3	101							
inniware nevision: 5	1.0.1							
Extracting RA3 progr Extracted Elf File Extracted Sof File Extracted Override	File							
Finding JTAG cables								
					Save	Сору	Clea	ar
				Update	Revert	Cancel	Close	
				opdate	Revent	Calicel	0.050	
t ready				Opdate	Revent	Calicer	ciose	

Figure 303: RA Firmware Update Dialog Box Programming Tools Not Found Message

#### 12.1.1 Update Firmware

The Update Firmware dialog box has the following sections:

- Tools
- Files

- Cables
- Firmware Revision
- Messages
- Action Buttons
- Status

#### 12.1.1.1 Tools

The Tools section, highlighted in Figure 304, shows the paths to the two executables RxAn uses to update the RA firmware. If the executable files were found, a green checkmark displays to the left of each file.

MI RA Firmware Update -		×
Tools		
Config: C:\intelFPGA_pro\17.0\qprogrammer\bin64\jtagconfig.exe		
✓ Programmer: C:\intelFPGA_pro\17.0\qprogrammer\bin64\quartus_pgm.exe		
Files		=1
✓ Elf File: ✓ C:\Users quasonix\Firmware\ra.elf.flash		
SOF File: C:\Users\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
✓ Override File: C:\Users\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		
✓ Backup Folder: C:\Users\		
	Resca	n
Cables		
JTAG Cable: USB-BlasterII [USB-3] (5CE(BA7 FA7 FA7ES))		~
	Resca	
	Nesca	
Firmware Revision: 3.1.0.1		
Extracted Elf File		
Extracted Sof File		î
Extracted Override File		
Finding JTAG cables		
1) USB-Blaster [USB-3]		
Unable to read device chain - JTAG chain broken		
2) USB-Blaster [USB-4] Unable to lock chain - Hardware not attached		
3) USB-Blasterili (USB-11		
Unable to read device chain - JTAG chain broken		
4) USB-Blasterll [USB-2]		
Unable to read device chain - JTAG chain broken		
5) USB-Blasterll [USB-3]		
02B130DD 5CE(BA7 FA7 FA7ES)		~
<u>S</u> ave <u>C</u> opy	Clej	<u>a</u> r
Update Revert Cancel	Close	
Ready		

Figure 304: RA Firmware Update Dialog Box, Tools Section

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The user may also manually select the executable files by clicking on the Browse button, shown in Figure 305.

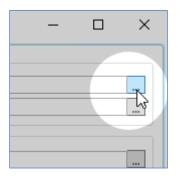


Figure 305: RA Firmware Update Dialog Box, Tools Section, Browse Button

#### 12.1.1.2 Files

The Files section, highlighted in Figure 306, shows the paths to the firmware update images (Elf/SOF), an override file (used to tell the Altera tools what it is programming), and a folder to place the backup firmware images.

🕅 RA Firmware Update			
Tools			
Config: C:\intelFPGA_pro\17.0\qprogrammer\bin64\jtaqconfig.exe			
Programmer: C:\intelFPGA_pro\17.0\gprogrammer\bin64\guartus_pgm.exe			
Files			_
Elf File: C:\Users\\quasonix\Firmware\ra.elf.flash			
SOF File: C:\Users\\\_quasonix\Firmware\ra.sof.flash			
Verride File: C:\Users\@@@.\quasonix\Firmware\N25Q064A.txt			
Backup Folder: C:\Users			
		Resca	n
Cables			
JTAG Cable: USB-Blasterll [USB-3] (5CE(BA7[FA7[FA7ES])			~
V MAG Cable. USB-blastelii [USB-5] (SCE(BAT FAT FATE3))			_
		Resca	n
Firmware Revision: 3.1.0.1			
Extracted Elf File			^
Extracted Sof File			
Extracted Override File			
Finding JTAG cables			_
1) USB-Blaster [USB-3] Unable to read device chain - JTAG chain broken			
2) USB-Blaster (USB-4)			
Unable to lock chain - Hardware not attached			
3) USB-Blasterli [USB-1]			
Unable to read device chain - JTAG chain broken			
4) USB-BlasterII [USB-2]			
Unable to read device chain - JTAG chain broken			
5) USB-Blasterll [USB-3] 02B130DD 5CE(BA7 FA7ES)			
			<b>_</b>
<u>Save</u>	ору	Cle	<u>a</u> r
Update Revert Can	cel	Close	
Ready			

Figure 306: RA Firmware Update Dialog Box, Files Section

The user may also manually select the update files or select an alternate backup location by clicking on the Browse button, as highlighted in Figure 307.

Files		
$\checkmark$	Elf File: 🗹 C:\Users\;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;;	
$\checkmark$	SOF File: 🔽 C:\Users\quasonix\Firmware\ra.sof.flash	
✓ c	verride File: C:\Users\@@@@.quasonix\Firmware\N25Q064A.txt	
V Ba	kup Folder: C:\Users\	
		Rescan

Figure 307: RA Firmware Update Dialog Box, Files Section, Browse Button

By default, the SOF (FPGA) and Elf (microprocessor) firmware are updated. The user can control which are updated using the check boxes next to the SOF and Elf files, as shown in Figure 308.

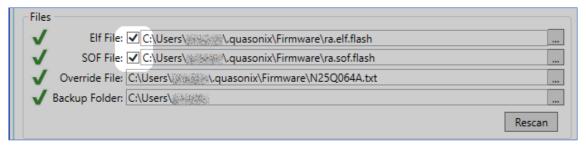


Figure 308: RA Firmware Update Dialog Box, Files Section, Update Selection Check Boxes

The Rescan button, highlighted in Figure 309, directs RxAn to attempt to find these files/locations again if there has been an error, or the user wants to return to the default files.

Rescan
Rescan

Figure 309: RA Firmware Update Dialog Box, Files, Rescan Button

#### 12.1.1.3 Cables

Inside the RA is a JTAG programming cable that is used by the Altera tools to program the firmware updates. Each RA has one JTAG cable, as highlighted in Figure 310. If you have multiple RAs attached to your PC, multiple JTAG cables will be found. RxAn populates the JTAG Cable drop down menu with a list of JTAG cables it finds. The user may select which cable to use.

📶 RA Firmware Update	—	□ ×
Tools		
Config: C:\intelFPGA_pro\17.0\qprogrammer\bin64\jtagconfig.exe		
✓ Programmer: C:\intelFPGA_pro\17.0\qprogrammer\bin64\quartus_pgm.exe		
Files		
✓ Elf File: ✓ C:\Users\quasonix\Firmware\ra.elf.flash		
SOF File: C:\Users\quasonix\Firmware\ra.sof.flash		
Verride File: C:\Users\@@@w.quasonix\Firmware\N25Q064A.txt		
✓ Backup Folder: C:\Users\		
		Rescan
		Rescon
JTAG Cable: USB-Blasteril [USB-3] (5CE(BA7 FA7ES))		
		Rescan
Firmware Revision: 3.1.0.1		
Extracted Elf File		^
Extracted Sof File		
Extracted Override File Finding JTAG cables		
1) USB-Blaster [USB-3]		
Unable to read device chain - JTAG chain broken		
2) USB-Blaster [USB-4]		
Unable to lock chain - Hardware not attached		
3) USB-Blasteril [USB-1] Unable to read device chain - JTAG chain broken		
4) USB-Blasteril [USB-2]		
Unable to read device chain - JTAG chain broken		
5) USB-Blasteril [USB-3]		
02B130DD 5CE(BA7 FA7 FA7ES)		~
<u>Save</u>	ору	Cle <u>a</u> r
Update Revert Cano	:el	Close
Ready		

Figure 310: RA Firmware Update Dialog Box, Cables Section

The Rescan button, highlighted in Figure 311, directs RxAn to rescan for JTAG cables.

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Figure 311: RA Firmware Update Dialog Box, Cables, Rescan Button

#### 12.1.1.4 Firmware Revision

The Firmware Revision, highlighted in Figure 312, shows the revision of the firmware to be programmed.

Cables		USB-Blasterll [USB-3] (5CE(BA7 FA7 FA7ES))	
		Rescan	
Firmwar	e Revision: 3.	1.0.1	

Figure 312: RA Firmware Update Dialog Box, Firmware Revision

#### 12.1.1.5 Messages

The Message section, shown in Figure 313, displays various messages generated during the Firmware Update process. The messages can be saved to a file using the Save button, copied to the clipboard using the Copy button, or cleared, using the Clear button.

💯 RA Firmware Update			×
Tools			
Config: C:\intelFPGA_pro\17.0\qprogrammer\bin64\jtagconfig.exe			
Programmer: C:\intelFPGA_pro\17.0\qprogrammer\bin64\quartus_pgm.exe			
Files			$\equiv$
✓ Elf File:      C:\Users quasonix\Firmware\ra.elf.flash			
✓ SOF File: ☑ C:\Users,,quasonix\Firmware\ra.sof.flash			
✓ Override File: C:\Users\@@@.quasonix\Firmware\N25Q064A.txt			
✓ Backup Folder: C:\Users\			
		Resca	n
Cables			
JTAG Cable: USB-Blasterll [USB-3] (5CE(BA7 FA7 FA7ES))			-
V STAG cable. USB-blasten [USB-5] (Set(DATTATTATES))			- 1
		Resca	<u>n</u>
Firmware Revision: 3.1.0.1			
Extracted Elf File			^
Extracted Sof File Extracted Override File			
Finding JTAG cables			
1) USB-Blaster [USB-3]			
Unable to read device chain - JTAG chain broken			
2) USB-Blaster [USB-4]			
Unable to lock chain - Hardware not attached			
3) USB-Blasterll [USB-1] Unable to read device chain - JTAG chain broken			
4) USB-Blasterli [USB-2]			
Unable to read device chain - JTAG chain broken			
5) USB-Blasterll [USB-3]			
02B130DD 5CE(BA7 FA7 FA7ES)			~
<u>S</u> ave <u>C</u> o	ру	Clej	<u>a</u> r
Update Revert Canc	el	Close	
Ready			

Figure 313: RA Firmware Update Dialog Box, Message Section

#### 12.1.1.6 Action Buttons

The Action buttons, highlighted in Figure 314, allow the user to Update the firmware using the selected files, Revert using a backup, Cancel an update in progress, or Close the Firmware Update dialog box.

Extracted Override File	
Finding JTAG cables	
1) USB-Blaster [USB-3]	
Unable to read device chain - JTAG chain broken	
2) USB-Blaster [USB-4]	
Unable to lock chain - Hardware not attached	
3) USB-Blasterll [USB-1]	
Unable to read device chain - JTAG chain broken	
4) USB-Blasterll [USB-2]	
Unable to read device chain - JTAG chain broken	
5) USB-Blasterll [USB-3]	
02B130DD 5CE(BA7 FA7 FA7ES)	<b>v</b>
	<u>S</u> ave <u>C</u> opy Cle <u>a</u> r
	Update Revert Cancel Close
Ready	

Figure 314: RA Firmware Update Dialog Box, Action Buttons

#### 12.1.1.6.1 Update Process

After the Update button is clicked, the firmware update process starts by extracting the entire contents of the RA firmware and storing it in the folder specified by the Backup Folder parameter in the Files section. The filename is based on the RA serial number, the major, minor, revision, and build number, and a date/time stamp. This helps ensure that no firmware backup file is inadvertently overwritten.

The SOF (if enabled) and Elf (if enabled) files are loaded and combined into a single image for programming.

The combined image is programmed into the RA. Any stored parameters in the RA are erased to ensure the new firmware is starting from a default configuration.

The entire contents of the RA firmware is then extracted again, and a new backup is created in the backup folder.

This allows the user to swap between any older versions of firmware if needed.

At the end of the update a final message displays, as shown in Figure 315.



Figure 315: RxAn Firmware Update Messages

#### 12.1.1.6.2 Revert

The Revert button allows the user to restore any of the backup files. An Open window, shown in Figure, provides a list of available backups. Select the backup file, then click on the Open button to immediately start the revert.

→ × ↑ 🚨 ›	Gregg Woo	d >			~ Ō	Search Gr	egg Wood		,
rganize 👻 New fo	older								
Targets	* ^	Name	Date modified	Туре	Siz				
Demod3.0	*	RA3 RA-064 3 0 14 130 20230622093531.full.flash	6/22/2023 9:35 AM	FLASH File		19,964 KB			
RA3.0	*	RA3_RA-064_3_0_14_130_20230622100729.full.flash	6/22/2023 3:33 AM	FLASH File		19,964 KB			
Ouasonix	*	RA3 RA-064 3 0 14 130 20230622100725.101.11851	6/22/2023 10:07 AM	FLASH File		19,964 KB			
Demod4.0	*	RA3 RA-064 3 0 14 130 20230622105809.full.flash	6/22/2023 10:58 AM	FLASH File		19,964 KB			
		RA3 RA-064 3 0 14 130 20230622124419.full.flash	6/22/2023 12:44 PM	FLASH File		19.964 KB			
greggw	*	RA3_RA-064_3_0_14_130_20230622125131.full.flash	6/22/2023 12:51 PM	FLASH File		19,964 KB			
VirtualStore	*	RA3 RA-064 3 0 14 130 20230622134514.full.flash	6/22/2023 1:45 PM	FLASH File		19.964 KB			
IODef	*	RA3 RA-064 3 0 14 130 20230622134712.full.flash	6/22/2023 1:47 PM	FLASH File		19,964 KB			
RA3.0	*	RA3 RA-064 3 0 14 130 20230622162952.full.flash	6/22/2023 4:29 PM	FLASH File		19.964 KB			
GWood	*	RA3 RA-064 3 0 14 130 20230622163147.full.flash	6/22/2023 4:31 PM	FLASH File		19,964 KB			
Projects	*	RA3_RA-064_3_0_14_151_20230629165313.full.flash	6/29/2023 4:53 PM	FLASH File		19,964 KB			
Targets	*	RA3_RA-064_3_0_15_1_20230629135401.full.flash	6/29/2023 1:54 PM	FLASH File		19,964 KB			
projects	*	RA3_RA-064_3_0_15_2_20230629203203.full.flash	6/29/2023 8:32 PM	FLASH File		19,964 KB			
- projects	<u>^</u>	RA3_RA-064_3_0_15_2_20230629203824.full.flash	6/29/2023 8:38 PM	FLASH File		19,964 KB			
🕃 Dropbox		RA3_RA-064_3_0_15_2_20230629204022.full.flash	6/29/2023 8:40 PM	FLASH File		19,964 KB			
OneDrive - Persona	al	RA3_RA-064_3_0_15_2_20230629204109.full.flash	6/29/2023 8:41 PM	FLASH File		19,964 KB			
		RA3_RA-064_3_0_15_2_20230629204308.full.flash	6/29/2023 8:43 PM	FLASH File		19,964 KB			
This PC		RA3_RA-064_3_0_15_2_20230630093920.full.flash	6/30/2023 9:38 AM	FLASH File		19,964 KB			
🧊 3D Objects		RA3_RA-064_3_0_15_8_20230630094252.full.flash	6/30/2023 9:42 AM	FLASH File		19,964 KB			
E Desktop		RA3_RA-064_3_0_15_8_20230630095117.full.flash	6/30/2023 9:50 AM	FLASH File		19,964 KB			
🕆 Documents		RA3_RA-064_3_0_15_8_20230630095332.full.flash	6/30/2023 9:53 AM	FLASH File		19,964 KB			
Downloads		RA3_RA-064_3_0_15_8_20230630102047.full.flash	6/30/2023 10:20 AM	FLASH File		19,964 KB			
Music		RA3_RA-064_3_0_15_10_20230630102303.full.flash	6/30/2023 10:22 AM	FLASH File		19,964 KB			
•		RA3_RA-064_3_0_15_10_20230705145113.full.flash	7/5/2023 2:50 PM	FLASH File		19,964 KB			
Pictures		RA3_RA-064_3_0_15_10_20230705145915.full.flash	7/5/2023 2:58 PM	FLASH File		19,964 KB			
🛃 Videos		RA3_RA-064_3_0_15_10_20230710165007.full.flash	7/10/2023 4:49 PM	FLASH File		19,964 KB			
L Windows (C:)	~	RA3_RA-064_3_0_16_7_20230710165341.full.flash	7/10/2023 4:53 PM	FLASH File		19,964 KB			
Fil	e <u>n</u> ame: 💽	\Users\greggw			~	All Files (	*.*)		
						Ope		Cancel	

Figure 316: Revert, Open File

#### 12.1.1.6.3 Status

A Status Bar is located at the bottom of the RA Firmware Update dialog box, as shown in Figure 317.



### 12.2 Upgrading from an RA2.0 to an RA3.0

When upgrading from an RA2.0 to an RA3.0, the first step must be a firmware update. While RxAn can update the RA2.0 firmware, it cannot communicate with an RA running RA2.0 firmware.

## 13 Appendix D – API Server

RxAn can host a REST API server. For detailed information about how to use the API server, refer to the Receiver Analyzer API Server User Manual (not yet available).

### 13.1 Start API Server From Shortcut

The API server can automatically be started when RxAn starts by specifying either -api or --apiserver on the command line. A shortcut can be created to RxAn to add command line options.

- 1. Open the RxAn installation folder (defaults to C:\Program Files\Quasonix\RxAn).
- 2. Create a link to RxAn.exe, as shown in Figure 318.

	📙   🛃 📕 🖛   C:\	Progra	m Files\Qu	М	anage	_		×
	File Home	Share	View	Applic	ation Tools			~ ?
	Pin to Quick Copy F access Clipboard		11		New folder New	Properties	•	Select
	$  \leftarrow \rightarrow \land \uparrow$	« Qu	ias → RxAn	ו	~ Ū	,○ Searce	ch RxAr	1
Create link in Desktop	✓ Quick access ■ Desktop ↓ Downloads ● Documents	* * *	Name FTDI-D Resour x64				3/ 3/ 3/	nte mo ^ 18/202 18/202 18/202
	Pictures	*	x86	-				18/202
	Program Files			exe nony.dll				/13/20
	Release	*		nDock.dl	I			/13/20
	Programs	4		nDock.Tł	nemes.Aero.d	П	10	/13/20
	Local Disk (C:		🗟 LibLog	g.dll			10	/13/20
		· ~			AsyncInterfac			/13/20
	This PC				nsions.Config			/13/20
	💣 Network				nsions.Config nsions.Config			/13/20 /13/20
					nsions.Contig nsions.Depen			/13/20
			< NICO:	SOILEXTE	iisioiis.Depeii	dencynijecu.	10	>
	61 items 1 item se	lected	18.3 MB					:==

Figure 318: Create Link to RxAn.exe

The created shortcut icon and the location it points to are shown in Figure 319.

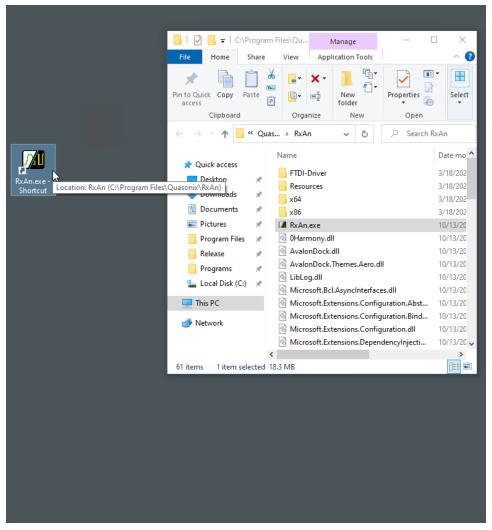


Figure 319: RxAn.exe Shortcut Created to Location Shown

3. Right click on the shortcut icon, then select Properties from the menu, as shown in Figure 320.

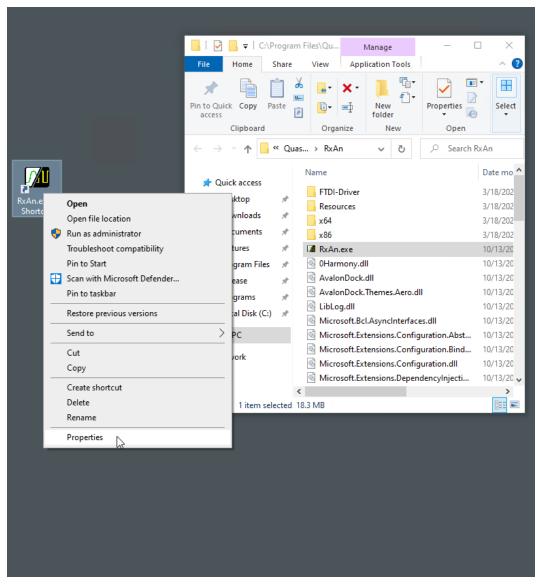


Figure 320: Shortcut Menu, Properties Selected

The shortcut properties dialog box is shown in Figure 321.

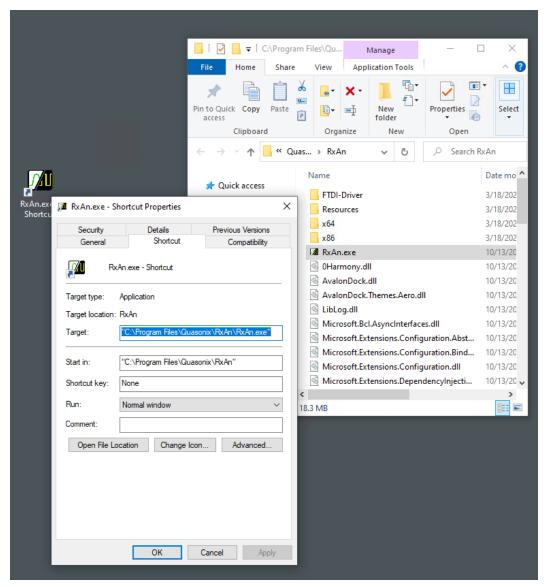


Figure 321: Shortcut Properties Dialog Box

4. In the Target field, add -api or --apiserver to the end of the line. Include the dashes, as shown in Figure 322.

		Program Files\Qu	Manage	- [	×
	File Home	Share View	Application Tools		× 🕐
	Pin to Quick Copy Placess	aste	× · New folder New	Properties Open	Select
		« Quas » RxAr		∠ Search Ro	An
	🖈 Quick access	Name	Driver		Date mo ^
RxAn.exe / RxAn.exe - Shortcut Proper	ies	× Resou			3/18/202
Security Details	Previous Versions	<b>x64</b>			3/18/202
General Short		<b>x86</b>			3/18/202
		🖾 RxAn.			10/13/20
RxAn.exe - Shortcu	t		nony.dll		10/13/20
			nDock.dll		10/13/20
Target type: Application			nDock.Themes.Aero.c	311	10/13/20
Target location: RxAn		SibLog	g.ciii soft.Bcl.AsyncInterfac	es dll	10/13/20
Target: C:\Program File	s\Quasonix\RxAn\RxAn.exe'' -a	ni –	soft.Extensions.Confi		10/13/20
			soft.Extensions.Confi	-	10/13/20
Start in: "C:\Program Fi	es\Quasonix\RxAn"		soft.Extensions.Confi	-	10/13/20
Shortcut key: None			soft.Extensions.Deper	-	10/13/20 🗸
		<			>
Run: Normal window		18.3 MB			
Comment:					
Open File Location Ch	ange Icon Advanced				
	Auvanceu				
ОК	Cancel Apply	,			

Figure 322: Shortcut Properties, Target Field with Added -api

5. Click on the OK button, as shown in Figure 323. When RxAn is started using this shortcut, the API Server is also started.

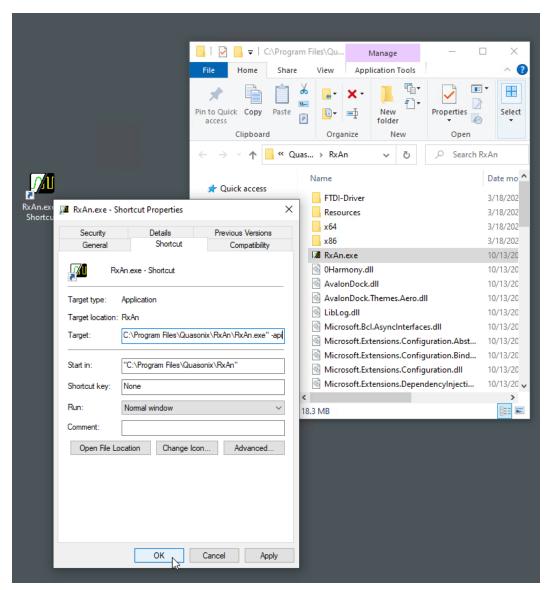


Figure 323: Shortcut Properties, OK Button Starts API Server

### 13.2 Start API Server From RxAn Tools Menu

The API server can be started, restarted, or stopped from the Tools > API Server menu, as shown in Figure 324.



### 3<sup>rd</sup> Generation Receiver Analyzer

Tool	Tools Window Help				Tools Window Help				Tools Window Help				
	RA Configuration	•				RA Configuration	•				RA Configuration	+	
	Firmware	•				Firmware	+				Firmware	+	
	Calibration	•				Calibration	•				Calibration	+	
	Properties	•				Properties	•				Properties	+	
	Logs	•				Logs	→				Logs	•	
	API Server	•	Start			API Server	•		Restart		API Server	•	Restart
			Stop						Stop				Stop
			Status						Status				Status

Figure 324: Start/Restart/Stop API Server Using Tools > API Server Menu

The status of the API server is available through Tools > API Server > Status option, as shown in Figure 325.

Tools	Window	Help		
	RA Configu	ration	•	
	Firmware		•	
	Calibration		•	
	Properties		+	
	Logs		+	
	API Server		×	Restart
				Stop
				Status

Figure 325: RxAn Tools > API Server > Status

The status is either Stopped or Running, as shown in Figure 326 and Figure 327. If the API Server status is Running, the URLs field is populated with the IP addresses the API server responds to. By default, the IP address is the IP address of the machine RxAn is running on. This is indicated by http://[::]:80.

📶 API Server Status - 🗆 🗙	🕅 API Server Status — 🗆 🗙
Status: Stopped	Status: Running
URLs:	URLs: http://[::]:80
Ok	Ok

Figure 326: API Server Stopped

Figure 327: API Server Running

# 14 Appendix E – Acronym List

Acronym	Description
АМ	Amplitude Modulation
AQPSK	Asymmetric (I/Q bit rates) Quadrature Phase Shift Keying
ARTM	Advanced Range Telemetry
ASM	Attached Sync Marker
AUQPSK	Asymmetric (I/Q bit rates) Unbalanced (I/Q power) Quadrature Phase Shift Keying
AWGN	Additive White Gaussian Noise
BEP	Bit Error Probability
BER	Bit Error Rate
BNC	Bayonet Neill-Concelman Connector (RF Connector)
BPSK	Binary Phase Shift Keying
СРМ	Continuous Phase Modulation
dBm	Decibels referenced to one milliwatt; A 0 dBm signal is equal to 1 milliwatt
DPM	Digital Phase Modulation
DQE	Data Quality Encapsulation
DQM	Data Quality Metric
Eb/N0	Energy per bit divided by noise; normalized signal-to-noise ratio
FEC	Forward Error Correction
IF	Intermediate Frequency
kHz	Kilohertz
LDPC	Low Density Parity Check
Mbps	Megabits per second
МНСРМ	multi-h Continuous Phase Modulation
MHz	Megahertz
OQPSK	Offset Quadrature Phase Shift Keying
PCM/FM	Pulse Code Modulation/Frequency Modulation
PM	Phase Modulation

Acronym	Description
PSK	Phase Shift Keying
QPSK	Quadrature Phase Shift Keying
RA	Receiver Analyzer
RF	Radio Frequency
RM	Rack Mount
RxAn	Receiver Analyzer Graphical User Interface
SOQPSK	Shaped Offset Quadrature Phase Shift Keying
TTL	Transistor Logic
UQPSK	Unbalanced (I/Q power) Quadrature Phase Shift Keying
USB	Universal Serial Bus
VAC	Volts Alternating Current