

## Technical Guide

# RDMS™ Bit Error Rate Testing

Quasonix, Inc.  
6025 Schumacher Park Dr.  
West Chester, OH 45069  
24 April 2022

**\*\*\* Revision 1.0.1 \*\*\***

Specifications subject to change without notice.

All Quasonix receiver products are under U.S. Department of Commerce jurisdiction categorized as 5A991; not covered by ITAR

Table of Contents

1 Bit Error Rate Testing ..... 2

1.1 Test Noise Commands ..... 8

List of Tables

Table 1: Bit Error Rate Serial Commands..... 2

Table 2: Test Noise Commands ..... 8

## 1 Bit Error Rate Testing

**\*Note:** The BER commands described in this document should be used with the Browser Interface for units that support it (Rack-Mount RDMS or Dual Channel Compact Receiver-Combiner).

There is a separate BERT for each data path in the receiver (0 main channel, 1 combiner channel). By default all commands are targeted to the current “tracking channel”, 0 or 1.

The results of the test can be displayed using the BER command with no parameters or a continuous display can be produced using BER D 1. BER D 0 turns off the continuous display.

**Table 1: Bit Error Rate Serial Commands**

Mnemonic	Name	Description
BER ?	Bit Error Rate Help	Provides help for using the bit error rate commands
BER	Set BER Tracking Display	<p>BER [&lt;e&gt; [-h][&lt;f&gt;][&lt;r&gt;]] - Sets bit error tracking display enable to &lt;e&gt; in format &lt;f&gt; at rate &lt;r&gt;</p> <p>where:</p> <ul style="list-style-type: none"> <li>&lt;e&gt; - Enable <ul style="list-style-type: none"> <li>0 - Disabled</li> <li>1 - Enabled</li> </ul> </li> <li>&lt;f&gt; - Format (last used, if not specified) <ul style="list-style-type: none"> <li>N - Normal</li> <li>W - Wide</li> <li>C - CSV</li> </ul> </li> <li>&lt;r&gt; - Display rate &lt;r&gt;, 100 to 60000 ms (500 ms if not specified)</li> </ul> <p>Options:</p> <ul style="list-style-type: none"> <li>-h - Do not display header</li> </ul> <p>Examples:</p> <ul style="list-style-type: none"> <li>BER 0           Set bit error tracking display to Disabled</li> <li>BER 1 N        Set bit error tracking display to Enabled with format set to Normal</li> <li>BER 1 W        Set bit error tracking format to Wide</li> <li>BER 1 W 142   Set bit error tracking format to Wide with a display rate of 142 ms</li> <li>BER 1 CSV      Set bit error tracking display to Comma Separated Values</li> </ul>

Mnemonic	Name	Description
BER DQ	Set Average DQ	<p>Sets average DQ for output (OUT) or datapath (DP) display enable to &lt;e&gt;</p> <p>BER [DQ {OUT DP} &lt;e&gt;]</p> <p>where &lt;e&gt;:</p> <p>0 - Disabled</p> <p>1 – Enabled</p> <p>Examples:</p> <p>BER DQ OUT 0      Set average DQ for output to Disabled</p> <p>BER DQ DP 1      Set average DQ for datapath to Enabled</p>
BER EST	Set Error Threshold	<p>BER [EST &lt;t&gt;]</p> <p>where:</p> <p>&lt;t&gt; - Sets error seconds threshold to 0 to 46666666</p> <p>Example:</p> <p>BER EST 420    Set error seconds threshold to 420</p>
BER G n	Set Bit Error Measurement Gating	<p>Sets bit error measurement gating</p> <p>The test can be configured to make a single measurement or, when a time limit, bit count, or error count is set, automatically repeat the test.</p> <p>BER [G &lt;n&gt;]</p> <p>Sets bit error measurement gating to &lt;n&gt;</p> <p>where &lt;n&gt; - Measurement gating</p> <p>S - Single</p> <p>R – Repeat</p> <p>C – Continuous (this is the default value)</p> <p>Examples:</p> <p>BER G S      Set gating to Single</p> <p>BER G R      Set gating to Repeat</p> <p>BER G C      Set gating to Continuous</p>
BER HEAD f	Show System Status Header	<p>BER [HEAD &lt;f&gt;] - Shows system status header in format &lt;f&gt;</p> <p>where &lt;f&gt; - Format</p> <p>N - Normal</p> <p>W - Wide</p> <p>C – CSV</p> <p>Examples:</p> <p>BER Head N    Show system status header format for Normal</p> <p>BER Head C    Show system status header format for Comma Separated Values</p>

Mnemonic	Name	Description
BER M	BERT Measurement Status	<p>Display current BERT measurement status in a specific style</p> <p>BER [M [&lt;s&gt;] [-h]]</p> <p>where:</p> <p style="padding-left: 40px;">&lt;s&gt; - Style</p> <p style="padding-left: 80px;">N - Normal</p> <p style="padding-left: 80px;">W - Wide</p> <p style="padding-left: 80px;">C - CSV</p> <p>Options:</p> <p style="padding-left: 40px;">-h - Do not display header</p> <p>Examples:</p> <p>BER M N      N indicates Normal</p> <p>BER M W      W indicates Wide</p> <p>BER M CSV    CSV indicates Comma Separated Values</p>
BER P n	Set Bit Error Test Pattern	<p>Set bit error test pattern to PN sequence or a fixed pattern</p> <p>BER [P {&lt;n&gt; &lt;p&gt;}]</p> <p>P indicates a preset or fixed pattern length</p> <p>where &lt;n&gt; is a PN sequence of</p> <p style="padding-left: 40px;">PN6, PN9, PN11, PN15, PN17, PN20, PN23, or PN31</p> <p style="padding-left: 40px;">&lt;p&gt; is a fixed pattern length of 2 to 32 bits</p> <p style="padding-left: 40px;">(fixed pattern lengths are automatically determined and leading zeros are significant)</p> <p>Examples:</p> <p>BER P PN9    Set test pattern to preset PN9</p> <p>BER P 13     Set test pattern to a fixed pattern length of 13 bits</p>
BER RR n	Set Restart Measurement	<p>Set restart measurements on resync enable</p> <p>BER [RR &lt;e&gt;]</p> <p>where &lt;e&gt; :</p> <p style="padding-left: 40px;">0 = Disable</p> <p style="padding-left: 40px;">1 = Enable</p> <p>Examples:</p> <p>BER RR 0     Set restart measurements on resync to Disabled</p>
BER R	Measurement Enable	<p>Starts or Restarts bit error measurement</p> <p>If a time limit, bit count, or error count limit is set and the limit has been reached (enabled but not running) the BERT is restarted.</p> <p>Example:</p> <p>BER R Start bit error measurement on current tracking channel</p>

Mnemonic	Name	Description
BER S	Measurement Disable	Stops bit error measurement Example: BER S Stop bit error measurement on current tracking channel
BER T n	Set Bit Error Measurement Type	<p>Sets bit error measurement type</p> <p>The test type can be configured to run continuously or stop when either a time limit, bit count, or error count has been reached. The error count limit guarantees a minimum number of errors.</p> <p>BER [T &lt;t&gt;]</p> <p>T indicates type</p> <p>where &lt;t&gt; is one of the following:</p> <p>C = Sets to Continuous (clears limits)</p> <p>T &lt;s&gt; = Sets BERT time limit</p> <p>where &lt;s&gt; is between 0.000 and 4294967.500 seconds</p> <p>B &lt;l&gt; = Sets bit limit</p> <p>where &lt;l&gt; is between 1 and 2.81475e+14 bits</p> <p>E &lt;l&gt; = Sets error limit</p> <p>Where &lt;l&gt; is between 0 and 2.81475e+14 errors</p> <p>Bit count and error count limits are mutually exclusive--either option may be combined with time limit</p> <p>Examples:</p> <p>BER T C Set bit error measurement type to Continuous</p> <p>BER T 2.333 Set BERT time limit to 2.333 seconds</p> <p>BER T B 1 Set bit limit to 1 bit</p> <p>BER T E 0.5 Set error limit to 0.5 errors</p>

The following examples illustrate how to display bit error registers in Normal, Wide, and CSV format. Note the details below.

- A column header is displayed every ten rows.
- The 'E' column indicates the BERT is enabled by displaying a pound sign '#'. If the BERT is not enabled, this column is blank.
- The 'R' column indicates the BERT is actually running and making a measurement by displaying a '!'. If the BERT is not running, this column is blank.
- The BERT can be enabled, but not running, in the case of a time, bit, or error count limit.
- An asterisk '\*' after the error rate column indicates that the data is inverted. If the display is continuous and a time, bit, or error limit is set with repeating gating, the display shows the end of the test by displaying '>>>' in the first three columns.

## Normal

```

R S I      Time      Bits      Errors      Delta Err      Error Rate      Seconds      Eb/N0 L
! * *      99:59:59.999 9999999999 9999999999 9999999999 4.999e+01 999999999 999.99 !
^ ^ ^      ^         ^         ^         ^         ^         ^         ^ ^
| | |      |         |         |         |         |         |         | |---Locked\n\r"
| | |      |         |         |         |         |         |         |-----Signal Eb/N0\n\r"
| | |      |         |         |         |         |         |         |-----Error/Interval\n\r"
| | |      |         |         |         |         |         |         |-----Bit error rate\n\r"
| | |      |         |         |         |         |         |         |-----Errors between\n\r"
| | |      |         |         |         |         |         |         |observations\n\r"
| | |      |         |         |         |         |         |         |-----Total bit errors\n\r"
| | |      |         |         |         |         |         |         |-----Total bits\n\r"
| | |      |         |         |         |         |         |         |-----Test time\n\r"
| | |      |         |         |         |         |         |         |-----Inverted data\n\r"
| | |      |         |         |         |         |         |         |-----Sync loss\n\r"
| | |      |         |         |         |         |         |         |-----! = Running\n\r"
| | |      |         |         |         |         |         |         |-----* = Gating restart\n\r"
| | |      |         |         |         |         |         |         |-----# = Pattern resync\n\r"

```

## Wide

```

R S I      Time      Bit Count      Error Count      Delta Errors      Error Rate      Error Seconds      Eb/N0      DQM BEP L"
! * *      999:23:59:59.999 9999999999999999 9999999999999999 9999999999999999 4.999e+01 9999999999 999.99 4.999e+01 ! \n\r"
^ ^ ^      ^         ^         ^         ^         ^         ^         ^         ^ ^ ^ \n\r"
| | |      |         |         |         |         |         |         |         | |-----Locked\n\r"
| | |      |         |         |         |         |         |         |         | |-----Average bit error
| | |      |         |         |         |         |         |         |         | |probability \n\r"
| | |      |         |         |         |         |         |         |         | |-----Signal Eb/N0\n\r"
| | |      |         |         |         |         |         |         |         | |-----Error/Interval\n\r"
| | |      |         |         |         |         |         |         |         | |-----Bit error rate\n\r"
| | |      |         |         |         |         |         |         |         | |-----Errors between\n\r"
| | |      |         |         |         |         |         |         |         | |observations\n\r"
| | |      |         |         |         |         |         |         |         | |-----Total bit errors\n\r"
| | |      |         |         |         |         |         |         |         | |-----Total bits\n\r"
| | |      |         |         |         |         |         |         |         | |-----Test time\n\r"
| | |      |         |         |         |         |         |         |         | |-----Inverted data\n\r"
| | |      |         |         |         |         |         |         |         | |-----Sync loss\n\r"
| | |      |         |         |         |         |         |         |         | |-----! = Running\n\r"
| | |      |         |         |         |         |         |         |         | |-----* = Gating restart\n\r"
| | |      |         |         |         |         |         |         |         | |-----# = Pattern resync\n\r"

```

[illegible]

## 1.1 Test Noise Commands

Digitally generated Additive White Gaussian Noise (AWGN) can be injected in the demodulator for test purposes. This noise can exhaust most or all of the demodulator's error-free signal processing margin so that small imperfections in the received signal will be visible as an increase in the bit error rate.

The noise level is calibrated relative to an extremely accurate measurement of the input signal level. This measurement will be most accurate when the received signal has a high signal-to-noise ratio. Therefore, input signal levels above -70 dBm are generally recommended. Due to the available dynamic range of the demodulator signal processing path and the faithful representation of the AWGN, noise samples may be clipped, especially at or below 0 dB Eb/N0.

Note that the noise is injected following downconversion to baseband but before demodulation. Therefore, the noise will affect demodulator output signals, including video outputs, but it will not change measurements of the input signal (signal strength and signal quality).

The AWGN command, described in Table 2, displays and controls Additive White Gaussian Noise settings.

**Table 2: Test Noise Commands**

Mnemonic	Name	Description
AWGN ?	Test Noise Help	Provides help for using the test noise commands
AWGN	Test Noise Status	Report test noise status of current tracking channel Example: AWGN AWGN disabled AWGN power 50.00 dB Eb/N0
AWGN <e>	Enable/Disable Test Noise	Turns the test noise output Enabled or Disabled AWGN <e> <e> - Sets AWGN enable to <e> If 'e' is 0, test noise is Off/Disabled If 'e' is 1, test noise is On/Enabled Example: AWGN 1 AWGN enabled
AWGN [P <p>]	Set Test Noise Level	Sets the test noise power level in dB Eb/N0 Range for p is -10.00 dB Eb/N0 to +50.00 dB Eb/N0 Example: AWGN P 42.00 AWGN power 42.00 dB Eb/N0