Quasonix

EVTM CONVERTER
PINOUT CODE AT

**Connector:**
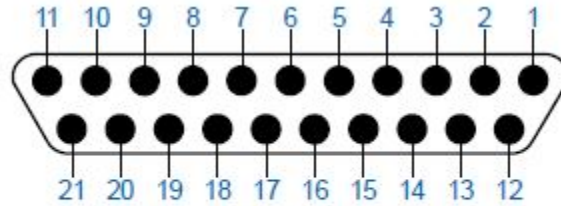
21 socket

Pin	Function
1	TTL Transmit Clock
2	TTL Transmit Data
3	TTL System Clock
4	N/C
5	N/C
6	DC Power In
7	N/C
8	N/C
9	N/C
10	TTL Receive Data
11	TTL Receive Clock
12	TTL Transmit Clock Return
13	TTL Transmit Data Return
14	TTL System Clock Return
15	N/C

Pin	Function
16	DC Power Return
17	DC Power Return
18	N/C
19	N/C
20	TTL Receive Data Return
21	TTL Receive Clock Return

Connector:
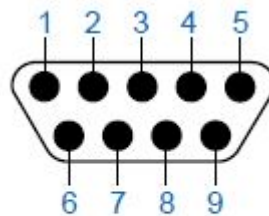
9 pin

Pin	Function
1	Positive leg of a differential pair; Transmit data on Ethernet network Pin 1 on a standard RJ-45 (Tx + on Ethernet/Rx + on MDM-9)
2	N/C
3	N/C
4	N/C
5	Positive leg of a differential pair; Receive data on Ethernet network Pin 3 on a standard RJ-45 (Rx + on Ethernet/Tx + on MDM-9)
6	Negative leg of a differential pair; Transmit data on Ethernet network Pin 2 on a standard RJ-45 (Tx - on Ethernet/Rx - on MDM-9)
7	N/C
8	N/C
9	Negative leg of a differential pair; Receive data on Ethernet network Pin 6 on a standard RJ-45 (Rx - on Ethernet/Tx - on MDM-9)

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DRAWING	qsx_pc_EVTM_AT.doc		
REVISION	-		
DRAWN	MDS	DATE	180731
VERIFIED	MDS	DATE	180731
APPROVED	MDS	DATE	180731